Software Development Guide
T113X core board



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1. Overview

This article mainly introduces the complete process of customizing a complete embedded Linux system based on the Ebyte core board, including the preparation of the development environment, code acquisition, and how to transplant the Boot loader and Kernel, customize the Rootfs suitable for your own application needs, etc. We first introduce how to build a system image suitable for the ECK30-T13IA core board based on the SDK source code we provide, and how to burn the built image to the development board. For those users who develop projects based on the ECK30-T13IA core board, we focus on the methods and key points of transplanting this system to the user's hardware platform, and through some actual transplantation cases and Rootfs customization cases based on Build root, users can quickly customize the system image suitable for their own hardware.

Please note that this document does not include the introduction of build root projects and basic knowledge related to Linux systems. It is suitable for embedded Linux system developers and embedded Linux BSP developers with certain development experience.

2. Development environment preparation

This chapter mainly introduces some software and hardware environments required before development based on the ECK30-T13IA core board, including the necessary hardware configuration, development, and burning environment.

By reading this chapter, you will understand the installation and use of related hardware tools and software development and debugging tools. You can also quickly build a related development environment to prepare for subsequent development and debugging.

Host: i7-12700 + win11 22H2

Virtual Machine: Ubuntu 18.04

2.1. Hardware Preparation

2.1.1. Product Model

Product Model Table

Serial number	Product specifications and models	Model Description
1	ECK30-T13IA2MN2M-I	Allwinner T113-i processor + 256MB memory + 256MB FLASH, domestic industrial-grade core board.

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2	ECK30-T13IA5ME8G-I	Allwinner T113-i processor + 512MB memory + 8GB eMMC, domestic industrial-grade core board.
3	ECK30-T13IA1GE8G-I	Allwinner T113-i processor + 1GB memory + 8GB eMMC, domestic industrial-grade core board.

2.1.2. Debug serial port

Debug serial port: baud rate 115200, data bits 8, stop bit 1, no parity check, no hardware flow control. Please configure the actual serial port yourself. The configuration method is detailed in the following text.

2.1.3. Startup method

According to the user manual of the T113-i chip, T113-i will start in sequence according to the boot pin configuration. The startup method and sequence are detailed in the table below.

Startup sequence table

BOOT_PIN[1:0]	Boot sequence
00	SPI NOR —> SPI NAND
01	SMHC0 —> SPI NOR —> other media
10	SMHC0 —> SPI NAND —> other media
11	SMHC0 —> EMMC2_BOOT —> EMMC2_USR —> other media

Users of our ECK30-T13IA core board do not need to worry about this part, as the relevant products have implemented two corresponding configurations, `10` and `11`, on the board. What users should remember is that, generally speaking, SMHC0 is configured as an SD card function, and once a boot able SD card is inserted, the system will boot from the SD card. If the SD card cannot be started, it will boot from the onboard storage device.

2.2.Virtual machine software deployment

This chapter will introduce how to build the development environment of T113-i. By reading this chapter, you will understand the installation and use of related development and debugging tools. And you can quickly build the relevant development environment to prepare for subsequent development and debugging.

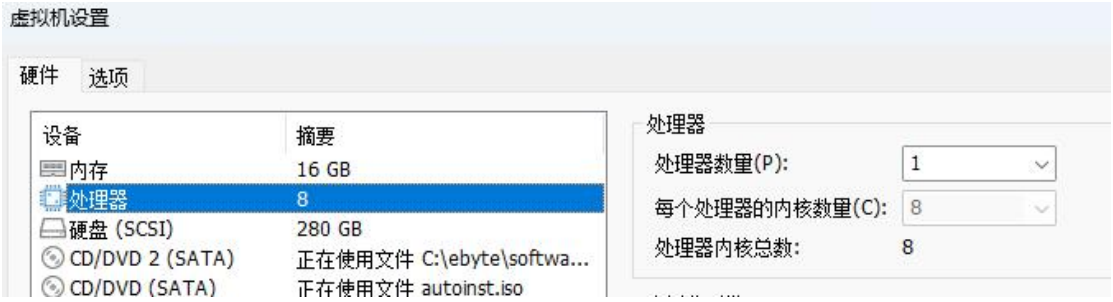
2.2.1. Virtual Machine Configuration

Currently, the more common virtual machine software includes Virtual Box, VM ware, etc. The configuration methods of different platforms are different, and user habits are also different. Therefore, please configure the virtual machine to use bridging to connect to the Internet according to the virtual machine software you use, and turn on the directory sharing function with Windows.

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It is recommended that the virtual machine be set to have a memory size of 16 GB or more and 8 cores, otherwise the compilation will fail.



2.2.2. Configure the compilation environment

Since the T113-i SDK uses build root 201902 version, it is recommended that users use the Ubuntu 18.04 64-bit distribution. For higher versions, you can also try to use build root 202205 version, but still make sure that the version is compatible with the build root version.

Install the software:

```
sudo apt update && sudo apt upgrade -y
sudo apt install -y git make gcc g++ flex bison xz-utils libncurses-dev device-tree-compiler netpbm bc libssl-dev lzop libncursesw5-dev bear gcc-arm-linux-gnueabihf flex bison xz-utils libssl-dev rsync fakeroot libtool texinfo automake unzip
sudo apt install -y libc6:i386 libgcc1:i386 libstdc++6:i386
sudo ln -sf /usr/bin/python3 /usr/bin/python
```

Create a working directory:

```
mkdir ~/work
```

2.2.3. Enable the TFTP service of the virtual machine

In the later debugging stage, TFTP is often used to load the kernel boot instead of burning the storage device every time. Therefore, you must first install and enable the TFTP service in Ubuntu. Use the following command to install the TFTP service:

```
sudo apt install tftpd-hpa -y
```

By default, the files in the /var/lib/tftpboot directory will be shared, and can only be downloaded, not uploaded. If you want to customize the configuration, please refer to the official

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documentation:https://help.ubuntu.com/community/TFTP, this document has modified the path to /srv/tftp.

Add users to the tftp group and modify the shared directory permissions:

```
sudo usermod -a -G tftp $(whoami)
sudo chmod 775 /srv/tftp/
sudo chgrp tftp /srv/tftp/
```

If the preceding operations are not performed, permission issues may occur and files cannot be written to the shared directory without root user permissions.

2.2.4. Enable the NFS service for the virtual machine

In the later debugging stage, you often need to share files with NFS or mount the file system directly through NFS. Therefore, you must first install and enable the NFS service in Ubuntu. Use the following command to install the NFS service:

```
sudo apt install nfs-kernel-server -y
```

Wait for the installation to complete. After the installation is complete, set the directory to be exported. Here, the default directory of tftp is used directly. Users can modify it by themselves, or use a convenient text editor to modify the /ext/exports file. For configuration methods, refer to the man manual:

```
echo "/srv/tftp *(rw,sync,no_subtree_check,no_root_squash,fsid=root)" | sudo tee -a /etc/exports
man exports
```

2.2.5. Enable the SSH service of the virtual machine

After enabling Ubuntu's SSH service, we can use ssh client software to log in to Ubuntu under Windows, such as using MobaXterm, VSCode, and use the following command to enable SSH service under Ubuntu:

```
sudo apt install openssh-server -y
```

The above command installs the ssh service. The ssh configuration file is /etc/ssh/sshd_config. Use the default configuration. For more configuration, refer to the man

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manual related to sshd_config.

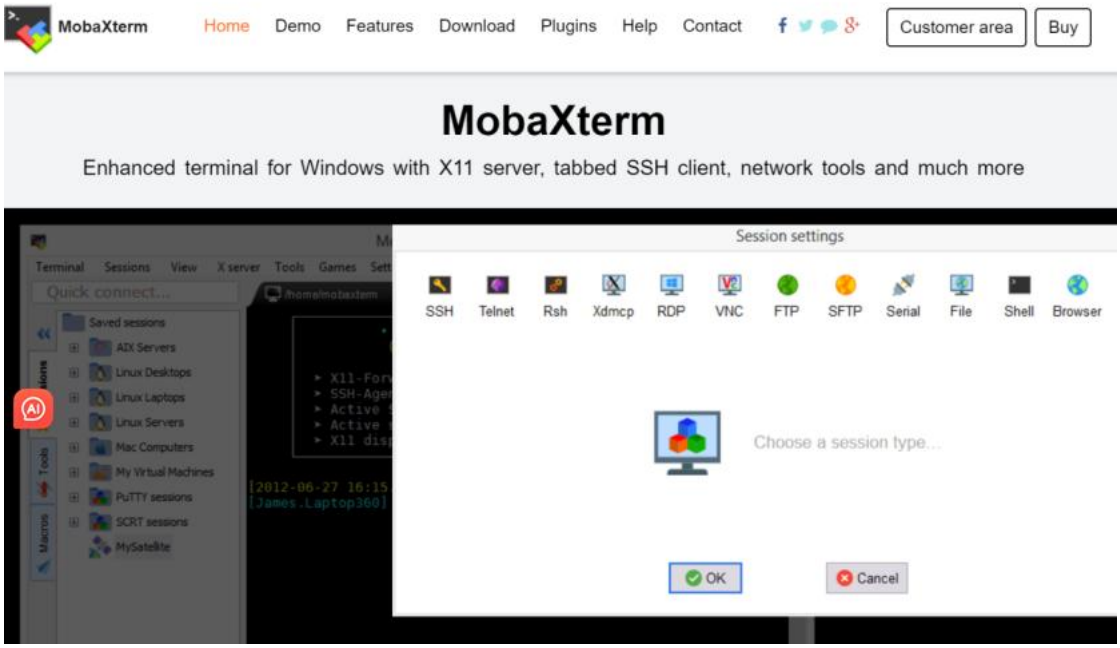
2.3.Host software installation

This chapter will introduce how to build the T113-i programming and debugging environment. By reading this chapter, you will understand the installation and use of related programming and debugging tools.

2.3.1. MobaXterm

2.3.1.1. Software download and installation

MobaXterm is a terminal software with powerful functions and is free. It is recommended to use this software as terminal debugging software. MobaXterm software can be downloaded from its official website athttps://mobaxterm.mobatek.net/, as shown in the figure:

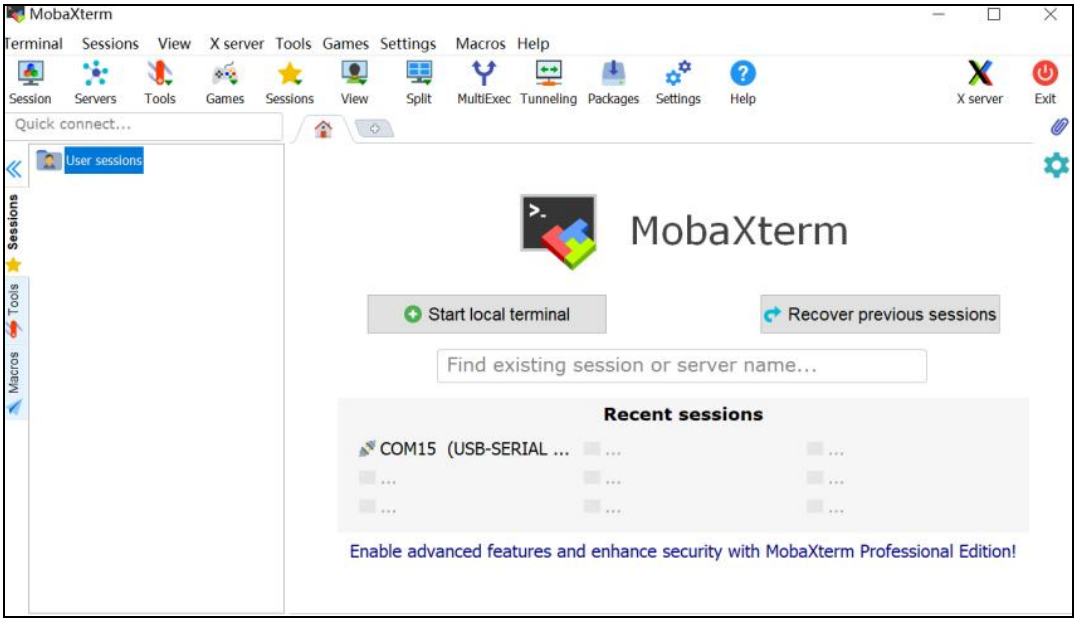


2.3.1.2. Software Usage

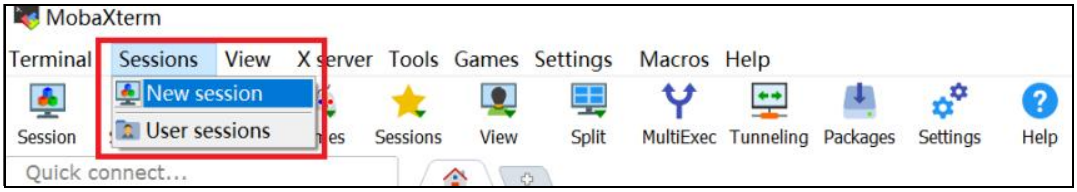
The software interface is shown in the figure:

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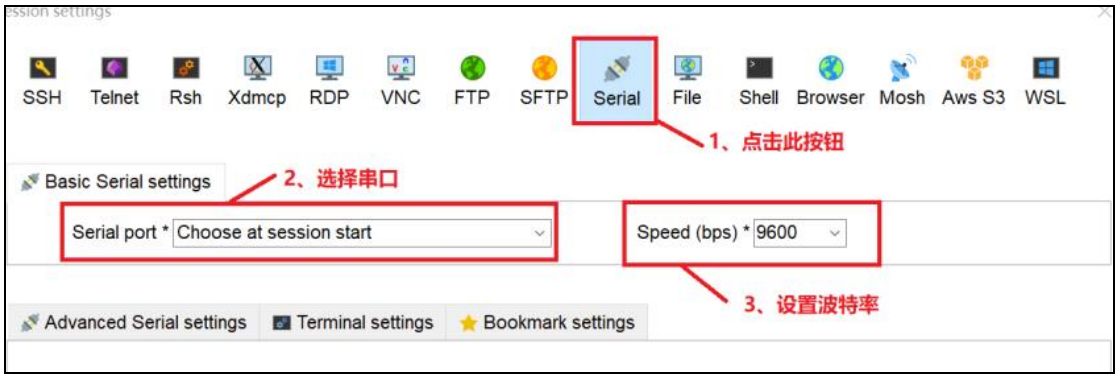
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Click the "Sessions->New session" button in the menu bar to open the new session window, as shown in the figure



Establish a Serial connection, that is, a serial port connection, because the main purpose of using MobaXterm is to use it as a serial port terminal. Click the "Serial" button in the figure to open the serial port setting interface, as shown in the figure:



After opening the serial port setting window, first select the serial port number to be set. Therefore, you must first connect the development board to the computer with a serial port cable. Since the default serial port baud rate used by T113-i is 115200, you need to set the baud rate to 115200.

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2.3.2. PhoenixSuit

This tool is officially provided by Allwinner to burn the system from USB. The software has been stored in the directory:“04_Tools/PhoenixSuit”. For detailed usage, please refer to the section “Official Tool Burning” below.

2.3.3. PhoenixCard

This is a tool officially provided by Allwinner for making system cards. You can choose to burn and make mass production cards and startup cards. The software has been stored in the directory: “04_Tools/PhoenixCard”. For detailed usage, please refer to the section “Making an SD Card Startup Card” below.

2.4.Data Transfer

In the previous chapters, we have configured various network services for the virtual machine. Now we just need to understand how to use them on the client and development board.

2.4.1. TFTP Client

In general, the development board only needs to use the download function. In this case, you can simply use the curl tool to download. Before using it, you need to select the curl function in buildroot, and then use curl to download the file (only for Linux version curl, Windows can choose to use filezilla):

```
curl -o /path/dist_file tftp://HOST/src_file
```

1. /path/dist_file: Where to download, the name and path of the output file.

2. HOST: The IP address of the virtual machine. You need to pay attention to whether this address is accessible.

3. src_file: file directory and name. Note that you do not need to add the "TFTP_DIRECTORY" part of the configuration. For example, if the file is stored in /srv/tftp/aaa/bbb/ccc, the src_file value is "aaa/bbb/ccc".

2.4.2. NFS Client

In general, the development board only needs to use the mount command to mount the NFS file system, but it is necessary to install basic support and then mount NFS:

```
mount -t nfs HOST:/path /mnt_point
```

1. HOST: The IP address of the virtual machine. You need to pay attention to whether this

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address is accessible.

2. path: The path on the NFS server to be mounted, such as /srv/tftp/aaa/bbb.

3. mnt_point: Where you want to mount the NFS file system, such as /mnt.

2.4.3. SSH

Generally speaking, we can also use the SFTP and SCP functions that come with SSH to download files, but there are not many cases of directly using commands to upload and download. More third-party tools are used, such as the SFTP function after using MobaXterm to log in to ssh, or VSCode using the remote-SSH plug-in to implement the download function during the development process. The way to use it is to right-click the mouse on the file to be downloaded, and then select Download.

2.4.4. USB flash drive/SD card

For SD cards, you need to use a card reader, and the actual usage is the same as that of a USB flash drive.

2.4.4.1. Copying data from a virtual machine

Users need to transfer the USB flash drive/SD card to the virtual machine according to the virtual machine software they are using. Generally, it will be mounted automatically and can be directly accessed in the file manager. After copying the data, uninstall the device in the file manager and then remove the device from the virtual machine.

If it is not mounted automatically, you can use the following two commands to mount it manually:

1. Method 1

```
udisksctl mount --block-device /dev/sdXY
```

2. Method 2

```
sudo mount /dev/sdXY /mnt_point
```

In this case, you need to have some understanding of the USB disk/SD card partitions before you can specify the Y value in sdXY. The X value is usually 'b', but you also need to judge the situation yourself based on the information returned by lsblk.

Use method 1 to mount. You can access it without root privileges and without specifying a

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path, but you need to have the `udisks2` package installed in your system.

Manual uninstallation corresponds to the following command:

1. Method 1

```
udisksctl unmount --block-device /dev/sdXY
udisksctl power-off --block-device /dev/sdXY
```

2. Method 2

```
sudo umount /mnt_point
```

2.4.4.2. Copy data to the development board

For development boards, they are generally streamlined and may not have a udisks2 package or a GUI interface. Even if there is an interface, it may not support automatic mounting. However, users often have root permissions, so mount operations are generally performed from the command line.

1. Mount

```
mount /dev/sdXY /mnt_point
```

2. copy

```
cp /mnt_point/file /out_path/out_file
cp -ra /mnt_point/dir /out_path/out_dir
```

3. Unmount

```
umount /mnt_point
```

3. SDK Compilation and Build

This section describes the complete process from obtaining the SDK to compiling and generating a usable image.

3.1. SDK Acquisition

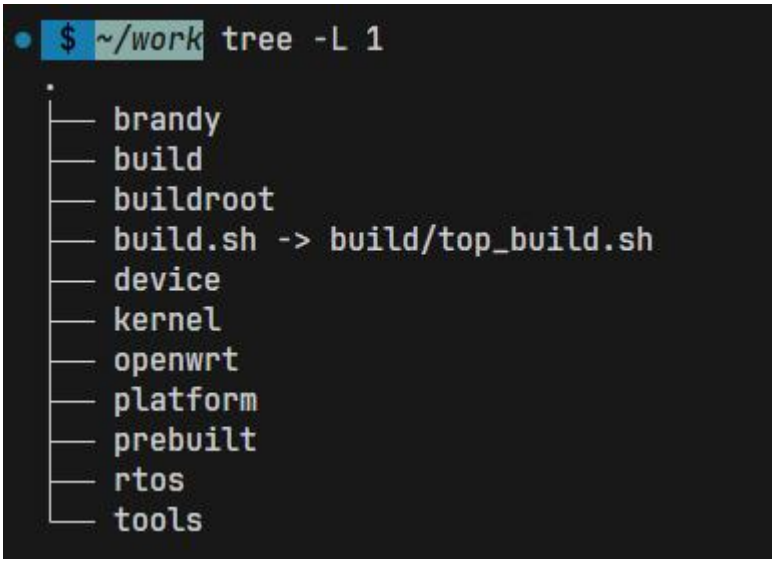
The T113-I development SDK is included in the download file. Please unzip it to the development directory.

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3.2. SDK Directory

After decompression, the entire SDK directory is as follows:



We need to pay attention to the following directories:

Top-level directory	Subdirectories	effect
Brandy	brandy-2.0/spl-pub/	SPL source code is generally not modified, and most of the official files provided by Allwinner are binary files
	brandy-2.0/u-boot-2018/	U-boot source code
build		Compile script directory, generally not modified
buildroot	buildroot-201902	File system source code
	buildroot-202205	
	package/auto/sdk_demo/	Sample Program
device	config/chips/t113_i	The chip configuration directory supported by the SDK, including configuration solutions for multiple development boards
	product	By default, no, after executing SDK configuration, point to the target chip, here is T113_i
kernel	linux-5.4	Kernel source code
openwrt	target/t113_i/	openwrt solution configuration
	openwrt/	openwrt source code
platform		Platform extension related source code, such as lvgl, qt, etc.
prebuilt		Kernel, buildroot and openwrt compilation tools
rtos		rtos source code, mainly related to heterogeneous cores and DSP
tools	tools_win	Some Windows tools

3.3. SDK Configuration

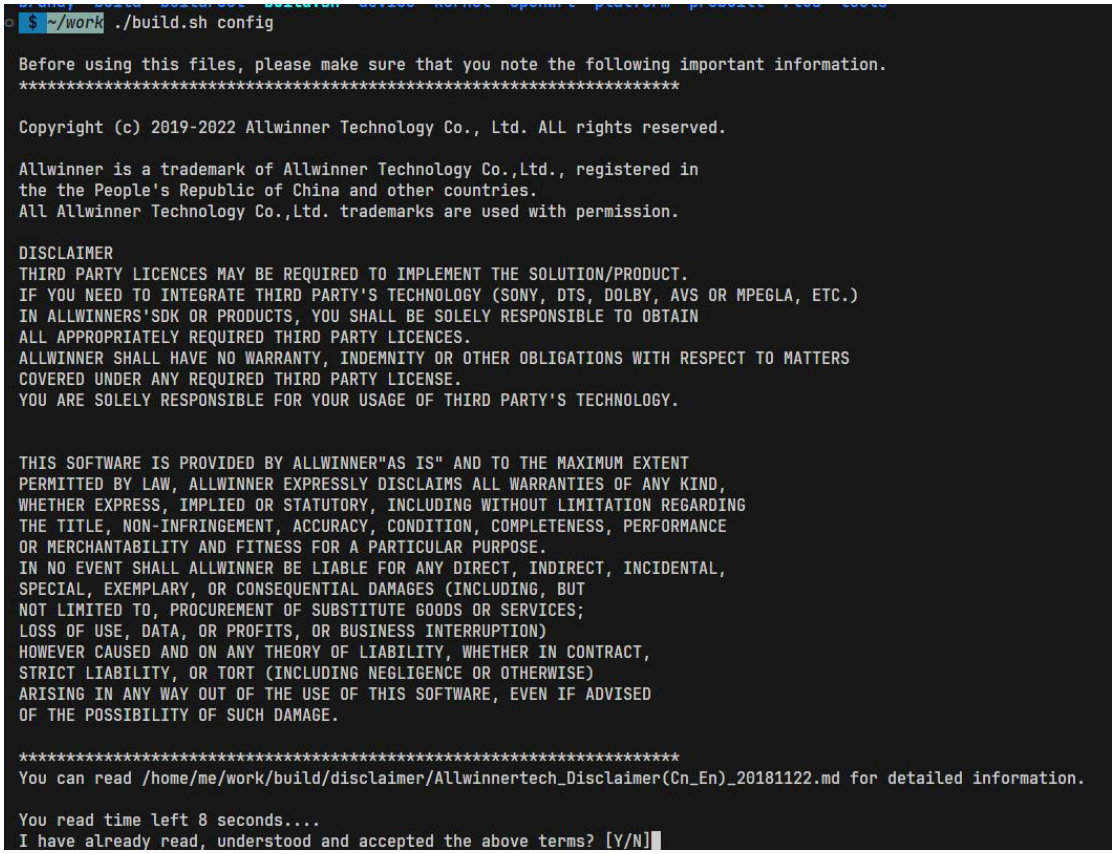
Since the SDK supports multiple chips and multiple solutions, users are required to select the configuration that matches their own development board before officially starting to compile. The SDK also provides the following configuration methods:

```
./build.sh config
```

After the execution is completed, you need to agree to some licensing certifications of Allwinner:

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If it is the T113-i of ECB30, configure it as shown in Figure 1. If it is the T113-S3 of ECB31 from EBIT, configure it as shown in Figure 2. If it is the T113-S4 of ECB31 from Ebit, configure it as shown in Figure 3. If it is ECB31-PB2E8-I, the T113S4 coreless board is deployed. Then the configuration is carried out in the way shown in Figure 4. ECB31-PB1E8-I selects the S3 board configuration.

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```
hu@ubuntu:~/linux/allwinner$ ./build.sh config
========ACTION List: mk_config ;========
options :
All available platform:
   0. android
   1. linux
Choice [linux]:
All available linux_dev:
   0. bsp
   1. buildroot
   2. openwrt
   3. ubuntu
Choice [buildroot]:
All available ic:
   0. t113
   1. t113_i
   2. t113_s3p
   3. t113_s4
   4. t113_s4p
   5. t113s2
Choice [t113_i]:
All available board:
   0. ebyte_t113i
   1. ebyte_t113i_BTB
   2. ebyte_t113i_nand
   3. evb1
   4. evb1_auto
   5. evb1_auto_buildroot2022_RT
   6. evb1_auto_buildroot2022_test
   7. evb1_auto_nand
   8. evb1_auto_nor
Choice [ebyte_t113i]:
All available flash:
   0. default
   1. nor
Choice [default]:
```

```
hu@ubuntu:~/linux/allwinner$ ./build.sh config
========ACTION List: mk_config ;========
options :
All available platform:
   0. android
   1. linux
Choice [linux]:
All available linux_dev:
   0. bsp
   1. buildroot
   2. openwrt
   3. ubuntu
Choice [buildroot]:
All available ic:
   0. t113
   1. t113_i
   2. t113_s3p
   3. t113_s4
   4. t113_s4p
   5. t113s2
Choice [t113]:
All available board:
   0. dev
   1. ebyte_t113
   2. ebyte_t113_nand
   3. evb1
   4. evb1_auto
   5. evb1_auto_nand
   6. evb1_auto_nand_fastboot
   7. evb1_auto_nor
   8. pro
Choice [ebyte_t113]:
All available flash:
   0. default
```

Figure 1 (Top left), Figure 2 (Top right), Figure 3 (bottom left) , Figure 4 (bottom right)

```
hu@ubuntu:~/linux/allwinner$ ./build.sh config
========ACTION List: mk_config ;========
options :
All available platform:
   0. android
   1. linux
Choice [linux]:
All available linux_dev:
   0. bsp
   1. buildroot
   2. openwrt
   3. ubuntu
Choice [buildroot]:
All available ic:
   0. t113
   1. t113_i
   2. t113_s3p
   3. t113_s4
   4. t113_s4p
   5. t113s2
Choice [t113_s4]:
All available board:
   0. ebyte_t113s4
   1. ebyte_t113s4_nand
   2. evb1_auto
   3. evb1_auto_fastboot_video
   4. evb1_auto_nand
   5. evb1_auto_nand_fastboot_video
Choice [ebyte_t113s4]:
All available flash:
   0. default
   1. nor
Choice [default]:
All available kern_name:
   0. linux-5.4
Choice [linux-5.4]:
```

```
hu@a709fd609e21:/t113/allwinner$ ./build.sh config
========ACTION List: mk_config ;========
options :
All available platform:
   0. android
   1. linux
Choice [linux]:
All available linux_dev:
   0. bsp
   1. buildroot
   2. openwrt
   3. ubuntu
Choice [buildroot]:
All available ic:
   0. t113
   1. t113_i
   2. t113_s3p
   3. t113_s4
   4. t113_s4p
   5. t113s2
Choice [t113_s4]:
All available board:
   0. ebyte_t113s4
   1. ebyte_t113s4_PI
   2. ebyte_t113s4_nand
   3. evb1_auto
   4. evb1_auto_fastboot_video
   5. evb1_auto_nand
   6. evb1_auto_nand_fastboot_video
Choice [ebyte_t113s4_PI]:
All available flash:
   0. default
   1. nor
Choice [default]:
All available kern_name:
   0. linux-5.4
Choice [linux-5.4]:
```

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Here to ECB30 T113I as an example, the use of the compiler configuration for ` SDK/device/config/one/t113_i/configs/ebyte_t113i/buildroot/BoardConfig. Mk `. After the configuration is completed, the final compilation rule configuration can be viewed in the '.buildconfig 'file under the SDK directory. The following are some of the more important parts listed:

```
LICHEE_PLATFORM //Compile platform
LICHEE_LINUX_DEV //Compiled solution
LICHEE_IC // Compiled IC
LICHEE_BOARD //Compiled board configuration
LICHEE_FLASH //Compiled flash configuration
LICHEE_CHIP //Compiled chip code name
LICHEE_KERN_VER //Compiled kernel version
LICHEE_KERN_DEFCONF //Compile kernel default configuration
LICHEE_BUILDING_SYSTEM //Compiled construction system
LICHEE_BR_VER //buildroot version
LICHEE_BR_DEFCONF //Default configuration of buildroot
LICHEE_BR_RAMFS_CONF //buildroot's ramfs default configuration
LICHEE_BRANDY_VER //uboot version
LICHEE_BRANDY_DEFCONF //Default configuration of brandy
LICHEE_CROSS_COMPILER //Cross-compilation chain used for compilation
LICHEE_CHIP_CONFIG_DIR //Compile system IC configuration directory
LICHEE_OUT_DIR //Compiled output directory
```

3.4. Compile SDK

Wait for the SDK to finish configuration, then start compiling:

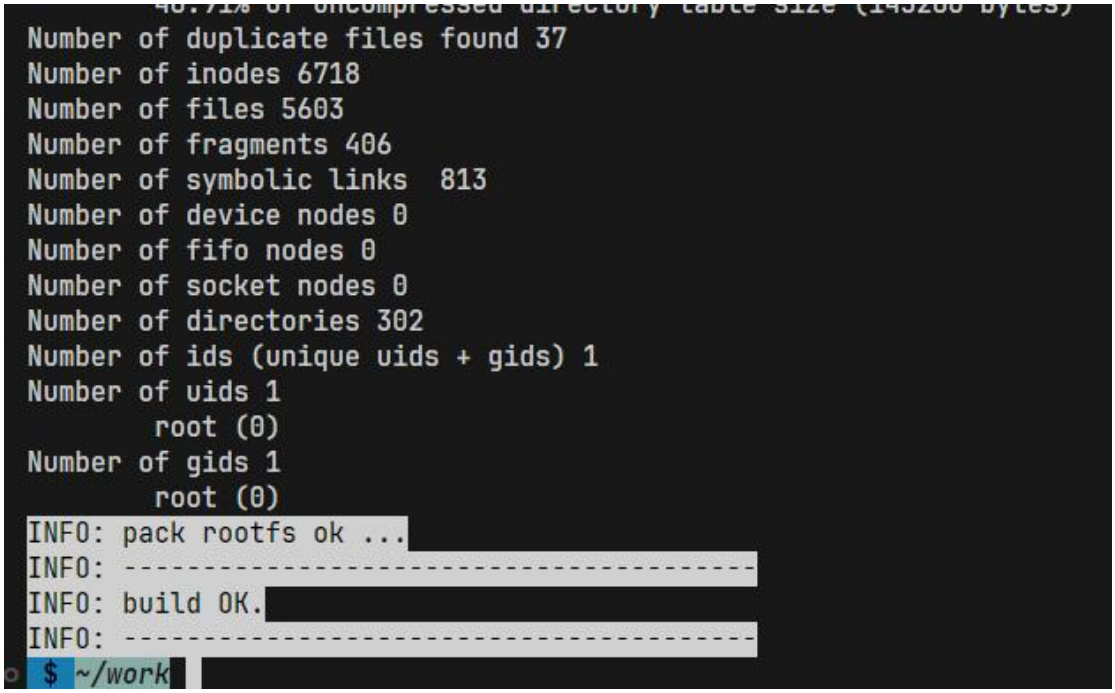
```
./build.sh
```

It is estimated that it will take about half an hour to compile and generate SPL, U-Boot, Linux kernel and Buildroot file system image files. After the compilation is completed, the

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following output will be displayed:



After the compilation is completed, the uboot, kernel and file system images are generated in the <SDK>/out directory. Then, in section 3.7, the packaging command is executed to generate the burning image.

3.5. Separate compilation

3.5.1. Compile u-boot separately

You can compile u-boot separately by using the following command, which can save compilation time when you only need to modify the u-boot stage:

```
./build.sh bootloader
```

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```
$ ~/work ./build.sh bootloader
========ACTION List: build_bootloader ;========
options :
find: '/home/me/work/brandy/brandy-2.0/spl': No such file or directory
find: '/home/me/work/brandy/brandy-2.0/u-boot-bsp': No such file or directory
find: '/home/me/work/brandy/dramlib': No such file or directory
INFO: build_bootloader: brandy_path=/home/me/work/brandy/brandy-2.0
INFO: uboot-board.dts updated.
build_option:-p sun8iw20p1_auto_t113_i -b t113_i
grep: /home/me/work/brandy/brandy-2.0/spl/Makefile: No such file or directory
_MAKE PATH: /home/me/work/brandy/brandy-2.0/tools/make_dir/make4.1/bin/make
Prepare toolchain ...
uboot version:u-boot-2018
build for sun8iw20p1_auto_t113_i_defconfig ...
cat: .tmp_config_from_defconfig.o.md5sum: No such file or directory
md5sum: .config: No such file or directory
  CLEAN   scripts/basic
  HOSTCC  scripts/basic/fixdep
  HOSTCC  scripts/kconfig/conf.o
  YACC    scripts/kconfig/zconf.tab.c
  LEX     scripts/kconfig/zconf.lex.c
  HOSTCC  scripts/kconfig/zconf.tab.o
  HOSTLD  scripts/kconfig/conf
board/sunxi/Kconfig:326:warning: defaults for choice values not supported
#
# configuration written to .config
#
```

The following log is output after compilation:

```
bootaddr is 0x20480
/home/me/work/brandy/brandy-2.0/spl-pub/../tools/toolchain/gcc-linaro-7.2.1-2017.11-x86_64_arm
ot.a /home/me/work/brandy/brandy-2.0/spl-pub/sboot/main/sboot_main.o  -L /home/me/work/brandy/
7.2.1 -lgcc  --gc-sections --gc-sections -Tsboot.lds -o sboot.elf -Map sboot.map
/home/me/work/brandy/brandy-2.0/spl-pub/../tools/toolchain/gcc-linaro-7.2.1-2017.11-x86_64_arm
'sboot_sun8iw20p1.bin' -> '/home/me/work/brandy/brandy-2.0/spl-pub/out/sun8iw20p1/bin/sboot_su
'sboot_sun8iw20p1.bin' -> '/home/me/work/device/config/chips/t113_i/bin/sboot_sun8iw20p1.bin'
'sboot_sun8iw20p1.bin' -> '/home/me/work/out/t113_i/evb1_auto/buildroot/sboot_sun8iw20p1.bin'
INFO: build brandy OK.
```

3.5.2. Compile the kernel separately

The kernel can be compiled separately by the following command, which can save compilation time when only the kernel stage needs to be modified:

```
./build.sh kernel
```

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```
$ ~/work ./build.sh kernel
========ACTION List: build_kernel ;========
options :
INFO: build kernel ...
INFO: prepare_buildserver
INFO: Prepare toolchain ...

update .config with /home/me/work/device/config/chips/t113_i/configs/evb1_auto/linux-5.4/config-5.4 ...

make[1]: Entering directory '/home/me/work/out/t113_i/kernel/build'
  GEN     Makefile
*** Default configuration is based on '../../../../../device/config/chips/t113_i/configs/evb1_auto/linux-5.4/config-5.4'
#
# No change to .config
#
make[1]: Leaving directory '/home/me/work/out/t113_i/kernel/build'
Building kernel
make[1]: Entering directory '/home/me/work/out/t113_i/kernel/build'
  GEN     Makefile
  DTC     arch/arm/boot/dts/board.dtb
  CALL    /home/me/work/kernel/linux-5.4/scripts/atomic/check-atomics.sh
  CALL    /home/me/work/kernel/linux-5.4/scripts/checksyscalls.sh
  CHK     include/generated/compile.h
  Kernel: arch/arm/boot/Image is ready
  Building modules, stage 2.
  MODPOST 29 modules
```

After compilation, the following log is output:

```
sun8iw20p1 compile all(Kernel+modules+boot.img) successful


INFO: build dts ...
INFO: Prepare toolchain ...
removed '/home/me/work/out/t113_i/evb1_auto/buildroot/.board.dtb.d.dtc.tmp'
removed '/home/me/work/out/t113_i/evb1_auto/buildroot/.board.dtb.dts.tmp'
'/home/me/work/out/t113_i/kernel/build/arch/arm/boot/dts/.board.dtb.d.dtc.tmp' -> '/home/me/work/out/t113_i/evb1
'/home/me/work/out/t113_i/kernel/build/arch/arm/boot/dts/.board.dtb.dts.tmp' -> '/home/me/work/out/t113_i/evb1_a
'/home/me/work/out/t113_i/kernel/staging/sunxi.dtb' -> '/home/me/work/out/t113_i/evb1_auto/buildroot/sunxi.dtb'
```

3.5.3. Compile buildroot separately

You can compile buildroot separately with the following command, which can save compilation time when you only need to modify the buildroot stage:

```
./build.sh buildroot_rootfs
```

```
$ ~/work ./build.sh buildroot_rootfs
========ACTION List: build_buildroot_rootfs ;========
options :
INFO: build buildroot ...
make: Entering directory '/home/me/work/buildroot/buildroot-201902'
>>>   Finalizing target directory
# Check files that are touched by more than one package
./support/scripts/check-uniq-files -t target /home/me/work/out/t113_i/evb1_auto/buildroot/buildroot/build/packages-file-list.txt
Warning: target file "./etc/fw_env.config" is touched by more than one package: ['busybox-init-base-files', 'uboot-tools']
Warning: target file "./usr/lib/libsubdecoder.so" is touched by more than one package: ['libcedarc', 'libcedarx']
Warning: target file "./usr/share/alsa/cards/aliases.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/AACI.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/ATIIXP.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/ATIIXP-SPDMA.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/ATIIXP-MODEM.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/AU8810.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/AU8820.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/AU8830.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/Audigy.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
Warning: target file "./usr/share/alsa/cards/Audigy2.conf" is touched by more than one package: ['alsa-lib', 'alsa-utils']
```

After compilation, the following log is output:

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```
# @echo -e '\e[1;33m cp -f mem_test /home/me/work/buildroot/package/auto/sdk_demo/bin \e[0m'
make finish!!!
make[1]: Leaving directory '/home/me/work/buildroot/package/auto/sdk_demo/mem_test'
make: Leaving directory '/home/me/work/buildroot/package/auto/sdk_demo'
build auto finish
INFO: copy the config files form device ...
make: Entering directory '/home/me/work/platform'
Makefile:35: "----------1--------"
Makefile:36: /home/me/work/platform
make: Nothing to be done for 'INSTALL_FILES'.
make: Leaving directory '/home/me/work/platform'
INFO: build buildroot OK.
```

3.6. Compilation Output

After the compilation is complete, you will find an out directory in the SDK directory, which stores all the compiled output of the SDK.

```
$ ~/work ls
brandy  build  buildroot  build.sh  device  kernel  openwrt  out  platform  prebuilt  rtos  tools
$ ~/work ls out/
kernel  serversocket  t113_i  toolchain
$ ~/work
```

The kernel directory is a soft link to the t113_i/kernel directory, which is the complete compiled output of the kernel. The toolchain directory is the cross-compilation toolchain used in the compilation process. We mainly look at the t113_i directory:

```
hu@hu:~/linux/t113/allwinner/out/t113_i$ tree -L 2
.
├── ebyte_t113i
│   ├── buildroot
│   ├── pack_out
│   └── ubuntu
├── ebyte_t113i_nand
│   ├── buildroot
│   └── pack_out
└── kernel
    ├── build
    └── staging
```

Enter the <SDK>/out/t113_i/ebyte_t113i/buildroot directory:

```
hu@hu:~/linux/t113/allwinner/out/t113_i/ebyte_t113i/buildroot$ ls
arisc                              boot0_nandfastboot_sun8iw20p1.bin  buildroot          ramfs.cpio.gz        sunxi.dtb                          vmlinux
bImage                             boot0_nand_sun8iw20p1.bin          dist               rootfs.ext4          System.map                         vmlinux.tar.bz2
boot0_mmc_car_fastboot_sun8iw20p1.bin  boot0_sdcard_sun8iw20p1.bin    dtc                rootfs.squashfs      t113_i_linux_ebyte_t113i_uart0.img  zImage
boot0_mmcfastboot_sun8iw20p1.bin   boot0_spinor_sun8iw20p1.bin         fes1_sun8iw20p1.bin  rootfs.ubifs         u-boot-sun8iw20p1.bin
boot0_nand_car_fastboot_sun8iw20p1.bin  boot.img                      lib                sboot_sun8iw20p1.bin  uImage
```

We mainly need to focus on the following files:

- boot0_*_sun8iw20p1.bin: SPL image, used to boot u-boot
- u-boot-sun8iw20p1.bin: u-boot image, used to boot the kernel
- boot.img: kernel image, including kernel, device tree, Android image format
- sunxi.dtb: The compiled kernel device tree file will eventually be packaged into

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boot.img.

- ❖ zImage: The compiled kernel file will eventually be packaged into boot.img.
- ❖ rootfs.*: The root file system generated by buildroot. Usually the one with the suffix ext4 is used for eMMC/SD card, and the one with the suffix ubifs is used for NAND.

3.7. Image packaging

After the previous compilation, various compiled files are output, but most of them cannot be used directly and need to be packaged into a final usable image:

```
./build.sh pack
```

After waiting for the packaging to be completed, check the out directory and you will find that there is an additional pack_out directory and an img file. The img file is the image file that needs to be burned to the development board in the end, and the pack_out directory is the intermediate file for packaging the image, some of which can also be used in the end.

Let's look at the pack_out directory again:

```
$ ~/work/out ls -l
total 266652
lrwxrwxrwx 1 me me        13 Sep 25 17:47 kernel -> t113_i/kernel
lrwxrwxrwx 1 me me        25 Sep 25 17:48 pack_out -> t113_i/evb1_auto/pack_out
srwxrw-rw- 1 me me         0 Sep 25 16:41 serversocket
drwxrwxr-x 4 me me      4096 Sep 25 16:41 t113_i
-rwxrwxr-x 2 me me 273043456 Sep 25 17:48 t113_i_linux_evb1_auto_uart0.img
drwxrwxr-x 3 me me      4096 Sep 25 16:40 toolchain
```

You can find that it is a soft link from t113_i/<board>/pack_out:

```
$ ~/work/out/t113_i/evb1_auto/pack_out ls
amp_rv0.fex                       boot-resource          kernel.fex             sys_partition_ab.fex
arisc.fex                         boot-resource.fex      new_fdt.dtb            sys_partition.bin
aultls32.fex                      boot-resource.ini      optee.fex              sys_partition_dump.fex
aultools.fex                      cardscript.fex         parameter.fex          sys_partition.fex
battery_charge.bmp                cardscript_secure.fex  postinstall_A.sh       sys_partition_nor.fex
battery_charge.bmp.lzma           cardtool.fex           postinstall_B.sh       sys_partition_private.fex
bempty.bmp                        cnf_base.cnf           preinstall_A.sh        sys_partition-recovery.fex
bempty.bmp.lzma                   config.fex             preinstall_B.sh        sysrecovery.fex
board_config.bin                  default.awlic          rdiff                  temp_ubootnodtb.bin
BoardConfig-conditions.mk         diskfs.fex             rootfs.fex             toc0.fex
board_config.fex                  dlinfo.fex             rootfs_nor.fex         toc0_ft.fex
BoardConfig.mk                    dragon_toc.cfg         rootfs-ubifs.fex       toc0_nand.fex
BoardConfig_nor.mk                env_ab.cfg             sboot.bin              toc0_sdcard.fex
board.fex                         env_burn.cfg           split_xxxx.fex         toc0_ufs.fex
boot0_nandfastboot_sun8iw20p1.fex env.cfg                sunxi.fex              toc1.fex
boot0_nand.fex                    env_dragon.cfg         sunxi_gpt.fex          u-boot-crash.fex
boot0_sdcard.fex                  env.fex                sunxi_mbr.fex          u-boot.fex
boot0_spinor.fex                  env_nor.cfg            sunxi_version.fex      usbtool_crash.fex
boot.fex                          env-recovery.cfg       sw-description-ab      usbtool.fex
bootlogo.bmp                      esm.fex                sw-description-ab-rdiff usbtool_test.fex
bootlogo.bmp.lzma                 fes1.fex               sw-description-recovery Vboot.fex
bootlogo.bmp.lzma.head            fit-image.its          sw-subimgs-ab.cfg      Vboot-resource.fex
bootlogo.fex                      image.cfg              sw-subimgs-ab-rdiff.cfg Venv.fex
boot_package.cfg                  image_crashdump.cfg    sw-subimgs-recovery.cfg version_base.mk
boot_package.fex                  image_linux.cfg        sys_config.bin         vmlinux.fex
boot_package_nor.cfg              image_nor.cfg          sys_config.fex         Vrootfs.fex
$ ~/work/out/t113_i/evb1_auto/pack_out
```

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There is a lot of content, we mainly focus on the following files:

- boot_package.fex: packaged u-boot image, including contents, see boot_package.cfg
- boot-resource.fex: Resources used during the startup process, such as logo images.
- env.fex: u-boot environment variable configuration, copy of SDK/device/product/configs/<board>/[buildroot]/env.cfg.
- boot.fex: soft link of the kernel image boot.img compiled above.
- rootfs*.fex: The soft link of the buildroot root file system compiled above.

3.7.1. Possible problems

The following situations may occur during the packaging process:

3.7.1.1. unable to open file amp_dsp0.fex

```
partitation file Path=/home/me/work/out/t113_i/evb1_auto/pack_out/sys_partition.bin
mbr_name file Path=/home/me/work/out/t113_i/evb1_auto/pack_out/sunxi_mbr.fex
download_name file Path=/home/me/work/out/t113_i/evb1_auto/pack_out/dlinfo.fex

mbr size = 16384
mbr magic softw411
disk name=boot-resource
disk name=env
disk name=env-redund
disk name=boot
disk name=rootfs
disk name=dsp0
ERROR:  unable to open file amp_dsp0.fex
update_for_part_info -1
ERROR: update mbr file fail
ERROR: update_mbr failed
```

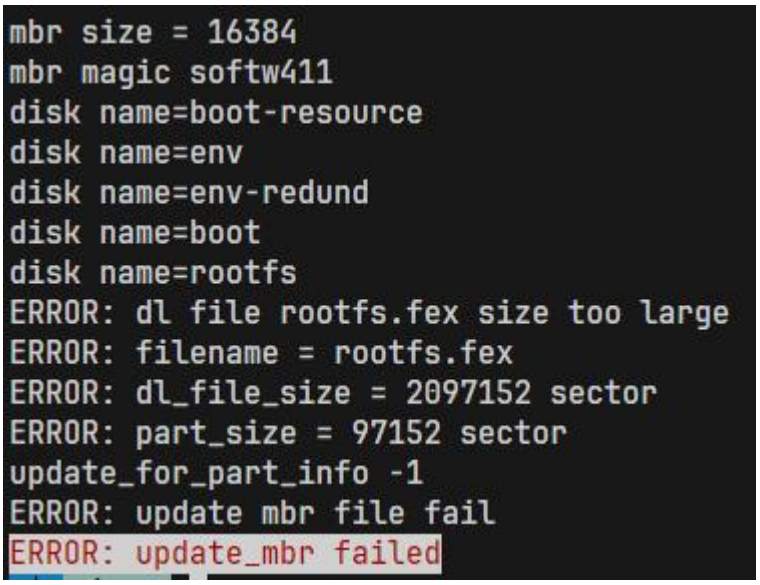
If the user does not need dsp, you can comment in SDK/device/product/configs/<board>/[buildroot]/sys_partition.fex as follows.

```
[partition]
    name         = dsp0
    size         = 2048
;    downloadfile = "amp_dsp0.fex"
    user_type    = 0x8000
```

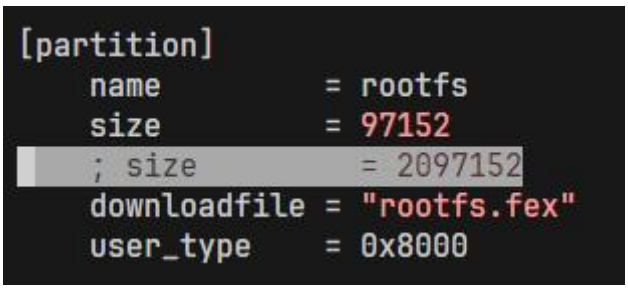
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3.7.1.2. dl file XXXX size too large



If this happens, it means that the partition size configured by the user does not match the final output file. You can find the partition corresponding to the filename in the figure in `SDK/device/product/configs/<board>/[buildroot]/sys_partition.fex, and then modify the partition size to adapt to the corresponding file. The minimum partition size is described as dl_file_size in the figure above:



For users of nand + ubi solution, please also note that size here should be an integer multiple of 504, and the actual effective size of the ubi partition is size*252K.

4. Development board adaptation

In order to adapt to the user's new hardware platform, the user needs to have a certain understanding of the CPU chip manual and the CPU chip user guide, and also needs to have a certain understanding of the SDK modification content. From the description of the SDK directory above, we can know that the development board/solution configuration is stored in SDK/device/product/configs/:

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```
$ ~/work/device/product/configs ls
default  evb1  evb1_auto  evb1_auto_buildroot2022_RT  evb1_auto_buildroot2022_test  evb1_auto_nand  evb1_auto_nor
$ ~/work/device/product/configs tree -L 1
.
├── default
├── evb1
├── evb1_auto
├── evb1_auto_buildroot2022_RT
├── evb1_auto_buildroot2022_test
├── evb1_auto_nand
└── evb1_auto_nor

7 directories, 0 files
$ ~/work/device/product/configs
```

In this level of directory, except for the default directory, all other directories can be considered to be different development board/solution configurations. The contents in the default directory are used as default configuration items when the required items are not found in the corresponding board-level configuration. Generally, they do not need to be modified and should not be modified.

Still taking evb1_auto as an example:

```
$ ~/work ls device/product/configs/evb1_auto
bin  BoardConfig.mk  board.dts  bsp  buildroot  env.cfg  linux-5.4  openwrt  sys_config.fex  sys_partition.fex  uboot-board.dts
$ ~/work tree -L 1 device/product/configs/evb1_auto
device/product/configs/evb1_auto
├── bin
├── BoardConfig.mk
├── board.dts -> linux-5.4/board.dts
├── bsp
├── buildroot
├── env.cfg
├── linux-5.4
├── openwrt
├── sys_config.fex
├── sys_partition.fex
└── uboot-board.dts

5 directories, 6 files
$ ~/work
```

Here are the files we need to pay attention to:

- BoardConfig.mk: The compilation option configuration required by the SDK.
- board.dts: Soft link of linux-5.4/board.dts, used for kernel device tree file.
- buildroot: Detailed configuration used by buildroot to build the root file system.
- linux-5.4: kernel-related configuration.
- sys_config.fex: system parameter configuration. The old version of the kernel plays the role of device tree. However, in Linux 5.4 and later versions, only a few modules (such as ddr, serial port debug level) configuration functions are retained, and they are mostly used in SPL and u-boot stages.
- uboot-board.dts: Device tree file at the u-boot stage.
- sys_partition.fex: mirror partition configuration.
- env.cfg: environment variables in the u-boot stage.

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We can find that some files with the same name in the upper directory also exist in the buildroot directory. This is an overwriting relationship. The overwriting relationship is as follows:

1. bsp, buildroot, openwrt and other system solutions are parallel directories and will not overwrite each other.

2. The BoardConfig.mk, env.cfg, and sys_partition.fex of bsp, buildroot, and other system solutions will overwrite the BoardConfig.mk, env.cfg, and sys_partition.fex of the default directory.

3. In the same level directory, BoardConfig_nor.mk, env_nor.cfg, and sys_partition_nor.fex will overwrite BoardConfig.mk, env.cfg, and sys_partition.fex. Note that this only happens when the nor solution is selected in the SDK configuration.

4. When the bsp, buildroot and other system solutions do not configure BoardConfig.mk, env.cfg, and sys_partition.fex, the BoardConfig.mk, env.cfg, and sys_partition.fex in the default directory will be used.

5. Note that BoardConfig.mk is an inclusion relationship, which is included by the BoardConfig.mk of the system solution to the upper layer. If the system solution and the upper layer have the same properties, they will be overwritten, otherwise they will be considered as added.

4.1. Solution Configuration

From the above information, we can understand that when the SDK is compiled, it will be initialized according to the configuration of the BoardConfig.mk file and decide which scheme to start compiling and packaging.

Taking the buildroot solution of evb1_auto as an example, the content of the BoardConfig.mk file is as follows:

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```
LICHEE_CHIP:=sun8iw20p1
LICHEE_PRODUCT:=t113_evb1_auto
LICHEE_BOARD:=t113_evb1_auto
LICHEE_FLASH:=
LICHEE_ARCH:=arm
LICHEE_KERN_DEFCONF:=config-5.4
LICHEE_KERN_DEFCONF_RECOVERY:=config-5.4-recovery
LICHEE_BUILDING_SYSTEM:=buildroot
LICHEE_BR_VER:=201902
LICHEE_BR_DEFCONF:=sun8iw20p1_t113_defconfig
LICHEE_COMPILER_TAR=arm/gcc-linaro-5.3.1-2016.05-x86_64_arm-linux-gnueabi.tar.xz
LICHEE_BRANDY_VER:=2.0
LICHEE_BRANDY_DEFCONF:=sun8iw20p1_auto_t113_i_defconfig
LICHEE_REDUNDANT_ENV_SIZE:=0x20000
LICHEE_ROOTFS:=target-arm-linaro-5.3.tar.bz2
LICHEE_COMPRESS:=gzip
LICHEE_NO_RAMDISK_NEEDED:=y
```

evb1_auto/BoardConfig.mk

<t113_i/configs/evb1_auto/BoardConfig.mk

```
LICHEE_KERN_DEFCONF:=config-5.4
#LICHEE_KERN_DEFCONF_RECOVERY:=sun8iw20p1smp_t113_recovery_defconfig
LICHEE_BUILDING_SYSTEM:=buildroot
LICHEE_BR_VER:=201902
LICHEE_BR_DEFCONF:=sun8iw20p1_t113_defconfig
LICHEE_COMPILER_TAR=arm/gcc-linaro-5.3.1-2016.05-x86_64_arm-linux-gnueabi.tar.xz
LICHEE_BRANDY_DEFCONF:=sun8iw20p1_auto_t113_i_defconfig
LICHEE_REDUNDANT_ENV_SIZE:=0x20000
LICHEE_RTOS_PROJECT_NAME:=t113_i_c906_evb1_auto
```

evb1_auto/buildroot/BoardConfig.mk

We mainly focus on the following:

- LICHEE_KERN_DEFCONF: The name of the kernel defconfig file, which is located in the linux-5.4 directory.
- LICHEE_BR_VER: The buildroot version, which will automatically search for the corresponding version in the SDK/buildroot/ directory.
- LICHEE_BR_DEFCONF: The default configuration of buildroot, in the path where the source code of the corresponding buildroot version is located.
- LICHEE_COMPILER_TAR: cross-compilation toolchain path. The corresponding cross-compilation chain compressed package should be stored in SDK/prebuilt/kernelbuilt/.
- LICHEE_BRANDY_DEFCONF: This is the default configuration of u-boot, in SDK/brandy/brandy-2.0/u-boot-2018.
- LICHEE_ROOTFS: If you do not want to use the buildroot build system, you can modify the value of this item to point to other root file systems you have built. Before using it, you should copy the corresponding root file system to SDK/device/config/rootfs_tar/.

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4.2. SPL

From the above information, we can understand that for the SPL stage, in most cases we only need to modify the sys_config.fex file.

This file is used for SPL and u-boot related configurations starting from kernel version 5.4 or higher. SDK will make some configurations for the current development board based on this file configuration when compiling/packaging. The main parts we need to modify are as follows:

4.2.1. Target board platform related information

```
;------------------------------------------------------------------------------
; debug_mode=0 means turning off the printing log of Boot0/SPL during startup, the default is 8.
; 0: Turn off all printing
; 1: Only print key nodes
; 4: Print error message
; 8: Print all log information
;------------------------------------------------------------------------------
; earseflag indicates whether to perform a burn or erase operation
; 0x0: Do not erase.
; 0x01: Erase, private, secure storage, user data, mbr partitions are retained, and others are erased.
; 0x11: Erase, private and secure storage partitions are retained, and others are erased.
; 0x12: Force erase (entire flash).
;------------------------------------------------------------------------------
[platform]
eraseflag = 1
debug_mode = 8
```

4.2.2. Target board storage device information

```
;------------------------------------------------------------------------------
```

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```
;storage_type = boot medium, 0-nand, 1-sd, 2-emmc, 3-nor, 4-emmc3, 5-spinand -1(defualt)auto scan
;----------------------------------------------------------------------------
[target]
storage_type = -1 ; It is recommended to use automatic identification or configure according to the actual storage device used
burn_key = 0 ; Whether to support DragonSN_V2.0 to burn sn number. 0-not supported, 1-supported
nand_use_ubi= 1   ; Whether to use ubi format partition for NAND storage, 1-use, 0-do not use
```

4.2.3. Target board debugging serial port information

```
;----------------------------------------------------------------------------
; Describe the form of gpio: Port: port + group number <function allocation><internal resistance status><drive capability><output level status>
;----------------------------------------------------------------------------
[uart_para]
uart_debug_port = 0
uart_debug_tx = port:PE2<6><1><default><default>
uart_debug_rx = port:PE3<6><1><default><default>
```

Here, you should determine which serial port to use as the debugging serial port based on your own baseboard configuration. It is recommended to use serial port 0. Otherwise, you need to modify the relevant content in the root file system built by buildroot. However, there is no requirement for which GPIOs are actually used by serial port 0. You can check the "T113-i_Series_Datasheet_V2.0.pdf" document to find out which pins can be reused as serial port 0.

4.2.4. Other requirements

By default, it is not recommended to modify SPL. If you really need to perform some special modifications in the SPL stage, please refer to the "Linux_SPL-PUB_Development Guide.pdf" document.

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4.3. u-boot

4.3.1. Device Tree

The uboot-board.dts file is the device tree file used in the early stage of uboot and is only used in the initial stage. When u-boot executes the sunxi_replace_fdt_v2 function, it will start to use the device tree information of the kernel stage, so only a few situations need to be configured in this file.

4.3.2. Environment variables

The env.cfg file is the environment variable file of the u-boot stage. Users can configure it by themselves, but there are several special places:

4.3.2.1. bootcmd variables

```
int sunxi_update_bootcmd(void)
{
    char  boot_commond[128];
    int   storage_type = get_boot_storage_type();
    memset(boot_commond, 0x0, 128);
    strncpy(boot_commond, env_get("bootcmd"), sizeof(boot_commond)-1);
    debug("base bootcmd=%s\n", boot_commond);

    if ((storage_type == STORAGE_SD) || (storage_type == STORAGE_EMMC) ||
        (storage_type == STORAGE_EMMC3) || (storage_type == STORAGE_EMMC0)) {
        sunxi_str_replace(boot_commond, "setargs_nand", "setargs_mmc");
        debug("bootcmd set setargs_mmc\n");
    } else if (storage_type == STORAGE_NOR) {
        sunxi_str_replace(boot_commond, "setargs_nand", "setargs_nor");
    } else if (storage_type == STORAGE_NAND) {
#ifdef CONFIG_SUNXI_UBIFS
#ifndef CONFIG_MACH_SUN8IW18
        if (nand_use_ubi()) {
            sunxi_str_replace(boot_commond, "setargs_nand", "setargs_nand_ubi");
            debug("bootcmd set setargs_nand_ubi\n");
        }
#endif
#else
        debug("bootcmd set setargs_nand\n");
#endif
    }
```

The above code exists in the SDK/brandy/brandy-2.0/u-boot-2018/board/sunxi/board_helper.c file, which means that the value of the bootcmd variable will be automatically switched according to the actual boot memory device. The switching rule is to change "setargs_nand" to the corresponding environment, so users can keep using the following configuration and let u-boot automatically switch.

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```
bootcmd=run setargs_nand boot_normal
```

4.3.2.2. mmc_root variable

```
int sunxi_update_partinfo(void)

        memset(part_name, 0, PARTITION_NAME_MAX_SIZE);
        if (storage_type == STORAGE_NAND) {
            sprintf(part_name, "nand0p%d", index);
        } else if (storage_type == STORAGE_NOR) {
            sprintf(part_name, "mtdblock%d", index);
        } else {
            sprintf(part_name, "mmcblk0p%d", index);
        }

        temp_offset = strlen((char*)info.name) + strlen(part_name) + 2;
        if (temp_offset >= PARTITION_SETS_MAX_SIZE) {
            printf("partition_sets is too long, please reduces "
                   "partition name\n");
            break;
        }
        sprintf(partition_index, "%s@%s:", info.name, part_name);
        if (root_partition && strncmp(root_partition, (char *)info.name, sizeof(info.name)) == 0)
            sprintf(root_part_name, "/dev/%s", part_name);
        if (blkoops_partition && strncmp(blkoops_partition, (char *)info.name, sizeof(info.name)) == 0
            sprintf(blkoops_part_name, "/dev/%s", part_name);
        offset += temp_offset;
        partition_index = partition_sets + offset;

    }
    partition_sets[offset - 1] = '\0';
    partition_sets[PARTITION_SETS_MAX_SIZE - 1] = '\0';
    env_set("partitions", partition_sets);

#ifdef CONFIG_SUNXI_DM_VERITY
    if (*root_part_name != 0) {
        printf("set root to dm-0\n");
        env_set("verity_dev", root_part_name);
        if (storage_type == STORAGE_NAND)
            env_set("nand_root", "/dev/dm-0");
        else if (storage_type == STORAGE_NOR)
            env_set("nor_root", "/dev/dm-0");
        else
            env_set("mmc_root", "/dev/dm-0");
    }
#else
    if (*root_part_name != 0) {
        printf("set root to %s\n", root_part_name);
        if (storage_type == STORAGE_NAND)      gerrit-sdk, 3个月前 • da-t113-tina5.0-v1.0 release
            env_set("nand_root", root_part_name);
        else if (storage_type == STORAGE_NOR)
            env_set("nor_root", root_part_name);
        else
            env_set("mmc_root", root_part_name);
```

The above code exists in the SDK/brandy/brandy-2.0/u-boot-2018/board/sunxi/board_helper.c file, which means that the value of the xxx_root variable will be automatically switched according to the actual boot memory device. The switching rule is to change "xxx_root" to the root_part_name value. Under normal circumstances, it should be automatically switched by u-boot. However, for users of eMMC core boards using SD cards, the mmc0 device may not correspond to the SD card, which may cause the boot to get stuck and fail to enter the file system.

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```
[    0.584401] 001: cfg80211: failed to load regulatory.db
[    0.606698] 001: Waiting for root device /dev/mmcblk0p5 ...
[    0.779385] 001: usb 1-1: new high-speed USB device number 2 using sunxi-ehci
[    1.031897] 001: hub 1-1:1.0: USB hub found
[    1.035936] 001: hub 1-1:1.0: 4 ports detected
[    1.120311] 000: [SNDCODEC][sunxi_check_hs_detect_status][191]:plugin --> switch:3
[    1.389589] 000: random: fast init done
[    1.489365] 001: usb 1-1.3: new high-speed USB device number 3 using sunxi-ehci
[    1.919379] 001: usb 1-1.4: new high-speed USB device number 4 using sunxi-ehci
[    2.222961] 001: cdc_ether 1-1.4:2.0 eth1: register 'cdc_ether' at usb-sunxi-ehci-1.4, CDC Ethernet Device, 00:e0:99:d3:98:19
[    4.649368] 001:
[    4.649368] 001: insmod_device_driver
[    4.649368] 001:
[    4.649661] 001: sunxi_usb_udc 4100000.udc-controller: 4100000.udc-controller supply udc not found, using dummy regulator
```

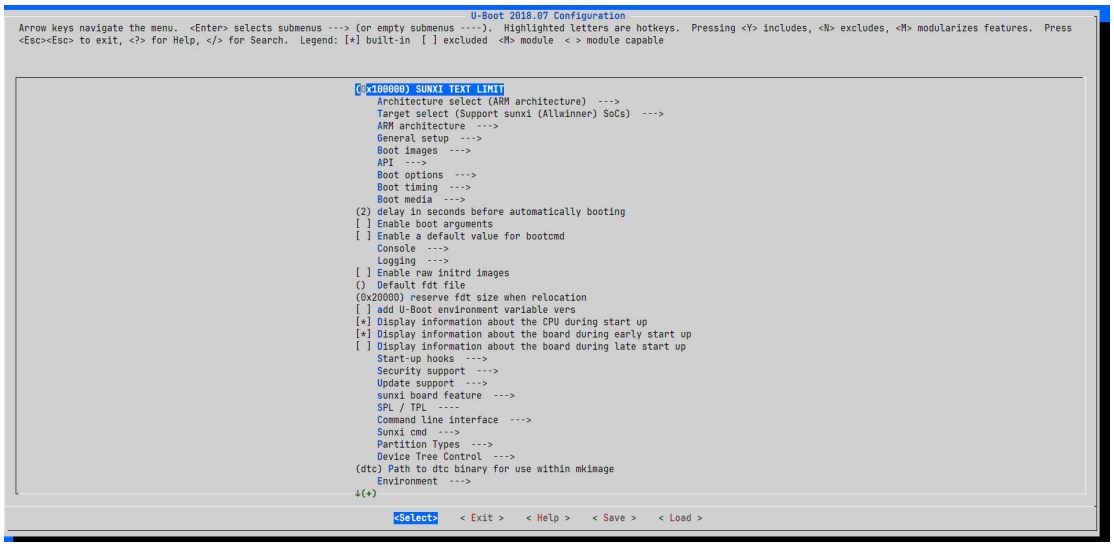
However, you can see from the above code that the requirement for assigning the value of root_part_name is that a partition with the same name as the root_partition in the environment variable can be found in the storage device partition. Therefore, for users who need to use SD cards, please do not try to configure the root_partition variable in env.cfg, and then modify the mmc_root variable value according to the actual situation. Generally, the variable value may be /dev/mmcblk0p5 or /dev/mmcblk1p5.

4.3.3. defconfig

The defconfig configuration file in the u-boot stage is determined by the value of the LICHEE_BRANDY_DEFCONF item in the BoardConfig.mk file. In the buildroot solution of evb1_auto, the file is SDK/brandy/brandy-2.0/u-boot-2018/configs/sun8iw20p1_auto_t113_i_defconfig. It is generally not recommended to modify it. If the user really needs to modify it, please follow the steps below to enter the menuconfig interface:

```
cd brandy/brandy-2.0/u-boot-2018

make menuconfig
```



It should be noted that if defconfig is reconfigured, the configuration just made through menuconfig will be overwritten.

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4.4. Kernel

The kernel-related configuration options are stored in the <SDK>/device/product/configs/<board>/linux-5.4 directory, which mainly consists of two files, one is board.dts and the other is config-5.4. The config-5.4 file can be renamed arbitrarily, just modify the value of the LICHEE_KERN_DEFCONF option in the BoardConfig.mk file accordingly, but the board.dts file should not be renamed. The SDK will soft-link the file to SDK/device/product/configs/<board>/board.dts during compilation, and finally generate SDK/out/kernel/staging/sunxi.dtb.

4.4.1. Device Tree

From the above, we know that the current board-level device tree configuration file is board.dts. For the board.dts file, most nodes have corresponding comments and descriptions for user reference, but some places are special:

1. spi-nand node
2. sdc0 node
3. sdc2 node

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```
int sunxi_update_fdt_para_for_kernel(void)
#endif
    /* fix nand&sdmmc */
    switch (storage_type) {
    case STORAGE_NAND:
        fdt_enable_node("nand0", 1);
        fdt_enable_node("spi0", 0);
        break;
    case STORAGE_SPI_NAND:
#ifdef CONFIG_SUNXI_UBIFS
#ifdef CONFIG_MACH_SUN8IW18
        if (nand_use_ubi() == 0) {
            /* sun8iw18 aw-nftl config */
            fdt_enable_node("spinand", 1);
            fdt_enable_node("spi0", 0);
        }
#else
        /* sun50iw11 config */
        fdt_enable_node("spi0", 1);
        fdt_enable_node("/soc/spi/spi-nand", 1);
#endif
#else
        /* legacy config */
        fdt_enable_node("spinand", 1);      gerrit-sdk
        fdt_enable_node("spi0", 0);
#endif
        break;
    case STORAGE_EMMC:
        ret = fdt_enable_node("sunxi-mmc2", 1);
        if (ret)
            fdt_enable_node("mmc2", 1);
#ifdef CONFIG_SUNXI_SDMMC
        mmc_set_mmcblckx("sunxi-mmc2");
#endif
        break;
    case STORAGE_EMMC0:
        ret = fdt_enable_node("sunxi-mmc0", 1);
        if (ret)
            fdt_enable_node("mmc0", 1);
        break;
    case STORAGE_EMMC3:
        ret = fdt_enable_node("sunxi-mmc3", 1);
        if (ret)
            fdt_enable_node("mmc3", 1);
        break;
    case STORAGE_SD:
```

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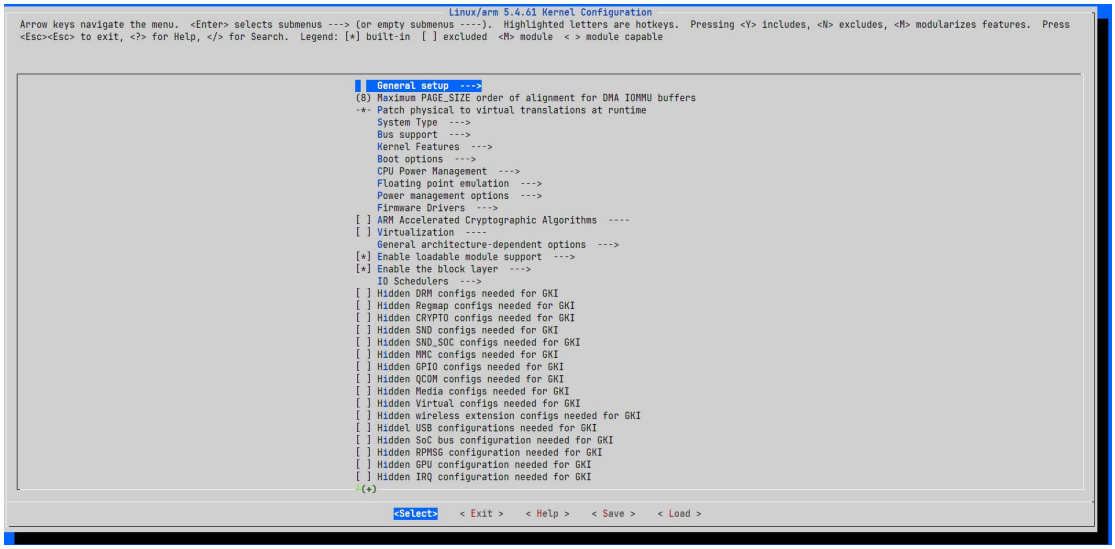
The above code exists in the SDK/brandy/brandy-2.0/u-boot-2018/board/sunxi/board_helper.c file, which means that u-boot will automatically enable/disable some nodes in the kernel device tree according to the corresponding storage device information at startup, so users may find that although the device node of the corresponding storage device is configured as `status = "disabled"`, they can still enter the system normally.

4.4.2. defconfig

From the above, we can know that the board-level defconfig file is determined by the LICHEE_KERN_DEFCONF value in the BoardConfig.mk file, and the configuration file must exist in the <SDK>/device/product/configs/<board>/linux-5.4 directory.

Here we take evb1_auto as an example, and its kernel defconfig file is <SDK>/device/product/configs/evb1_auto/linux-5.4/config-5.4. If the user needs to modify the configuration, he can use the following method to enter the menuconfig interface in the SDK top-level directory to configure:

```
./build.sh menuconfig
```



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```
$ ~/work ./build.sh menuconfig
========ACTION List: handle_defconfig menuconfig;========
options :
INFO: Prepare toolchain ...
make: Entering directory '/home/me/work/kernel/linux-5.4'
make[1]: Entering directory '/home/me/work/out/t113_i/kernel/build'
  GEN     Makefile
scripts/kconfig/mconf  Kconfig


*** End of the configuration.
*** Execute 'make' to start the build or try 'make help'.

make[1]: Leaving directory '/home/me/work/out/t113_i/kernel/build'
make: Leaving directory '/home/me/work/kernel/linux-5.4'
```

After the modification is completed, the user can update and save the new configuration to the config-5.4 file using the following command:

```
./build.sh saveconfig
```

```
$ ~/work ./build.sh saveconfig
========ACTION List: handle_defconfig saveconfig;========
options :
INFO: Prepare toolchain ...
make: Entering directory '/home/me/work/kernel/linux-5.4'
make[1]: Entering directory '/home/me/work/out/t113_i/kernel/build'
  GEN     Makefile
scripts/kconfig/conf  --savedefconfig=defconfig Kconfig
make[1]: Leaving directory '/home/me/work/out/t113_i/kernel/build'
make: Leaving directory '/home/me/work/kernel/linux-5.4'
$ ~/work
```

4.5. File System

4.5.1. Buildroot

From the above, we can know that the board-level configuration related to buildroot is stored in the <SDK>/device/product/configs/<board>/[buildroot]/BoardConfig.mk file, mainly LICHEE_BR_VER and LICHEE_BR_DEFCONF.

For more buildroot related information, please refer to the buildroot official manual:https://buildroot.org/downloads/manual/manual.html.

4.5.1.1. defconfig

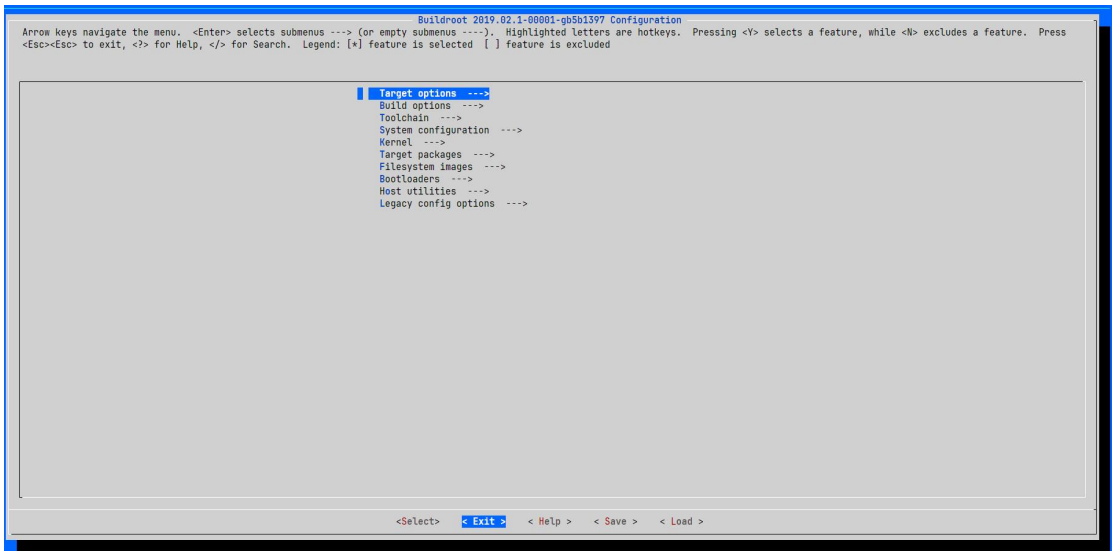
Here we take ebyte_t113s4 as an example. The buildroot version used is 201902. The source code file is located in SDK/buildroot/buildroot-201902, and its compilation configuration file is located in <SDK>/buildroot/buildroot-201902/configs/sun8iw20p1_t113_defconfig.

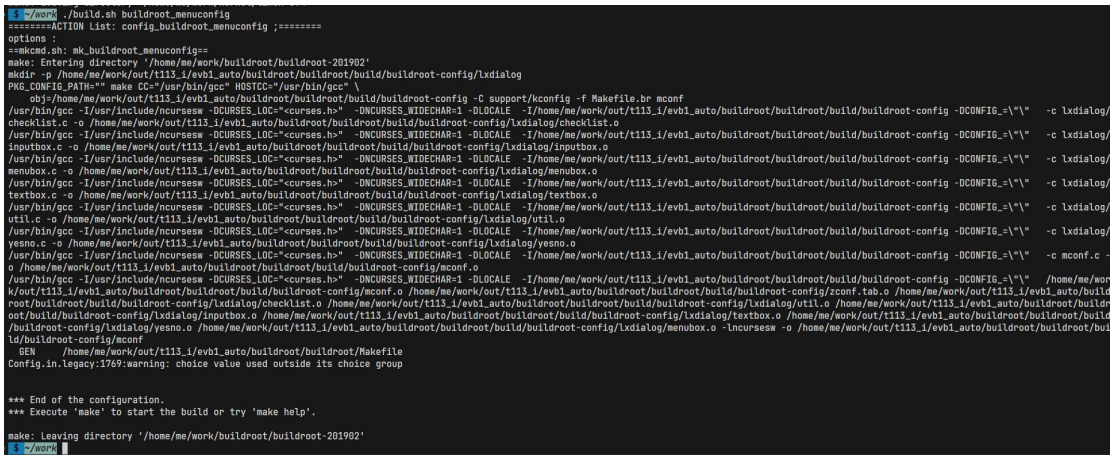
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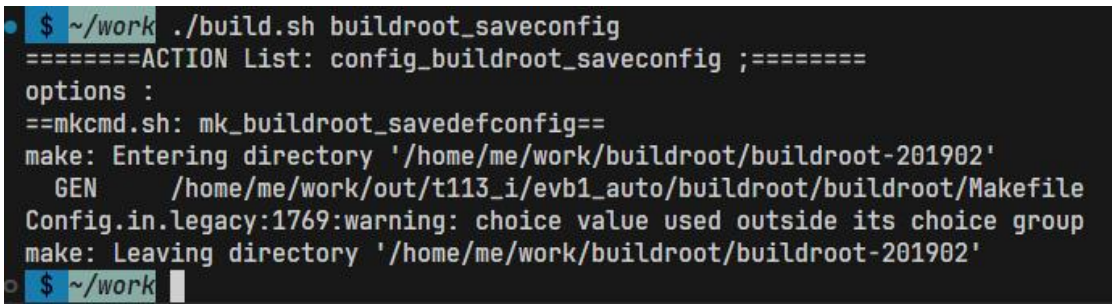
For most users, in addition to adapting the kernel to the baseboard, they also need to modify the root file system to adapt to their own usage requirements. It is recommended to use menuconfig to modify the configuration to further adapt to their own solution requirements. You can enter the menuconfig interface of buildroot with the following command:

```
./build.sh buildroot_menuconfig
```





After the modification is completed, the user can update and save the new configuration to the sun8iw20p1_t113_defconfig file using the following command:



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4.5.2. Modify the file system to buildroot202205

Take ECB30's T113-S4 as an example, modify <SDK>/device/config/chips/t113_s4/configs/ebyte_t113s4/buildroot/BoardConfig.mk

Revise:

LICHEE_BR_VER:=202205

LICHEE_BR_DEFCONF:=sun8iw20p1_t113_i_emmc_defconfig

Then execute the modified ./build.sh config

Finally, execute the compilation./build,sh

Users can get the file system image of buildroot2022. However, all functions of Ebyte are verified under buildroot2018, and buildroot202205 requires users to verify or add functions by themselves.

4.5.2.1. Unable to log in

For users using buildroot version 202205, if you cannot log in or find that you need to enter the same content twice in a row after logging in before it will be displayed once in the terminal, such as entering `aa` and the terminal displays `a`, please modify the following file: `<SDK>/buildroot/config/buildroot/buildroot/post_build.sh` and add the following content:

```
+ # change ttyAS0 to ttyS0
+ if grep -q "ttyAS0" ${TARGET_DIR}/etc/inittab; then
+ echo "found ttyAS0, change it to ttyS0!"
+ sed -i 's/ttyAS0/ttyS0/g' ${TARGET_DIR}/etc/inittab
+ fi
+
        #commented ttyS0, insert /bin/sh
        grep "#ttyS0::respawn:" ${TARGET_DIR}/etc/inittab >/dev/null
```

4.5.3. Change the file system to ubuntu

Here we take T113I as an example. We execute ./build.sh config in the SDK directory to configure it, and select linux->ubuntu->linux-5.4->t113_i->ebyte_t113i->default. Finally, execute ./build.sh and ./build.sh pack to generate the image. The default password of the provided

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ubuntu image is ebyte.

4.5.4. Partition

The sys_partition.fex file describes the partitioning of the packaged image. The main parts that users need to modify are the rootfs partition size and the root file system that should be placed in the partition. It must not be smaller than the actual root file system size, nor larger than the physical available size of the storage device. It should be noted that since the ubi solution reserves part of the storage size as a safety guarantee, the actual available size is slightly smaller than the theoretical size.

1. For eMMC or SD card storage devices, the rootfs partition should be roughly as follows:

```
[partition]
    name         = rootfs
    size         = 2097152
    downloadfile = "rootfs.fex"
    user_type    = 0x8000
```

2. For NAND storage devices, the rootfs partition should look like the following:

```
[partition]
    name         = rootfs
    size         = 342216
    downloadfile = "rootfs-ubifs.fex"
    user_type    = 0x8000
```

4.6. RISC-V + DSP

For RISC-V and DSP porting, customers can refer to "FreeRTOS_RTOS_System_Development Guide.pdf" and "DSP_Customer_User Guide.pdf" respectively.

5. How to burn and update the system image

This chapter mainly introduces the burning and updating method based on the ECK30-T13IA core board.

5.1. Official tool burning

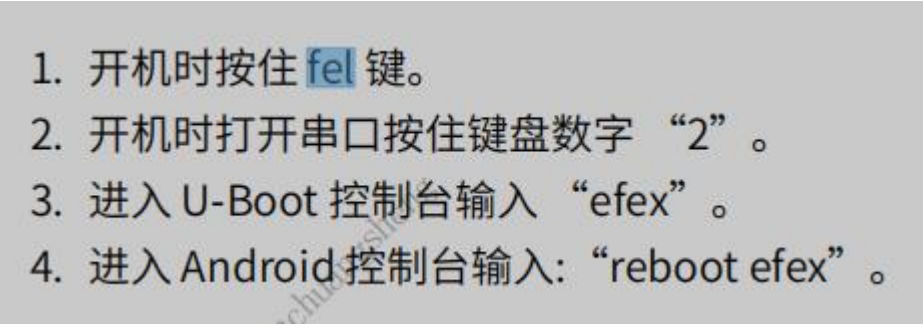
This section describes how to use the Allwinner official tool PhoenixSuit to implement USB fast burning function.

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5.1.1. Enter burning mode

Before using, we need to understand how to put the machine into programming mode. According to the official documentation, there are four ways:



1. Press and hold the FEL button while booting

2. Connect the serial port when booting, press and hold the number key "2"

3. Enter the u-boot console and enter: "efex"

4. Enter the system and enter in the system console: "reboot efex"

The common method is to press and hold the FEL button when turning on the computer. This requires the user to pull out the corresponding pins and connect them to a button when making the baseboard. This method does not require an available system on the storage device. For method 2, please note that this function is implemented in the boot firmware provided by Allwinner. If the user configures an incorrect serial port description in sys_config.fex, method 2 will fail. However, there is no need to worry. There are two other solutions:

1. Method 1: Re-burn and modify the correct firmware.

2. Make a mass production card by following the method in "Making an SD card boot card" and re-burn and modify the correct firmware.

5.1.2. Burning tool configuration

Let's look at the flashing tool. After installing and opening the tool, the interface is as follows:

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Click the "One-click Flash" item above to enter the image selection. The images we provide are divided into nand version and emmc version. Users need to choose the correct image. ECB30 and ECB31 series currently use emmc core boards. After selecting, you do not need to click the upgrade button immediately.

ECB30-T113I select t113_i_linux_ebyte_t113i_uart0.img

ECB31-P4T13SA1* select t113_linux_ebyte_t113_uart0.img

ECB31-P4T13SA2* select t113_s4_linux_ebyte_t113s4_uart0.img

ECB31-PB1E8 select t113_linux_ebyte_t113_PI_uart0.img

ECB31-PB2E8 select t113_s4_linux_ebyte_t113s4_PI_uart0.img

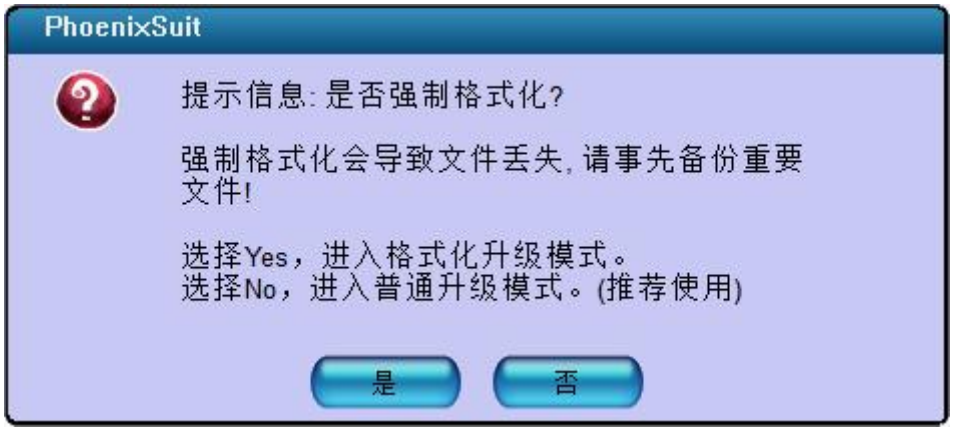
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Next, we connect two USB-typec data cables, one for observing the burning log and one for burning the onboard flash. After the connection is complete, we need to enter the burning mode.

ECB30 leads out the FEL button. The user can press FEL and hold it, then press the reset button or power on the board to enter the programming mode, and a programming inquiry box will pop up. ECB31 does not lead out the FEL button. The user can first open the serial terminal software, use the keypad to press keypad 2 on the serial terminal, then press the reset button or power on the board to enter the programming mode, and a programming inquiry box will pop up.



Users can choose whether to format and upgrade as needed.

It should be noted that T113 starts from the SD card first. After burning to the onboard flash,

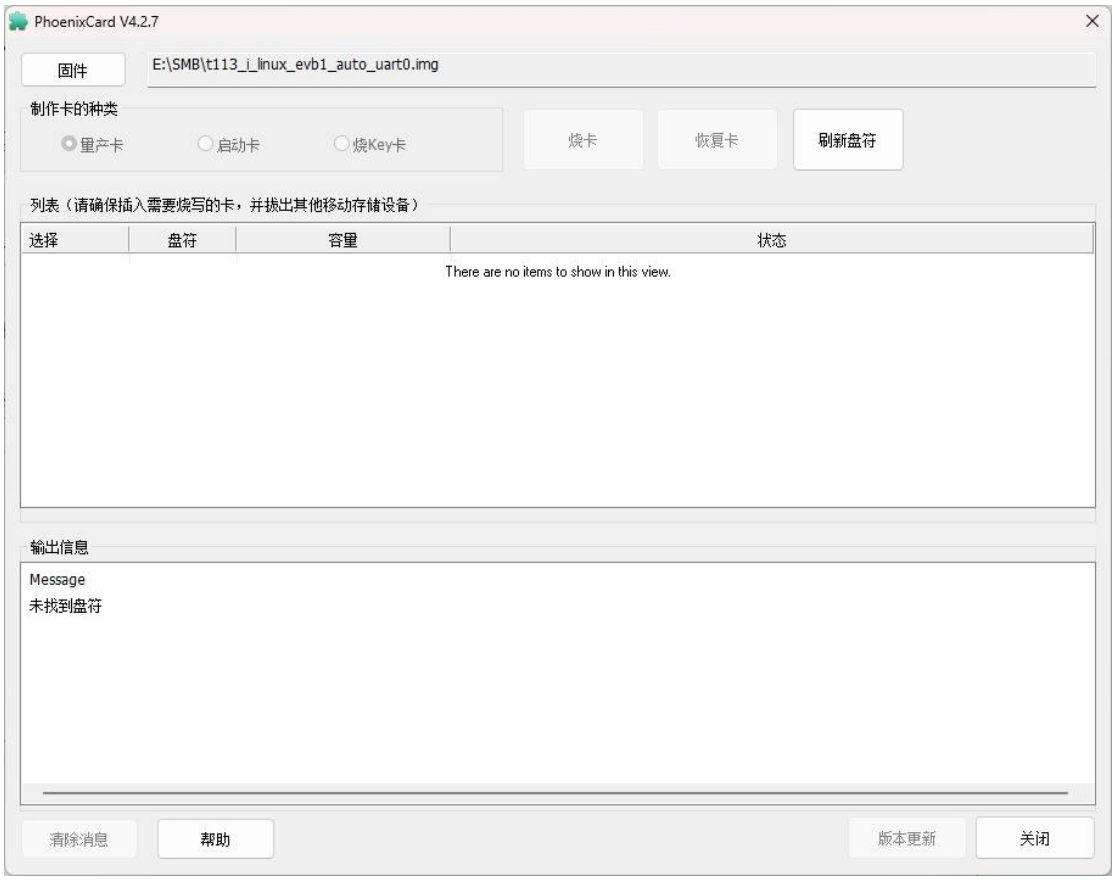
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you need to unplug the SD card to start from the onboard flash.

5.2. Make an SD card boot card

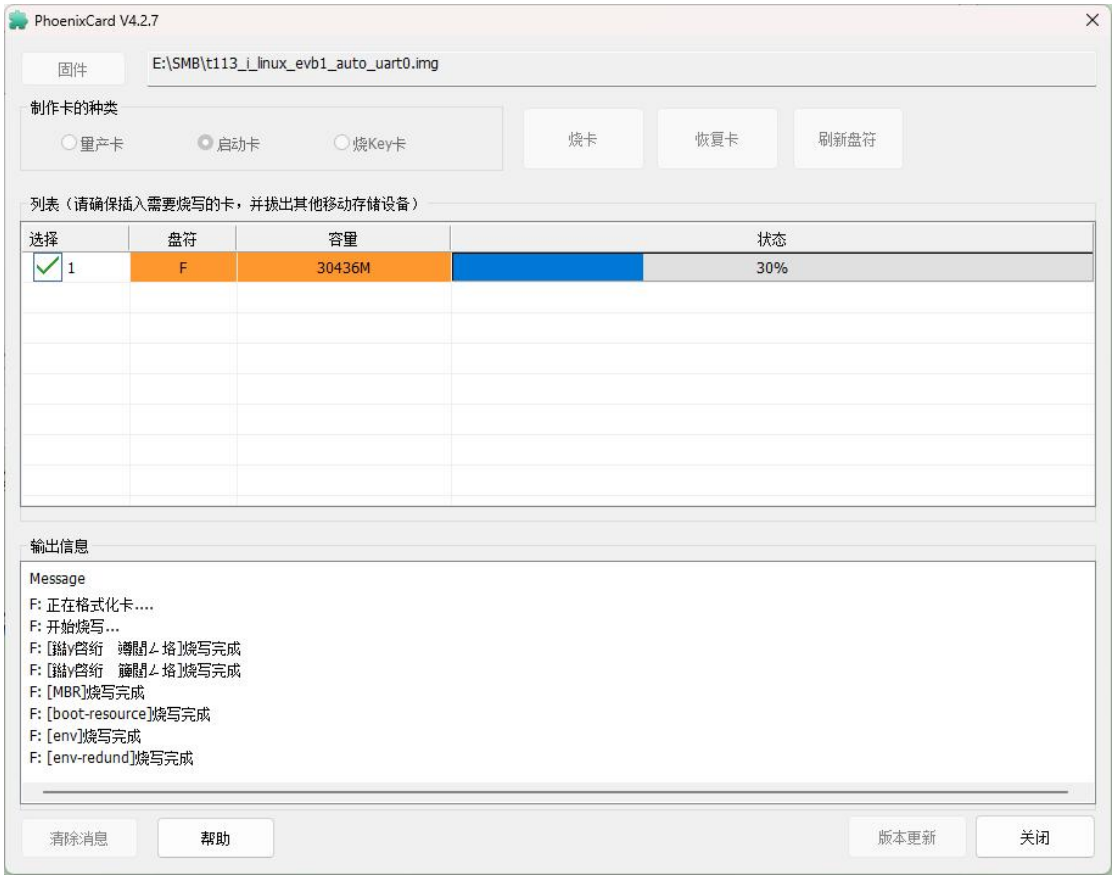
First open the PhoenixCard program, the interface is as follows:



The user should select the firmware to be burned, select the card type as "Startup Card", and then select the SD card device to be burned. The software will automatically scan the available mobile storage devices. Please be careful not to select the wrong device. Finally, click the "Burn Card" button, and the current progress will be displayed in real time during the burning process:

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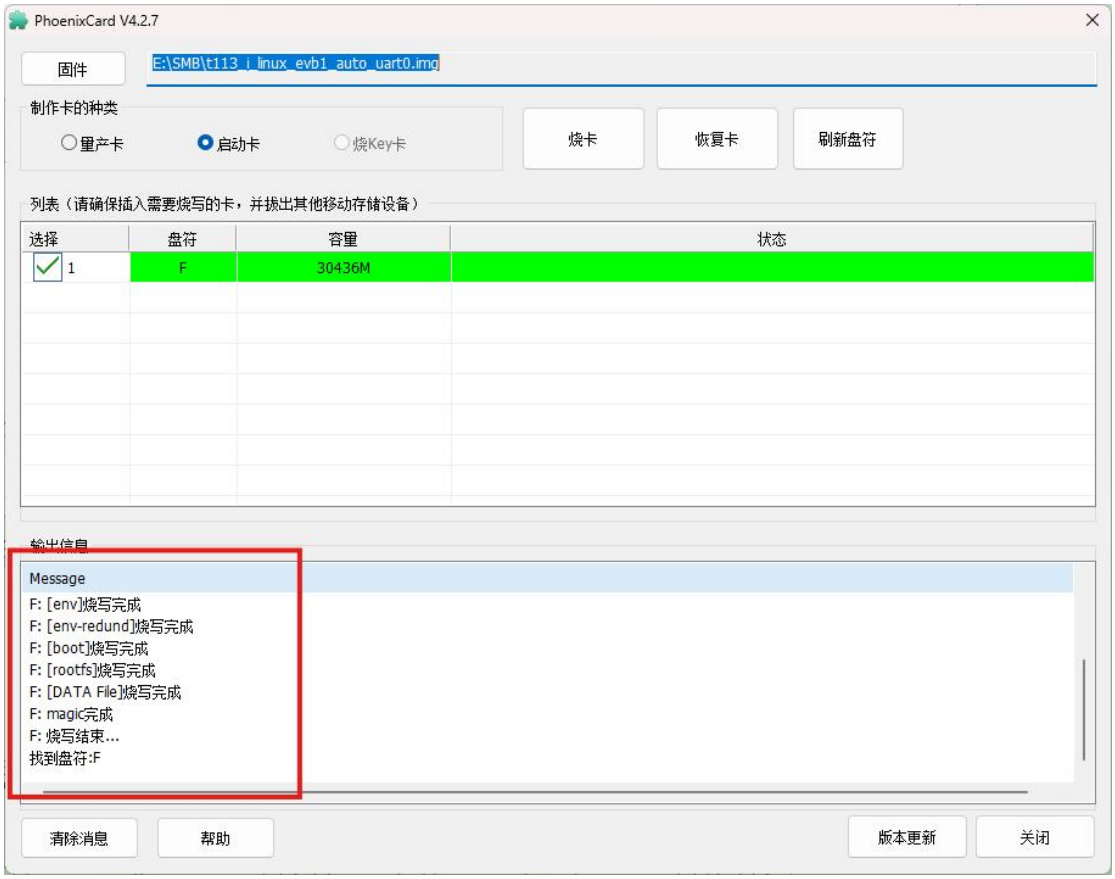
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After the flashing is completed, the interface is as follows. You can check whether the startup card is successfully made through the output information at the bottom of the software interface:

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5.3. Mass production card burning

When making an SD card startup card, we will also find that there is a type of mass-produced card. This method can be used to make an SD card for pipeline operation of the burning function. Users do not need to use a separate image firmware, but can directly use the image that can be used correctly in the startup card stage.

After the production is completed, start the system from the mass production card, connect the debug serial port and we can view all the printed information of the burning process:

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```
U-Boot 2018.07-g91003ae (Feb 06 2024 - 07:10:27 +0000) Allwinner Technology

[00.570]CPU:   Allwinner Family
[00.573]Model: sun8iw20
I2C:   ready
[00.594]optee version: major:3 minor:7
[00.598]DRAM:  [00.599]drm_base=0x0
[00.601]drm_size=0x0
[00.603]dram_base=0x40000000
[00.605]dram_size=0x40000000
1 GiB
[00.610]Relocation Offset is: 3ce93000
[00.640]secure enable bit: 0
E/TC:0   fdt_getprop_u32:336 prop trace_level not found
[00.654]CPU=1008 MHz,PLL6=600 Mhz,AHB=200 Mhz, APB1=100Mhz  MBus=300Mhz
[00.660]gic: sec monitor mode
[00.663]sunxi flash map init
SPI ALL:   ready
[00.668]line:719 init_clocks
[00.671]init_clocks:finish
[00.673]flash init start
[00.675]workmode = 17,storage type = 1
try card 2
set card number 2
get card number 2
[00.683][mmc]: mmc driver ver uboot2018:2023-07-4 16:18:00
[00.690][mmc]: Is not Boot mode!
[00.692][mmc]: SUNXI SDMMC Controller Version:0x50310
[00.704][mmc]: ************Try SD card 2************
[00.709][mmc]: mmc 2 cmd timeout 100 status 100
[00.713][mmc]: smc 2 err, cmd 8,  RTO
[00.716][mmc]: mmc 2 close bus gating and reset
[00.721][mmc]: mmc 2 cmd timeout 100 status 100
[00.725][mmc]: smc 2 err, cmd 55,  RTO
[00.729][mmc]: mmc 2 close bus gating and reset
[00.733][mmc]: ************Try MMC card 2************
[00.756][mmc]: mmc 2 cmd timeout 100 status 100
[00.760][mmc]: smc 2 err, cmd 8,  RTO
[00.763][mmc]: mmc 2 close bus gating and reset
[00.768][mmc]: mmc 2 cmd timeout 100 status 100
[00.772][mmc]: smc 2 err, cmd 55,  RTO
[00.776][mmc]: mmc 2 close bus gating and reset
[00.791][mmc]: gen_tuning_blk_bus8: total blk 10
[00.795][mmc]: gen_tuning_blk_bus4: total blk 6
[00.799][mmc]: Using 4 bit tuning now
[00.809][mmc]: write_tuning_try_freq: write ok
[00.814][mmc]: Pattern compare ok
[00.817][mmc]: Write tuning pattern ok
[00.820][mmc]: ================== HSSDR52_SDR25 ...
[00.825][mmc]: skip freq 400000
[00.828][mmc]: skip freq 25000000
[00.831][mmc]: freq: 2-50000000-64-4
```

After the system boots into the u-boot stage, the system begins to be burned.

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```
[61.690]successed in writting part dsp0
origin_verify value = 2e679ba6, active_verify value = 2e679ba6
[61.708]successed in verify part dsp0
[61.711]successed in download part dsp0
[61.716]successed in downloading part
uboot size = 0x168000
storage type = 2
sunxi_sprite_deal_uboot ok
[61.880]successed in downloading uboot
[61.886][mmc]: write mmc 2 info ok
dram para[0] = 318
dram para[1] = 3
dram para[2] = 7b7bfb
dram para[3] = 1
dram para[4] = 1102
dram para[5] = 4000000
dram para[6] = 1c70
dram para[7] = 42
dram para[8] = 18
dram para[9] = 0
dram para[10] = 4a2195
dram para[11] = 2423190
dram para[12] = 8b061
dram para[13] = b4787896
dram para[14] = 0
dram para[15] = 48484848
dram para[16] = 48
dram para[17] = 1620121e
dram para[18] = 0
dram para[19] = 0
dram para[20] = 0
dram para[21] = 770000
dram para[22] = 2
dram para[23] = b4056103
dram para[24] = 0
dram para[25] = 0
dram para[26] = 0
dram para[27] = 0
dram para[28] = 0
dram para[29] = 0
dram para[30] = 0
dram para[31] = 0
storage type = 2
[61.951]successed in downloading boot0
CARD OK
[61.955]sprite success
sprite_next_work=3
next work 3
SUNXI_UPDATE_NEXT_ACTION_SHUTDOWN
[64.963][mmc]: mmc exit start
[64.985][mmc]: mmc 2 exit ok
```

After the above-mentioned content appears, the user can disconnect the power supply of the development board, unplug the mass production card, and then power it on again. The serial terminal will print the following startup information to confirm that it is started from the eMMC:

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```
[33]HELLO! BOOT0 is starting!
[35]BOOT0 commit : 069ed30b88
[38]set pll start
[44]periph0 has been enabled
[47]set pll end
[49][pmu]: bus read error
[51]board init ok
[53]enable_jtag
[55]get_pmu_exist() = -1
[57]DRAM BOOT DRIVE INFO: V0.34
[60]DRAM CLK = 792 MHz
[62]DRAM Type = 3 (2:DDR2,3:DDR3)
[65]DRAMC ZQ value: 0x7b7bfb
[68]DRAM ODT value: 0x42.
[71]ddr_efuse_type: 0x0
[74]DRAM SIZE = 1024 MB
[81]DRAM simple test OK.
[83]rtc standby flag is 0x0, super standby flag is 0x0
[88]dram size =1024
[91]card no is 2
[92]sdcard 2 line count 4
[95][mmc]: mmc driver ver 2021-05-21 14:47
[104][mmc]: Wrong media type 0x0, but host sdc2, try mmc first
[110][mmc]: **Try MMC card 2**
[133][mmc]: RMCA OK!
[135][mmc]: mmc 2 bias 0
[138][mmc]: MMC 5.1
[140][mmc]: HSSDR52/SDR25 4 bit
[143][mmc]: 50000000 Hz
[145][mmc]: 7396 MB
[147][mmc]: **SD/MMC 2 init OK!!!**
[249]Loading boot-pkg Succeed(index=0).
[253]Entry_name        = u-boot
[260]Entry_name        = optee
[264]Entry_name        = dtb
[267]tunning data addr:0x430003e8
[270]Jump to second Boot.
M/TC: OP-TEE version: 2a99a16f (gcc version 5.3.1 20160412 (Linaro GCC 5.3-2016.05)) #1 Thu Aug 17 11:13:02 UTC 2023 arm
E/TC:0 0 platform_standby_fdt_parse:126 no pmu0 node
E/TC:0 0 sunxi_twi_parse_from_dt:121 no pmu node
```

For NAND boot, the following will be displayed:

```
[29]HELLO! BOOT0 is starting!T
[32]BOOT0 commit : 069ed30b88
[35]set pll start
[40]periph0 has been enabled
[43]set pll end
[45][pmu]: bus read error
[47]board init ok
[49]enable_jtag
[51]get_pmu_exist() = -1
[53]DRAM BOOT DRIVE INFO: V0.34
[56]DRAM CLK = 792 MHz
[58]DRAM Type = 3 (2:DDR2,3:DDR3)
[61]DRAMC ZQ value: 0x7b7bfb
[64]DRAM ODT value: 0x42.
[67]ddr_efuse_type: 0x0
[70]DRAM SIZE = 256 MB
[77]DRAM simple test OK.
[79]rtc standby flag is 0x0, super standby flag is 0x0
[84]dram size =256
[87]spinand UBOOT_START_BLK_NUM 8 UBOOT_LAST_BLK_NUM 32
[92]block from 8 to 32
[239]dma 0x2b3c4 int is not used yet
[242]dma 0x2b3c4 int is free, you do not need to free it again
[248]Entry_name        = u-boot
[255]Entry_name        = optee
[259]Entry_name        = dtb
[262]Jump to second Boot.
M/TC: OP-TEE version: 2a99a16f (gcc version 5.3.1 20160412 (Linaro GCC 5.3-2016.05)) #1 Thu Aug 17 11:13:02 UTC 2023 arm
E/TC:0 0 platform_standby_fdt_parse:126 no pmu0 node
E/TC:0 0 sunxi_twi_parse_from_dt:121 no pmu node
```

5.4. Possible problems

5.4.1. Can't find storage device when making startup card

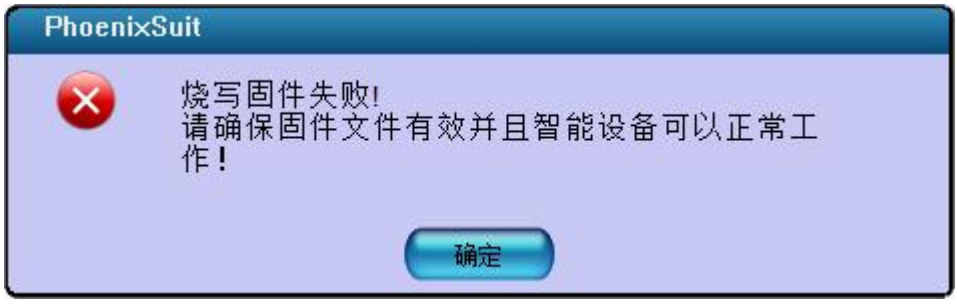
When using the tool to burn the startup card, the user may find that the tool cannot find the corresponding storage device after connecting the SD card. Please confirm whether the SD card is occupied by VMware. If it is occupied, please release the occupation and return the usage right to the host. If it is still unavailable, please try to change the USB interface or check whether it is occupied by other programs.

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5.4.2. Failed to flash the firmware

When making a NAND board image and burning it, you may encounter a burning failure. For example, the burning tool will display the following message:



At this time, please open the serial port tool, connect the debug serial port, and check the error reported during the burning process. Take the following situation as an example:

```
defaults:
mtdids  : nand0=nand
mtdparts: mtdparts=nand:1024k@0(boot0)ro,3072k@1048576(uboot)ro,1024k@4194304(secure_storage)ro,-(sys)
[29.582]MTD info (4)
[29.584]pagesize: 0x1000
[29.586]blksize: 0x40000
[29.589]num  offset     bytes      name
[29.592]0    0x00000000 0x00100000 boot0
[29.596]1    0x00100000 0x00300000 uboot
[29.599]2    0x00400000 0x00100000 secure_storage
[29.604]3    0x00500000 0x0fb00000 sys
[29.607]MBR info (unalign):
[29.610]partno   addr       sects      type       name
[29.615]0        0x00000000 0x00008000 0x00000001 mbr
[29.619]1        0x00008000 0x00008686 0x00008000 boot-resource
[29.625]2        0x00010686 0x00000800 0x00008000 env
[29.630]3        0x00010e86 0x00000800 0x00008000 env-redund
[29.635]4        0x00011686 0x00008980 0x00008000 boot
[29.640]5        0x0001a006 0x00200000 0x00008000 rootfs
[29.645]6        0x0021a006 0x00000800 0x00008000 dsp0
[29.650]7        0x0021a806 0x00008000 0x00008000 private
[29.655]8        0x00222806 0x00000000 0x00008100 UDISK
[29.660]ubi attach the last part of mtd device: NO.3
[29.665]MBR info (align):
[29.667]partno   addr       sects      type       name
[29.672]0        0x00002800 0x000081f0 0x00000001 mbr
[29.677]1        0x0000a9f0 0x000087d8 0x00008000 boot-resource
[29.682]2        0x000131c8 0x000009d8 0x00008000 env
[29.687]3        0x00013ba0 0x000009d8 0x00008000 env-redund
[29.692]4        0x00014578 0x000089d0 0x00008000 boot
[29.697]5        0x0001cf48 0x002001f0 0x00008000 rootfs
[29.702]6        0x0021d138 0x000009d8 0x00008000 dsp0
[29.707]7        0x0021db10 0x000081f0 0x00008000 private
[29.717]ubi attach the last part of mtd device: NO.3
[29.722]ubi volume total size is larger than mtd size.
ubi_vol_total_bytes : 0x446a0000, mtd_bytes: 0xfb00000
[29.732]initialize sunxi spinand ubi failed
download_standard_gpt:write mbr sectors fail ret = 0
```

Check the contents in the red box and you will find that the ubi volume size exceeds the actual device storage size. In connection with the above content about image packaging and development board adaptation, we can know that the ubi volume size is actually determined by the configuration in the file `SDK/device/product/configs/<board>/[buildroot]/sys_partition.fex`. At this time, we need to modify the size of one of the partitions to ensure that it does not exceed the physical storage size limit. The following is an example to modify the largest rootfs partition size

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and reduce the ubi volume size:

```
[partition]
    name         = rootfs
    size         = 342216
    downloadfile = "rootfs-ubifs.fex"
    user_type    = 0x8000
```

At the same time, we also need to pay attention that the size cannot be smaller than the actual rootfs-ubifs.fex file size.

5.5. Existing system updates

In the previous chapters "Official Tool Burning" and "Making SD Card Boot Card", we have successfully solidified the Linux file system, and the system has been successfully started. But sometimes during the development process, we try to compile and make uboot, kernel and file system by ourselves, and later we only want to update one of uboot, device tree and file system, especially during the development process, we may need to update frequently. In order to save development time, we can use a separate update method to update the part we need to update, instead of spending more time to re-make an SD system boot card or use scripts to solidify the system to eMMC or NAND.

For the method of transferring files between the development host and the development board, please refer to this document "Data Transfer"chapter.

5.5.1. Related Documents

If you want to flash the system update, you will inevitably need to know the path of the relevant files. Here is a description of where the flash-related files are and what their names are:

Burning files

document	file name	True Path
Uboot Image	boot_package.fex	SDK/out/t113_i/<board>/pack_out/boot_package.fex
Kernel image	boot.fex	SDK/out/t113_i/<board>/buildroot/boot.img
Root file system	rootfs.fex	SDK/out/t113_i/tlt113-evm-emmc/buildroot/rootfs.ext4

5.5.2. Burning location

After understanding the relevant files, we also need to understand where these files should be burned:

Burning location

document	Programming location (NAND)	Burning location (eMMC)

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Uboot Image	/dev/mtd1	/dev/mmcblkX(seek 32800)
Kernel image	/dev/mtd8	/dev/mmcblk0p4
Root file system	/dev/mtd9	/dev/mmcblk0p5

For the kernel image and root file system, you also need to refer to the partition configuration in the `SDK/device/config/chips/t113_i/configs/<board>/[buildroot]/sys_partition.fex` file to determine.

5.5.3. eMMC + SD card

If the user is using an eMMC core board and wants to update the firmware in an existing system, we need to boot from eMMC or from an SD card to replace our own firmware.

5.5.3.1. U-boot

The uboot compiled by the SDK of T113-i is a packaged file boot_package.fex, which should be burned completely during the burning process.

1. Copy the compiled boot_package.fex file to the current directory of the eMMC/SD card system through nfs/tftp/U disk and other methods.

2. Find the path of the device to be flashed, and determine whether the device is eMMC or SD card according to the description of `timing spec` in the `ios` node. In the following text, `mmc0` is used to represent the eMMC device for demonstration. Users should determine it according to the actual situation.

```
mount -t debugfs debugfs /sys/kernel/debug/
ls /sys/kernel/debug/mmc*
cat /sys/kernel/debug/mmc0/ios
cat /sys/kernel/debug/mmc1/ios
```

3. Write the u-boot image to the mmcblk0 device

```
dd if=boot_package.fex of=/dev/mmcblk0 seek=32800
```

4. [Optional] Write the u-boot image to the backup partition

```
dd if=boot_package.fex of=/dev/mmcblk0 seek=24576
```

5. Aftermath and Restart

```
sync && reboot
```

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5.5.3.2. Kernel image

The kernel compiled by the SDK of T113-i has packaged the kernel and device tree into a whole according to the Android image, so you can directly burn the boot.img when burning.

1. Copy the compiled kernel and device tree files to the current directory of the eMMC/SD card system through nfs/tftp/U disk and other methods.

2. Find the path of the device to be flashed, and determine whether the device is eMMC or SD card according to the description of `timing spec` in the `ios` node. In the following text, `mmc0` is used to represent the eMMC device for demonstration. Users should determine it according to the actual situation.

```
mount -t debugfs debugfs /sys/kernel/debug/
ls /sys/kernel/debug/mmc*
cat /sys/kernel/debug/mmc0/ios
cat /sys/kernel/debug/mmc1/ios
```

3. Copy the kernel and device tree to the boot partition.

```
dd if=boot.fex of=/dev/mmcblk0p4
```

4. Aftermath and Restart

```
sync && reboot
```

5.5.3.3. Root file system

Generally speaking, onlyUse SD card boot to update the eMMC file system, or boot through eMMC to update the SD card. A running system cannot format itself!

1. Conversionrootfs.fex to a compressed package [in ububtu environment]

```
sudo apt install android-tools-fsutils
simg2img rootfs.fex rootfs.img
mkdir rootfs_dir
sudo mount rootfs.img rootfs_dir
cd rootfs_dir
sudo tar -cavf ../rootfs.tar.gz *
```

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2. Copy the converted root file system (rootfs.tar.gz) to the eMMC/SD card system via nfs/tftp/U disk.

3. Mountmmcblk0p5Partition

```
mkdir dest_dir
mount /dev/mmcblk0p5 ./dest_dir
```

4. Clearmmcblk0p5Partition old data

```
rm -rf dest_dir/*
```

5. Copy the new root file system tommcblk0p5Partition

```
tar -xavf rootfs.tar.gz -C dest_dir/
sync
```

6. Unmount the partition

```
umount dest_dir/
```

7. Restart the system

```
reboot
```

When copying rootfs.tar.gz, there may be insufficient available space. You can try to copy it to the `/mnt/UDISK` directory. If the user does not have this directory, please mount the /dev/mmcblk0p8` partition to any directory and copy rootfs to this directory.

5.5.4. NAND

If the user is using a NAND core board and wants to update the firmware in an existing system, we need to boot from the NAND itself.

5.5.4.1. U-boot

The uboot compiled by the SDK of T113-i is a packaged file boot_package.fex, which should be burned in its entirety during the burning process

1. Copy the compiled boot_package.fex file to the current directory of the system through nfs/tftp/U disk or other methods.

2. Erase old u-boot

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```
flash_erase /dev/mtd1 0 0
```

3. Write the u-boot image to the mtd1 device

```
dd if=boot_package.fex of=/dev/mtd1
```

4. Aftermath and Restart

```
sync && reboot
```

5.5.4.2. Kernel image

The kernel compiled by the SDK of T113-i has packaged the kernel and device tree into a whole according to the Android image, so you can directly burn the boot.img when burning.

1. Copy the compiled kernel and device tree files to the current directory of the system through nfs/tftp/U disk

2. Erase the old kernel image

```
flash_erase /dev/mtd8 0 0
```

3. Copy the kernel and device tree to the boot partition.

```
dd if=boot.fex of=/dev/mtd8
```

4. Aftermath and Restart

```
sync && reboot
```

6. References

- T113-i_Series_Datasheet_V2.0.pdf
- T113-i_Series_User_Manual_V2.0.pdf
- Linux kernel open source community:https://www.kernel.org/

7. Revision Notes

Revision Notes Table

Version	Modifications	Modification time	prepared by	Proofreading	Approval
V1.0	First Draft	2024-9-25	WYQ	WFX	WFX
V1.1	Fix the missing development environment and solve the problem that buildroot 202205 cannot log in	2024-11-20	WYQ	WFX	WFX
V1.2	Add ubuntu and modify some operation descriptions	2025-3-17	HSL	WFX	WFX
V1.3	Add the configuration instructions of ECB31-PBxE8	25-5-29	HSL	WFX	WFX

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