Table of Content

框图
File: 框图.kicad_sch

电源树
File: 电源树.kicad_sch

POWER
File: POWER.kicad_sch

USB_HOST
File: USB_HOST.kicad_sch

USB_OTG&UART
File: USB_OTG&UART.kicad_sch

RGMII_1000M
File: RGMII_1000M.kicad_sch

MICRO SD
File: MICRO SD.kicad_sch

AUDIO
File: AUDIO.kicad_sch

CONNECTOR
File: CONNECTOR.kicad_sch

WIFI
File: WIFI.kicad_sch

MIPI_DSI
File: MIPI_DSI.kicad_sch

SOM CONNECTOR
File: SOM CONNECTOR.kicad_sch

30012

P2024-EC007

Revision History

V1.0-2024.09-23-WJ
1、初始版本。
2、调整器件位号。
3、J1电源接口封装引脚画反，2号引脚改为4，4号引脚改为2。
4、D10和D11型号由LESD8LL5.0CT5G改为LESD8D3.30CT5G。
5、按键旁增加TVS和滤波电容。
6.USB_OTG_ID到SOM中间加一个1K电阻。
7.CH334U芯片由原来的5V电源供电改为3.3V供电，降低功耗。
8.OTG电路的TYPE-C接口的CC检测引脚上拉电阻由原来的10K改为51K。
9.USB转串口电路芯片改为CH340X，且增加MOS管保护电路，可防止电源漏电。
10.网口指示灯电路调整，已按芯片手册参考电路调整。
11.TVIN0信号两个对地电阻封装由0402改为0603，增大功率承受能力。
12.MIPI_DSI接口RESET引脚加电平转换电路，将MCU端3.3V电平转为1.8V对外输出。

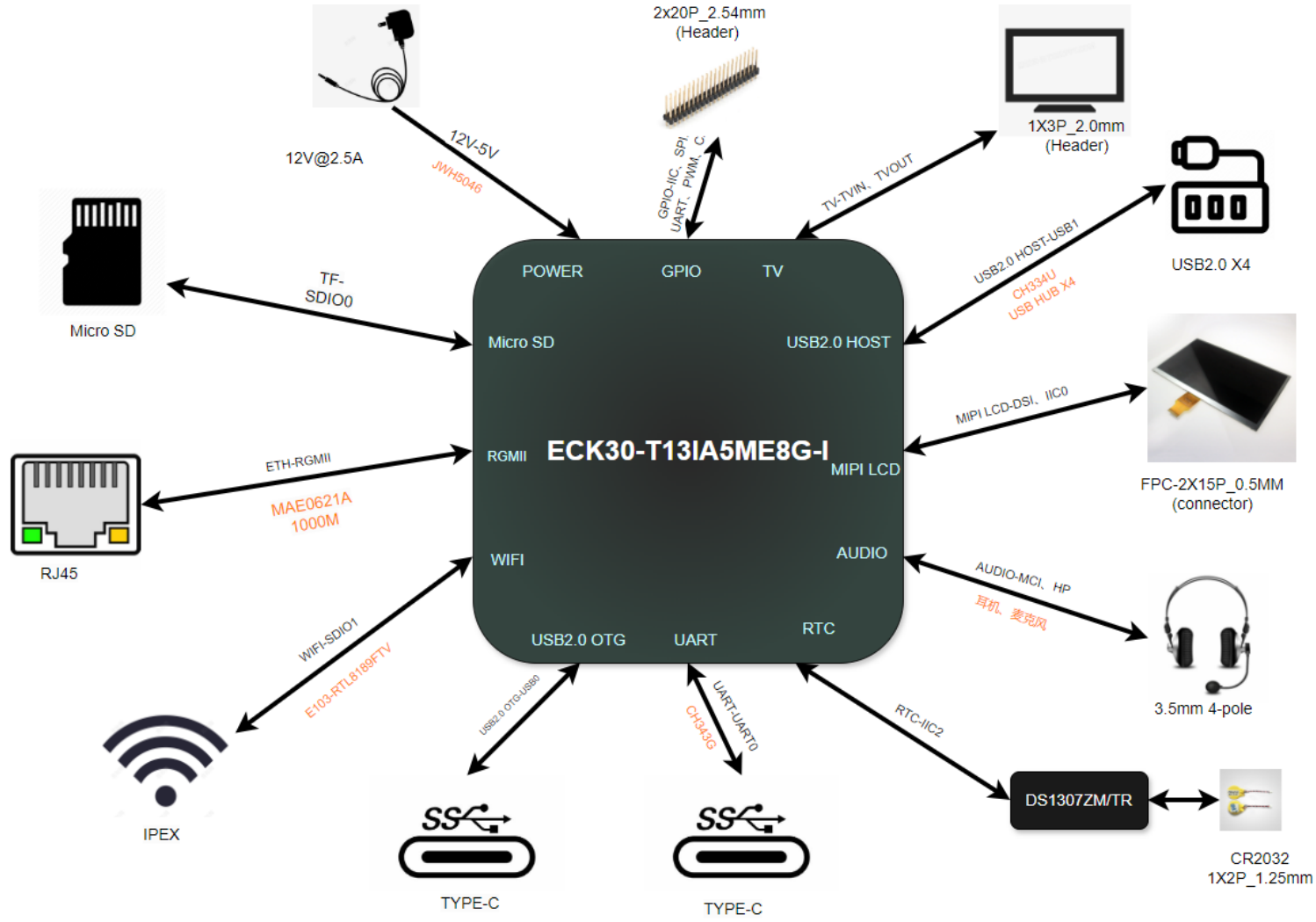
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KiCad E.D.A. 8.0.1		Id: 1/13

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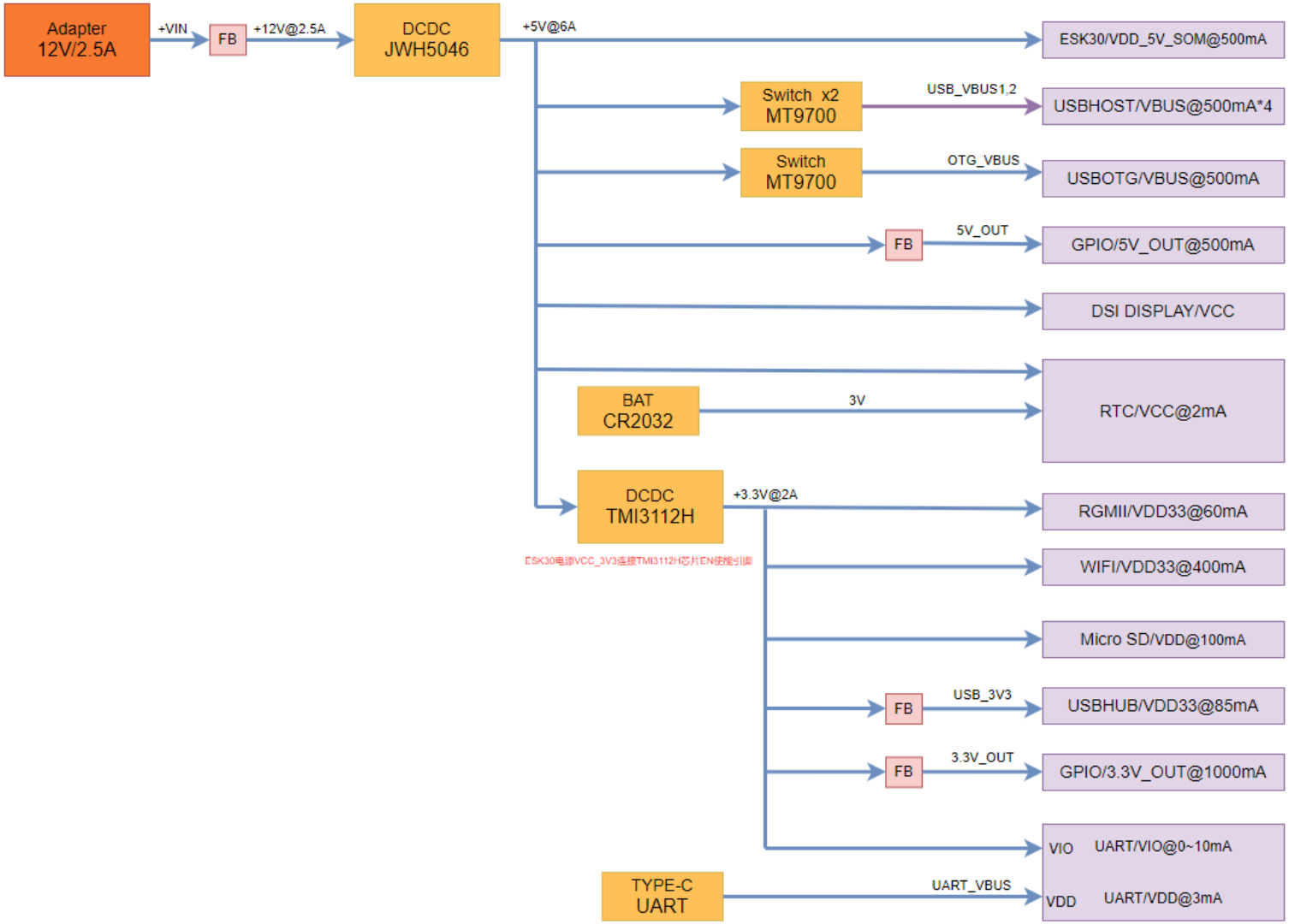
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KiCad E.D.A. 8.0.1		Id: 1/13

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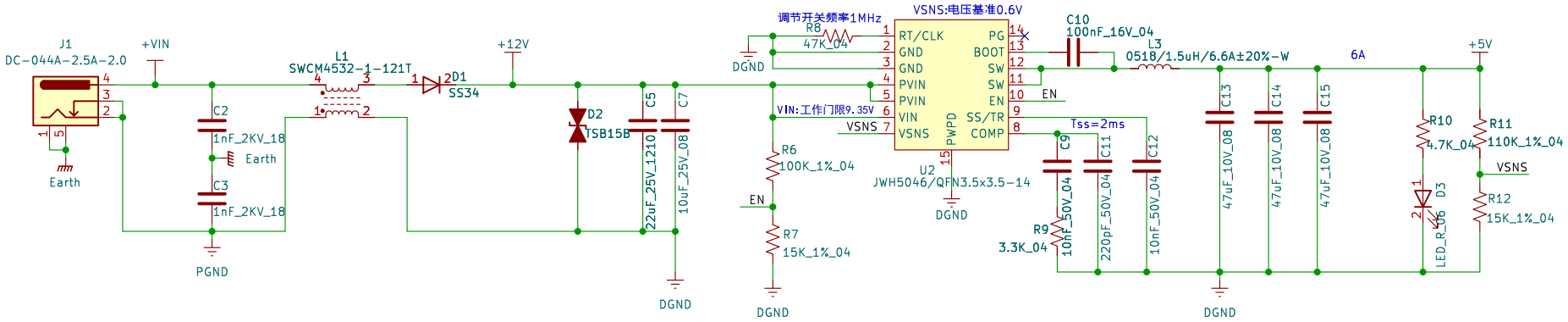
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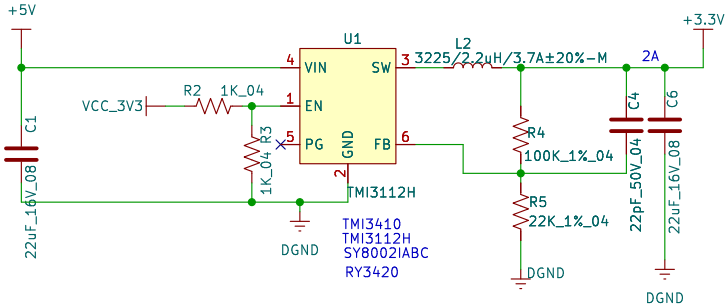
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KiCad E.D.A. 8.0.1		Id: 2/13

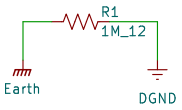
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12V TO 5V



5V TO 3.3V





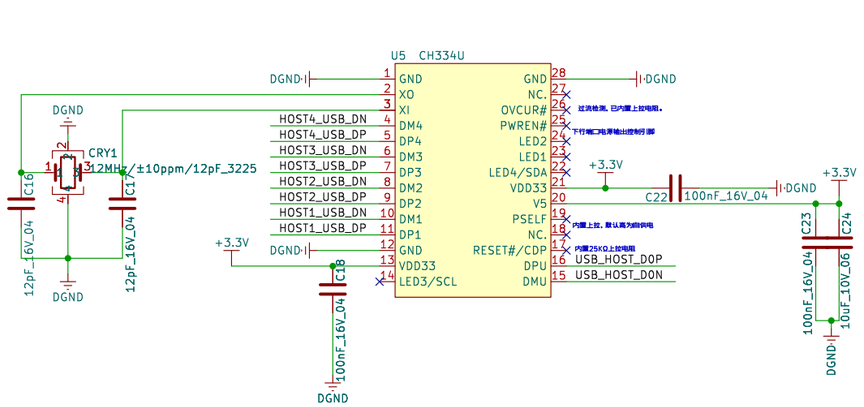
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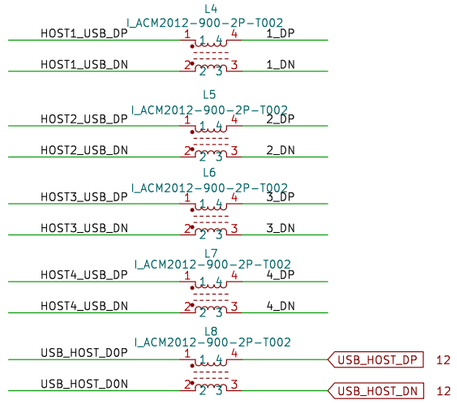
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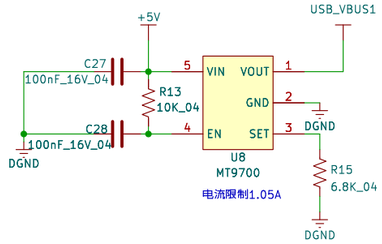
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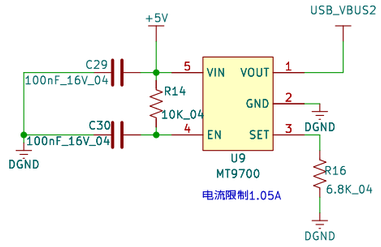
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KiCad E.D.A. 8.0.1 | Id: 3/13

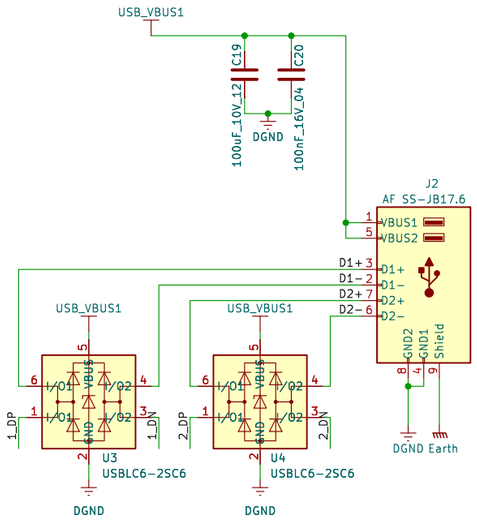
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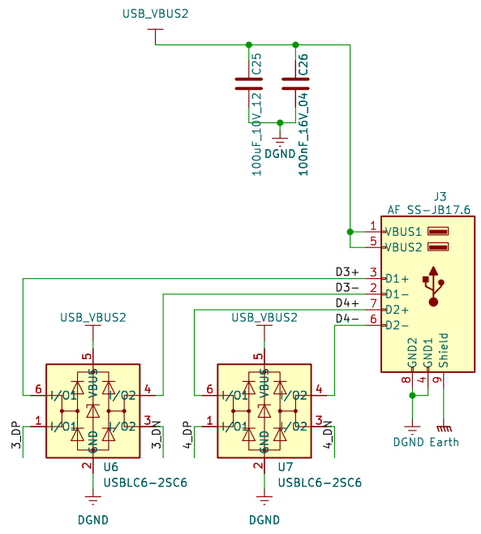














USB_HOST

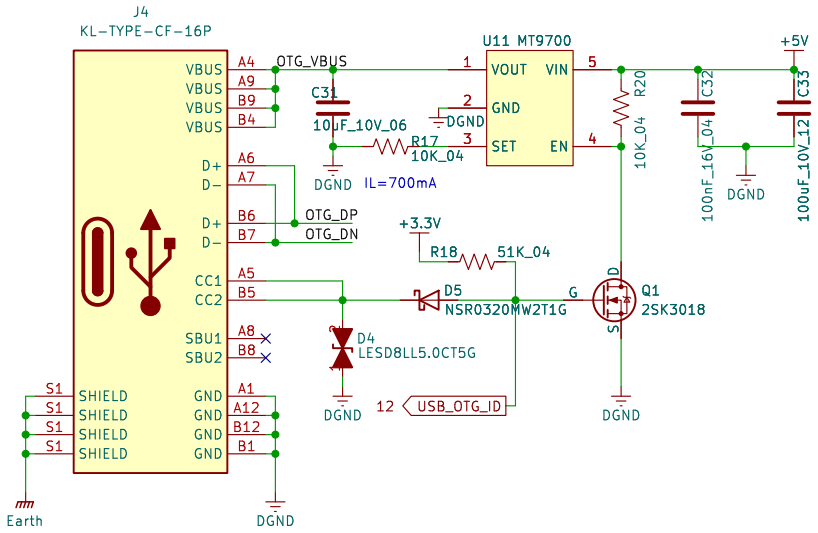
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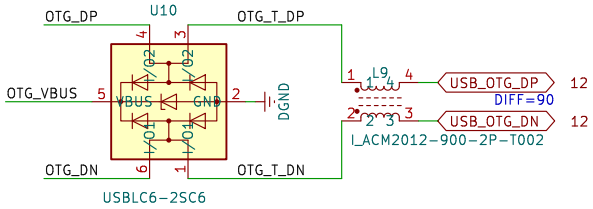
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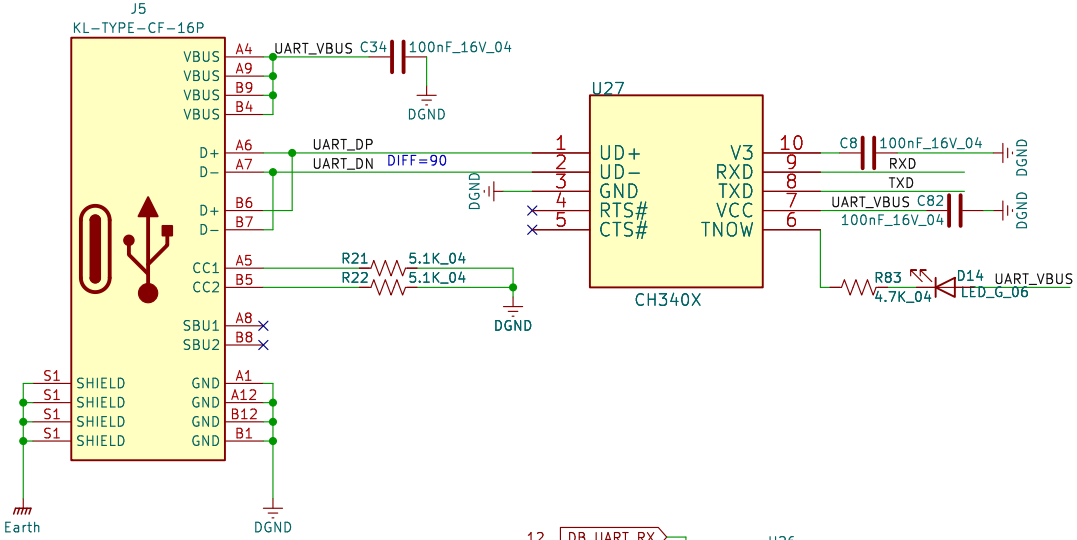
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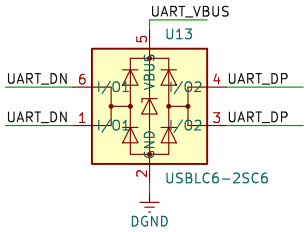
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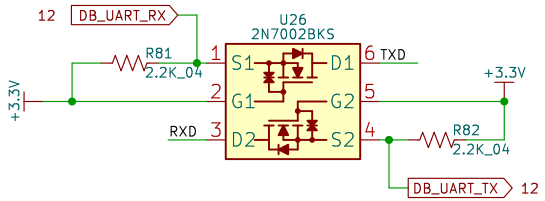




USB_OTG





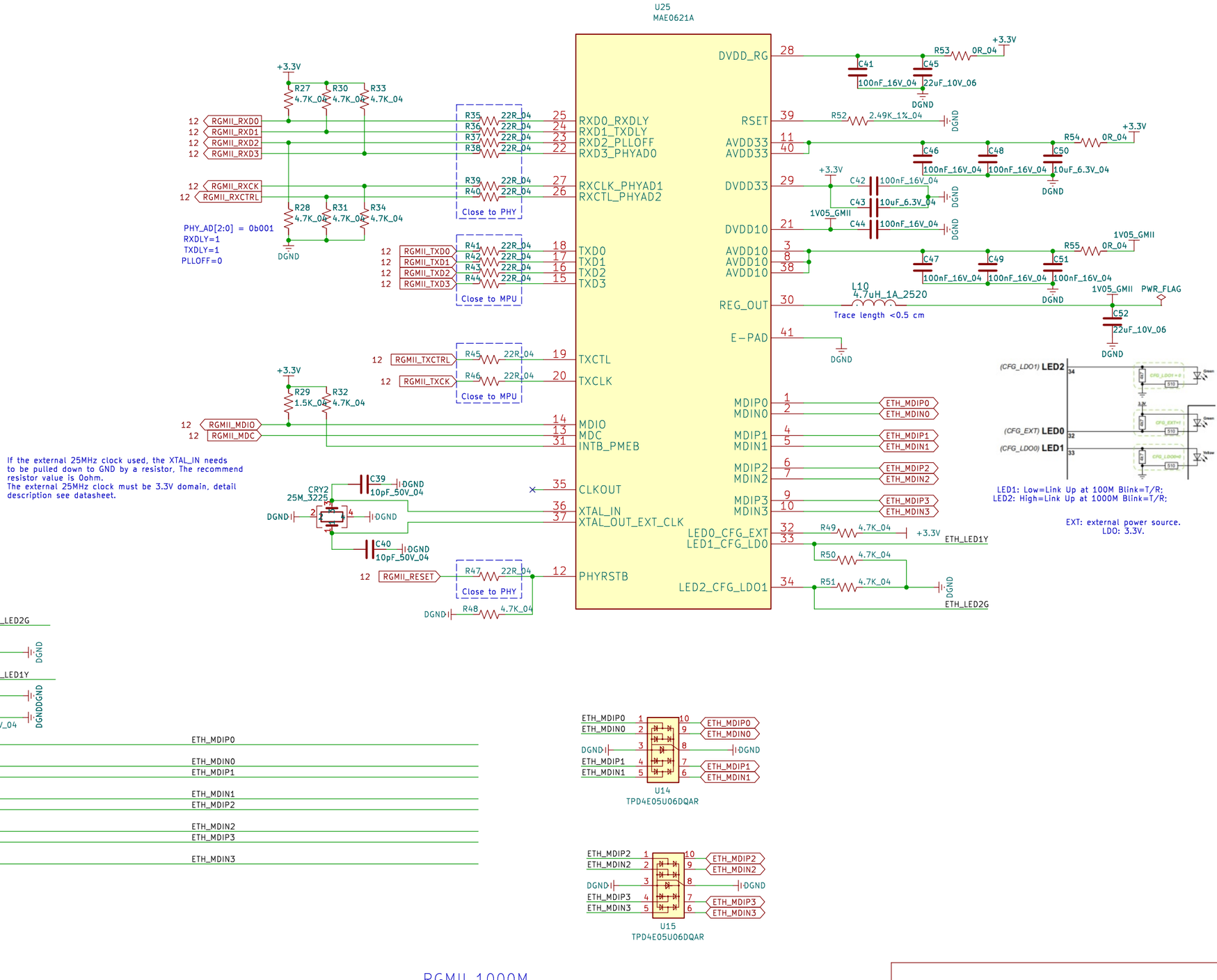


UART

当CC1和CC2都被下拉时，表明连接的是上行端口UFP；当CC1和CC2都未被下拉时，表明连接的是下行端口DFP
当Type-C接口作为下行端口（DFP）与上行端口（UFP）连接时，CC引脚会检测到UFP的下拉，即CC引脚被拉低，这表示UFP设备已连接并请求VBUS电源
DFP:下行端口，可以理解为Host或HUB，负责提供VBUS（电源）和VCONN（配置通道电源），并接收数据。
UFP:上行端口，可以理解为Device，从VBUS中取电，并提供数据。

EBYTE		
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Title: 30012		
Size: A4	Date: 2024-09-23	**Rev: V1.0**
KiCad E.D.A. 8.0.1		Id: 5/13

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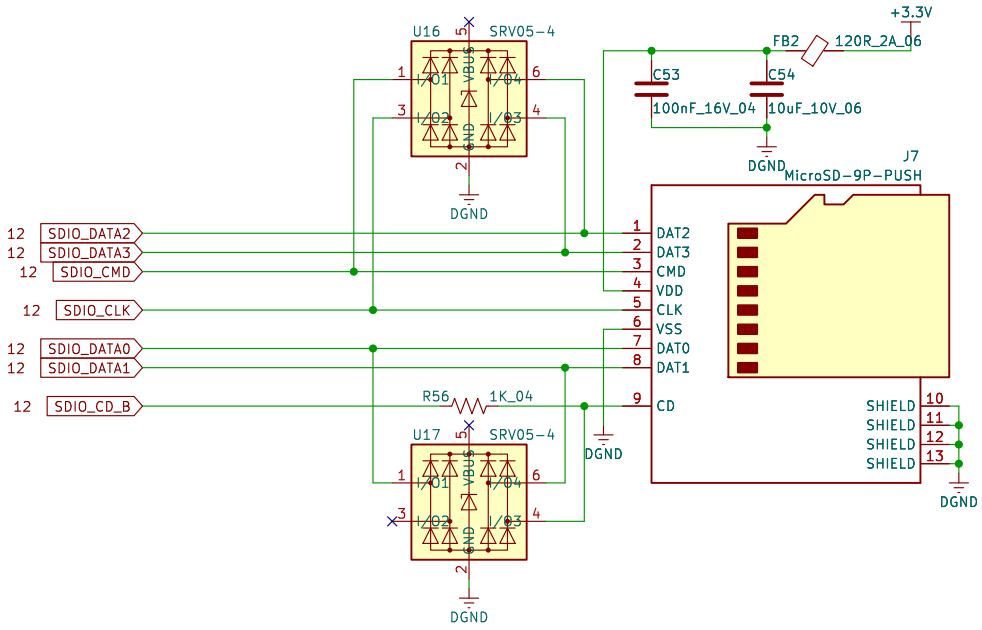
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KiCad E.D.A. 8.0.1 | Id: 6/13

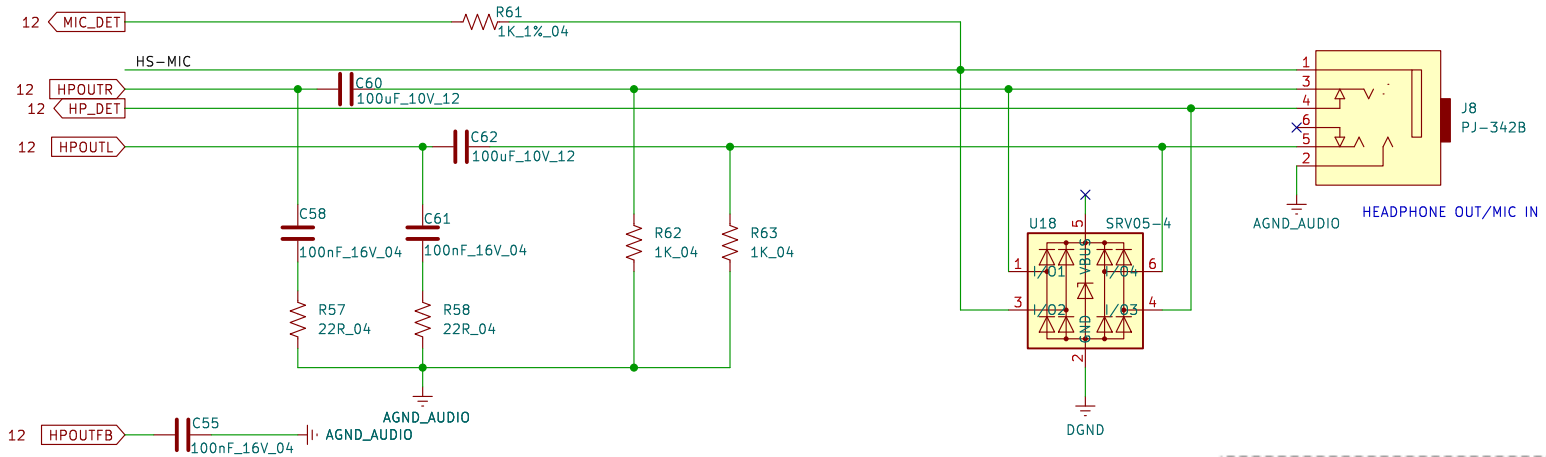
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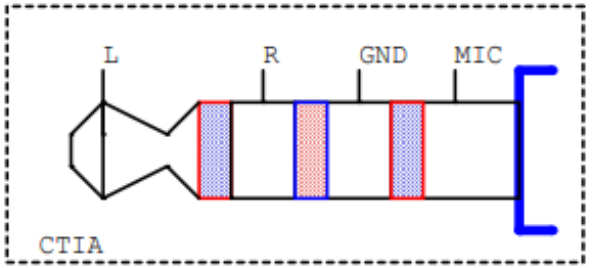


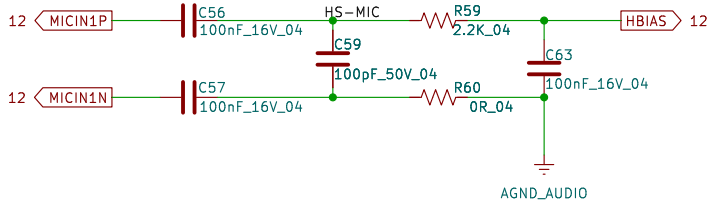
MICRO SD

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KiCad E.D.A. 8.0.1		Id: 7/13

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AUDIO

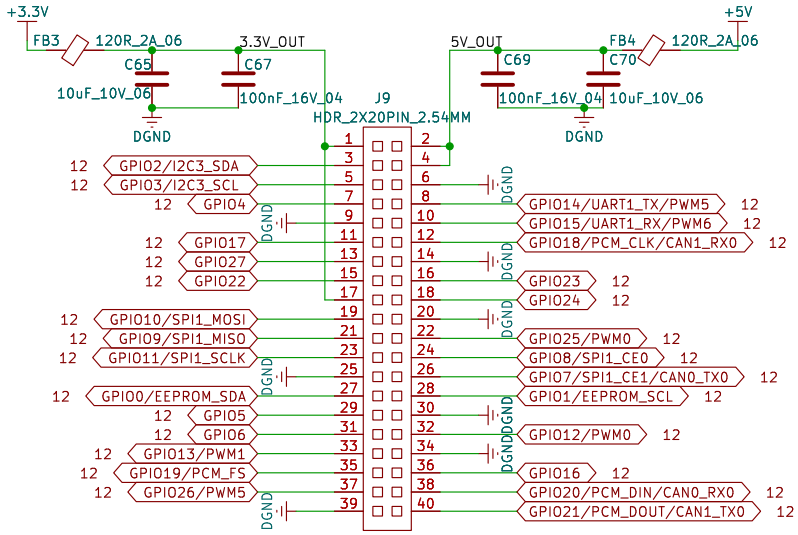
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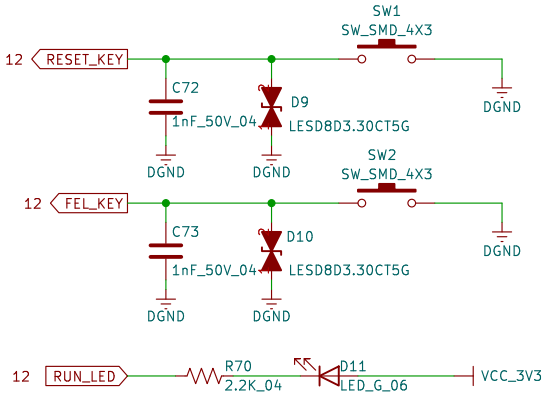
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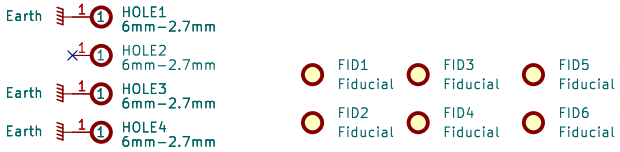
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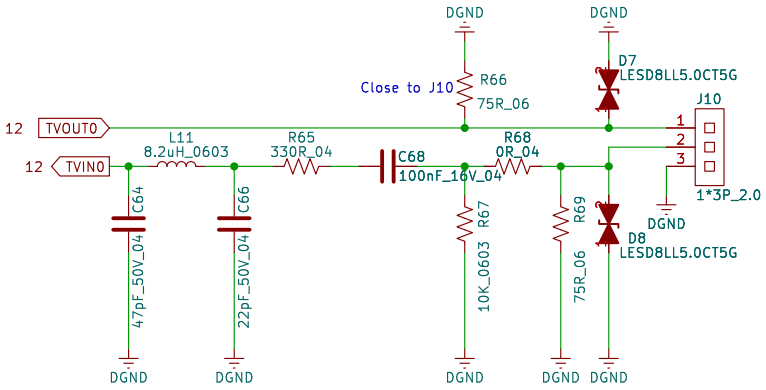


GPIO

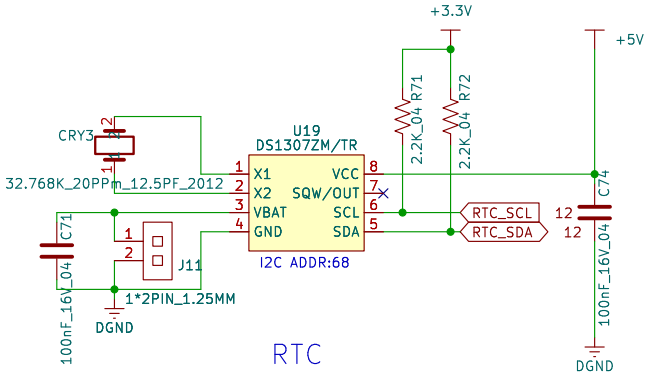


LED&KEY





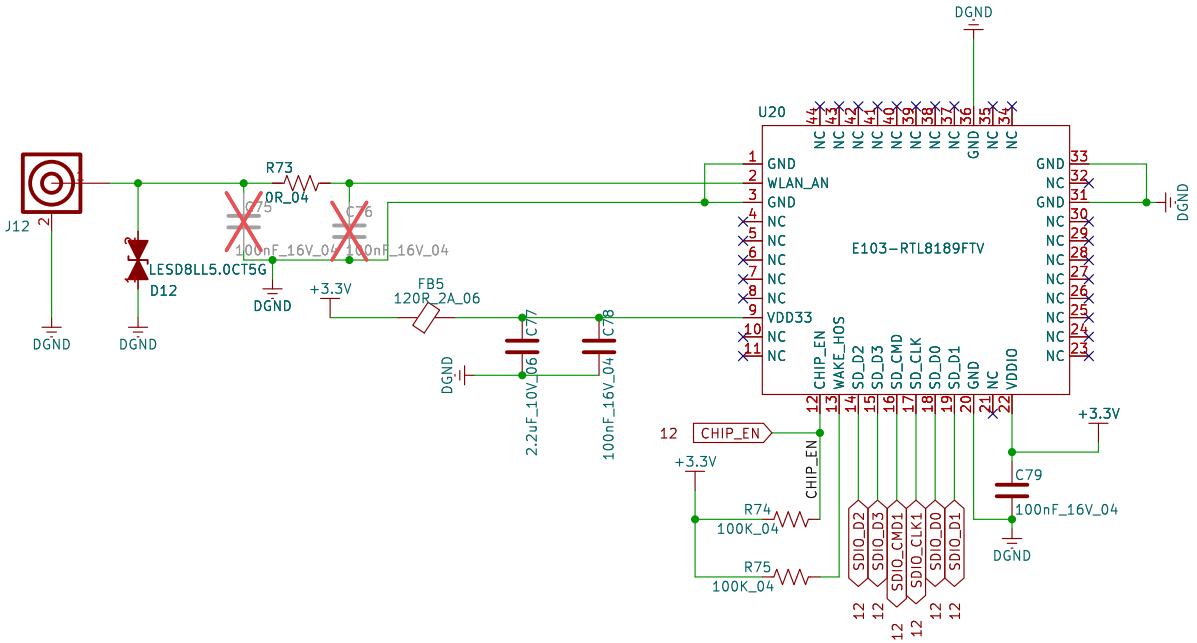
模拟信号接口



RTC

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KiCad E.D.A. 8.0.1		Id: 9/13

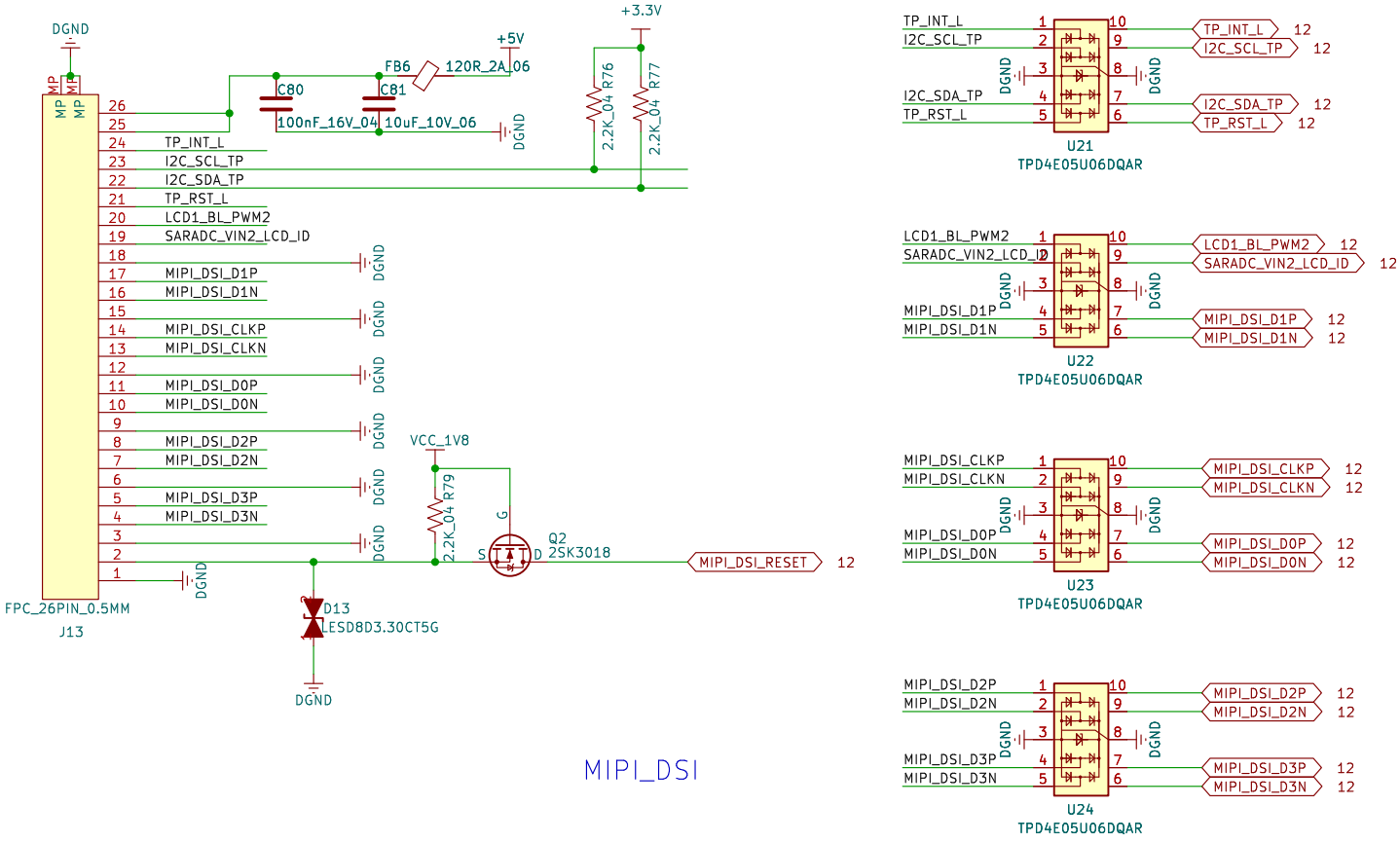
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WIFI

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KiCad E.D.A. 8.0.1		Id: 10/13

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EBYTE

Sheet: /MIPI_DSI/

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KiCad E.D.A. 8.0.1		Id: 11/13

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P1A
ECK30_T13IA5ME8G_I

PIN 1–40

J2/<REFCLK_OUT>/1V8	1		
A18/<FEL>/3V3	2	FEL_KEY	9
N3/<NMI>/1V8	3		
M2/<RESET>/PU/3V3	4	RESET_KEY	9
GND	5	DGND	
E7/<PG17/UART2_TX/TWI3_SCK/PWM7/CLK_FANOUT0/IR_TX/UART0_TX/PG_EINT17>/3V3	6	DB_UART_TX	5
D7/<PG18/UART2_RX/TWI3_SDA/PWM6/CLK_FANOUT1/SPDIF_OUT/UART0_RX/PG_EINT18>/3V3	7	DB_UART_RX	5
D6/<PG13/I2S1_BCLK/TWI0_SDA/RGMII_CLKIN/RMII_RXER/PWM2/LEDC_DO/UART1_RX/PG_EINT13>/3V3	8	USB_OTG_ID	5
B6/<PG9/UART1_CTS/TWI1_SDA/RGMII_RXD3/UART3_RX/PG_EINT9>/3V3	9	GPIO0/EEPROM_SDA	9
A6/<PG8/UART1_RTS/TWI1_SCK/RGMII_RXD2/UART3_TX/PG_EINT8>/3V3	10	GPIO1/EEPROM_SCL	9
A3/<PG2/SDC1_D0/UART3_RTS/RGMII_RXD1/RMII_RXD1/UART4_TX/PG_EINT2>/3V3	11	SDIO_D0	10
B3/<PG1/SDC1_CMD/UART3_RX/RGMII_RXD0/RMII_RXD0/PWM6/PG_EINT1>/3V3	12	SDIO_CMD1	10
B2/<PG0/SDC1_CLK/UART3_TX/RGMII_RXCTRL/RMII_CRS_DV/PWM7/PG_EINT0>/3V3	13	SDIO_CLK1	10
C6/<PG10/PWM3/TWI3_SCK/RGMII_RXCK/CLK_FANOUT0/IR_RX/PG_EINT10>/3V3	14	CHIP_EN	10
D5/<PG12/I2S1_LRCK/TWI0_SCK/RGMII_TXCTRL/RMII_TXEN/CLK_FANOUT2/PWM0/UART1_TX/PG_EINT12>/3V3	15	GPIO12/PWM0	9
C5/<PG7/UART1_RX/TWI2_SDA/RGMII_TXD3/SPDIF_IN/PG_EINT7>/3V3	16	RTC_SDA	9
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A4/<PG4/SDC1_D2/UART5_TX/RGMII_TXD0/RMII_TXD0/PWM5/PG_EINT4>/3V3	19	SDIO_D2	10
C3/<PG3/SDC1_D1/UART3_CTS/RGMII_TXCK/RMII_TXCK/UART4_RX/PG_EINT3>/3V3	20	SDIO_D1	10
F7/<PG16/IR_RX/TCON_TRIG/PWM5/CLK_FANOUT2/SPDIF_IN/LEDC_DO/PG_EINT16>/3V3	21	GPIO26/PWM5	9
D4/<PG11/I2S1_MCLK/TWI3_SDA/EPHY_25M/CLK_FANOUT1/TCON_TRIG/PG_EINT11>/3V3	22	GPIO24	9
E6/<PG14/I2S1_DIN0/TWI2_SCK/MDC/I2S1_DOUT1/SPI0_WP/UART1_RTS/PG_EINT14>/3V3	23	GPIO22	9
F6/<PG15/I2S1_DOUT0/TWI2_SDA/MDIO/I2S1_DIN1/SPI0_HOLD/UART1_CTS/PG_EINT15>/3V3	24	GPIO23	9
GND	25	DGND	
D3/<PF6/SPDIF_OUT/IR_RX/I2S2_MCLK/PWM5/PF_EINT6>/3V3	26	SDIO_CD_B	7
C2/<PF0/SDC0_D1/JTAG_MS/R_JTAG_MS/I2S2_DOUT1/I2S2_DIN0/PF_EINT0>/3V3	27	SDIO_DATA1	7
C1/<PF1/SDC0_D0/JTAG_DI/R_JTAG_DI/I2S2_DOUT0/I2S2_DIN1/PF_EINT1>/3V3	28	SDIO_DATA0	7
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E2/<PF5/SDC0_D2/JTAG_CK/R_JTAG_CK/I2S2_LRCK/PF_EINT5>/3V3	30	SDIO_DATA2	7
D1/<PF3/SDC0_CMD/JTAG_DO/R_JTAG_DO/I2S2_BCLK/PF_EINT3>/3V3	31	SDIO_CMD	7
E3/<PF4/SDC0_D3/UART0_RX/TWI0_SDA/PWM6/IR_TX/PF_EINT4>/3V3	32	SDIO_DATA3	7
GND	33	DGND	
R5/<PE12/TWI2_SCK/NCSI0_FIELD/I2S0_DOUT2/I2S0_DIN2/RGMII_TXD3/PE_EINT12>/3V3	34	RGMII_TXD3	6
R4/<PE13/TWI2_SDA/PWM5/I2S0_DOUT0/I2S0_DIN1/DMIC_DATA3/RGMII_RXD2/PE_EINT13>/3V3	35	RGMII_RXD2	6
T2/<PE4/NCSI0_D0/UART4_TX/TWI2_SCK/CLK_FANOUT2/D_JTAG_MS/R_JTAG_MS/RGMII_TXD0/RMII_TXD0/PE_EINT4>/3V3	36	RGMII_TXD0	6
T3/<PE5/NCSI0_D1/UART4_RX/TWI2_SDA/LEDC_DO/D_JTAG_DI/R_JTAG_DI/RGMII_TXD1/RMII_TXD1/PE_EINT5>/3V3	37	RGMII_TXD1	6
R1/<PE6/NCSI0_D2/UART5_TX/TWI3_SCK/SPDIF_IN/D_JTAG_DO/R_JTAG_DO/RGMII_TXCTRL/RMII_TXEN/PE_EINT6>/3V3	38	RGMII_TXCTRL	6
R2/<PE7/NCSI0_D3/UART5_RX/TWI3_SDA/SPDIF_OUT/D_JTAG_CK/R_JTAG_CK/RGMII_CLKIN/RMII_RXER/PE_EINT7>/3V3	39	RGMII_RESET	6
R3/<PE8/NCSI0_D4/UART1_RTS/PWM2/UART3_TX/JTAG_MS/MDC/PE_EINT8>/3V3	40	RGMII_MDC	6

P1B
ECK30_T13IA5ME8G_I

PIN 41–70

P2/<PE9/NCSI0_D5/UART1_CTS/PWM3/UART3_RX/JTAG_DI/MDIO/PE_EINT9>/3V3	41	RGMII_MDIO	6
P3/<PE10/NCSI0_D6/UART1_TX/PWM4/IR_RX/JTAG_DO/EPHY_25M/PE_EINT10>/3V3	42	GPIO17	9
N1/<PE11/NCSI0_D7/UART1_RX/I2S0_DOUT3/I2S0_DIN3/JTAG_CK/RGMII_TXD2/PE_EINT11>/3V3	43	RGMII_TXD2	6
N4/<PE14/TWI1_SCK/D_JTAG_MS/I2S0_DOUT1/I2S0_DIN0/DMIC_DATA2/RGMII_RXD3/PE_EINT14>/3V3	44	RGMII_RXD3	6
N5/<PE15/TWI1_SDA/D_JTAG_DI/PWM6/I2S0_LRCK/DMIC_DATA1/RGMII_RXCK/PE_EINT15>/3V3	45	RGMII_RXCK	6
N6/<PE16/TWI3_SCK/D_JTAG_DO/PWM7/I2S0_BCLK/DMIC_DATA0/PE_EINT16>/3V3	46	GPIO3/I2C3_SCL	9
M6/<PE17/TWI3_SDA/D_JTAG_CK/IR_TX/I2S0_MCLK/DMIC_CLK/PE_EINT17>/3V3	47	GPIO2/I2C3_SDA	9
V1/<PE0/NCSI0_HSYNC/UART2_RTS/TWI1_SCK/LCD0_HSYNC/RGMII_RXCTRL/RMII_CRS_DV/PE_EINT0>/3V3	48	RGMII_RXCTRL	6
U1/<PE1/NCSI0_VSYNC/UART2_CTS/TWI1_SDA/LCD0_VSYNC/RGMII_RXD0/RMII_RXD0/PE_EINT1>/3V3	49	RGMII_RXD0	6
U2/<PE2/NCSI0_PCLK/UART2_TX/TWI0_SCK/CLK_FANOUT0/UART0_TX/RGMII_RXD1/RMII_RXD1/PE_EINT2>/3V3	50	RGMII_RXD1	6
U3/<PE3/NCSI0_MCLK/UART2_RX/TWI0_SDA/CLK_FANOUT1/UART0_RX/RGMII_TXCK/RMII_TXCK/PE_EINT3>/3V3	51	RGMII_TXCK	6
GND	52	DGND	
G15/<PB8/DMIC_DATA3/PWM5/TWI2_SCK/SPI1_HOLD/DBI_DCX/DBI_WRX/UART0_TX/UART1_TX/PB_EINT8>/3V3	53	GPIO14/UART1_TX/PWM5	9
G16/<PB9/DMIC_DATA2/PWM6/TWI2_SDA/SPI1_MISO/DBI_SDI/DBI_TE/DBI_DCX/UART0_RX/UART1_RX/PB_EINT9>/3V3	54	GPIO15/UART1_RX/PWM6	9
F17/<PB10/DMIC_DATA1/PWM7/TWI0_SCK/SPI1_MOSI/DBI_SDO/CLK_FANOUT0/UART1_RTS/PB_EINT10>/3V3	55	I2C_SCL_TP	11
F15/<PB11/DMIC_DATA0/PWM2/TWI0_SDA/SPI1_CLK/DBI_SCLK/CLK_FANOUT1/UART1_CTS/PB_EINT11>/3V3	56	I2C_SDA_TP	11
F16/<PB12/DMIC_CLK/PWM0/SPDIF_IN/SPI1_CS/DBI_CSX/CLK_FANOUT2/IR_RX/PB_EINT12>/3V3	57	RUN_LED	9
M16/<PB2/LCD0_D0/I2S2_DOUT2/TWI0_SDA/I2S2_DIN2/LCD0_D18/UART4_TX/CAN0_TX0/PB_EINT2>/3V3	58	GPIO7/SPI1_CE1/CAN0_TX0	9
M15/<PB3/LCD0_D1/I2S2_DOUT1/TWI0_SCK/I2S2_DIN0/LCD0_D19/UART4_RX/CAN0_RX0/PB_EINT3>/3V3	59	GPIO20/PCM_DIN/CAN0_RX0	9
K16/<PB4/LCD0_D8/I2S2_DOUT0/TWI1_SCK/I2S2_DIN1/LCD0_D20/UART5_TX/CAN1_TX0/PB_EINT4>/3V3	60	GPIO21/PCM_DOUT/CAN1_TX0	9
K15/<PB5/LCD0_D9/I2S2_BCLK/TWI1_SDA/PWM0/LCD0_D21/UART5_RX/CAN1_RX0/PB_EINT5>/3V3	61	GPIO18/PCM_CLK/CAN1_RX0	9
K17/<PB6/LCD0_D16/I2S2_LRCK/TWI3_SCK/PWM1/LCD0_D22/UART3_TX/CPUBIST0/PB_EINT6>/3V3	62	GPIO19/PCM_FS	9
J15/<PB7/LCD0_D17/I2S2_MCLK/TWI3_SDA/IR_RX/LCD0_D23/UART3_RX/CPUBIST1/PB_EINT7>/3V3	63	GPIO4	9
J16/<PB0/PWM3/IR_TX/TWI2_SCK/SPI1_WP/DBI_TE/UART0_TX/UART2_TX/SPDIF_OUT/PB_EINT0>/3V3	64	GPIO6	9
J17/<PB1/PWM4/I2S2_DOUT3/TWI2_SDA/I2S2_DIN3/UART0_RX/UART2_RX/IR_RX/PB_EINT1>/3V3	65	GPIO5	9
GND	66	DGND	
W19/<PD0/LCD0_D2/LVDS0_V0P/DSI_D0P/TWI0_SCK/PD_EINT0>/3V3	67	MIPI_DSI_D0P	11
V20/<PD1/LCD0_D3/LVDS0_V0N/DSI_D0N/UART2_TX/PD_EINT1>/3V3	68	MIPI_DSI_D0N	11
V19/<PD2/LCD0_D4/LVDS0_V1P/DSI_D1P/UART2_RX/PD_EINT2>/3V3	69	MIPI_DSI_D1P	11
U20/<PD3/LCD0_D5/LVDS0_V1N/DSI_D1N/UART2_RTS/PD_EINT3>/3V3	70	MIPI_DSI_D1N	11

P1C
ECK30_T13IA5ME8G_I

PIN 71–110

GND	71	DGND	
U19/<PD4/LCD0_D6/LVDS0_V2P/DSI_CKP/UART2_CTS/PD_EINT4>/3V3	72	MIPI_DSI_CLKP	11
U18/<PD5/LCD0_D7/LVDS0_V2N/DSI_CKN/UART5_TX/PD_EINT5>/3V3	73	MIPI_DSI_CLKN	11
T19/<PD6/LCD0_D10/LVDS0_CKP/DSI_D2P/UART5_RX/PD_EINT6>/3V3	74	MIPI_DSI_D2P	11
T18/<PD7/LCD0_D11/LVDS0_CKN/DSI_D2N/UART4_TX/PD_EINT7>/3V3	75	MIPI_DSI_D2N	11
R20/<PD8/LCD0_D12/LVDS0_V3P/DSI_D3P/UART4_RX/PD_EINT8>/3V3	76	MIPI_DSI_D3P	11
R19/<PD9/LCD0_D13/LVDS0_V3N/DSI_D3N/PWM6/PD_EINT9>/3V3	77	MIPI_DSI_D3N	11
GND	78	DGND	
T17/<PD10/LCD0_D14/LVDS1_V0P/SPI1_CS/DBI_CSX/UART3_TX/PD_EINT10>/3V3	79	GPIO8/SPI1_CE0	9
R17/<PD11/LCD0_D15/LVDS1_V0N/SPI1_CLK/DBI_SCLK/UART3_RX/PD_EINT11>/3V3	80	GPIO11/SPI1_SCLK	9
P19/<PD12/LCD0_D18/LVDS1_V1P/SPI1_MOSI/DBI_SDO/TWI0_SDA/PD_EINT12>/3V3	81	GPIO10/SPI1_MOSI	9
P18/<PD13/LCD0_D19/LVDS1_V1N/SPI1_MISO/DBI_SDI/DBI_TE/DBI_DCX/UART3_RTS/PD_EINT13>/3V3	82	GPIO9/SPI1_MISO	9
N17/<PD14/LCD0_D20/LVDS1_V2P/SPI1_HOLD/DBI_DCX/DBI_WRX/UART3_CTS/PD_EINT14>/3V3	83	GPIO27	9
N16/<PD15/LCD0_D21/LVDS1_V2N/SPI1_WP/DBI_TE/IR_RX/PD_EINT15>/3V3	84	GPIO16	9
GND	85	DGND	
N20/<PD16/LCD0_D22/LVDS1_CKP/DMIC_DATA3/PWM0/PD_EINT16>/3V3	86	GPIO25/PWM0	9
N19/<PD17/LCD0_D23/LVDS1_CKN/DMIC_DATA2/PWM1/PD_EINT17>/3V3	87	GPIO13/PWM1	9
M19/<PD18/LCD0_CLK/LVDS1_V3P/DMIC_DATA1/PWM2/PD_EINT18>/3V3	88	LCD1_BL_PWM2	11
M18/<PD19/LCD0_DE/LVDS1_V3N/DMIC_DATA0/PWM3/PD_EINT19>/3V3	89	MIPI_DSI_RESET	11
V18/<PD21/LCD0_VSYNC/TWI2_SDA/UART1_TX/PWM5/PD_EINT21>/3V3	90	TP_INT_L	11
W18/<PD20/LCD0_HSYNC/TWI2_SCK/DMIC_CLK/PWM4/PD_EINT20>/3V3	91	TP_RST_L	11
GND	92	DGND	
C17/<FMINR>	93		
B17/<FMINL>	94		
C16/<LINEINR>	95		
B16/<LINEINL>	96		
AGND_AUDIO	97	AGND_AUDIO	
E16/<MBIAS>	98		
A17/<MIC_DET>	99	MIC_DET	8
D20/<MICIN1P>	100	MICIN1P	8
D19/<MICIN1N>	101	MICIN1N	8
E15/<MICIN2P>	102		
D15/<MICIN2N>	103		
D17/<MICIN3P>	104		
D16/<MICIN3N>	105		
AGND_AUDIO	106	AGND_AUDIO	
B14/<LINEOUTRP>	107		
C14/<LINEOUTRN>	108		
B15/<LINEOUTLP>	109		
C15/<LINEOUTLN>	110		

P1D
ECK30_T13IA5ME8G_I

PIN 111–140

E17/<HBIAS>	111	HBIAS	8
A13/<HP_DET>	112	HP_DET	8
D13/<HPOUTR>	113	HPOUTR	8
E13/<HPOUTFB>	114	HPOUTFB	8
F13/<HPOUTL>	115	HPOUTL	8
GND	116	DGND	
E19/<TVOUT0>	117	TVOUT0	9
C9/<TVIN1>	118		
B9/<TVIN0>	119	TVIN0	9
GND	120	DGND	
INPUT:0–1.266V — B12/<LRADC>	121		
INPUT:0–1.8V — C13/<GPADC0>	122		
B13/<GPADC1>	123	R78 510R_04 — SARADC_VIN2_LCD_ID	11
C12/<TP_X1>	124		
A11/<TP_X2>	125		
B11/<TP_Y1>	126		
C11/<TP_Y2>	127		
GND	128	DGND	
A8/<USB1_DP>	129	USB_HOST_DP	4
B8/<USB1_DM>	130	USB_HOST_DN	4
GND	131	DGND	
B7/<USB0_DP>	132	USB_OTG_DP	5
C7/<USB0_DM>	133	USB_OTG_DN	5
GND	134	DGND	
SOM to EVM — VDD_3V3_SOM	135	VCC_3V3	
SOM to EVM — VDD_1V8_SOM	136	VCC_1V8	
GND	137	DGND	
GND	138	DGND	
EVM to SOM — VDD_5V_SOM	139	FB7 120R_2A_06 — +5V	
EVM to SOM — VDD_5V_SOM	140		

EBYTE

Sheet: /SOM CONNECTOR/		
File: SOM CONNECTOR.kicad_sch		
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