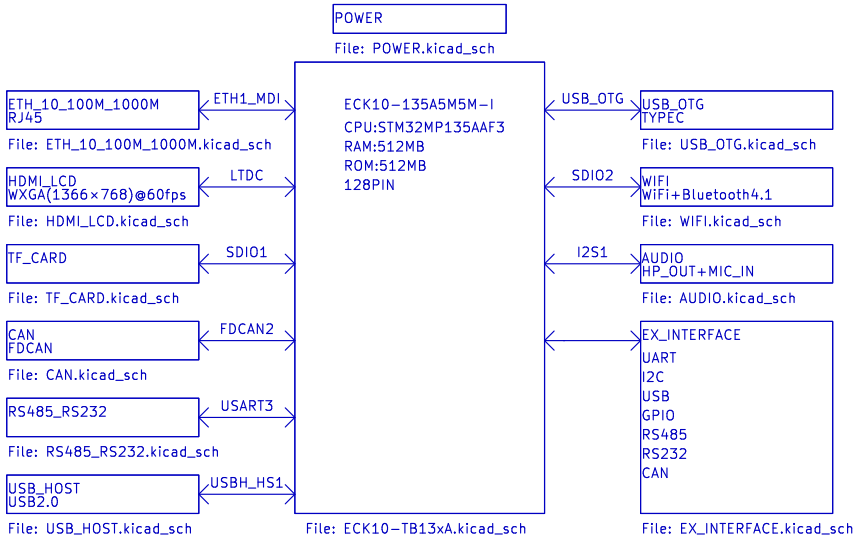
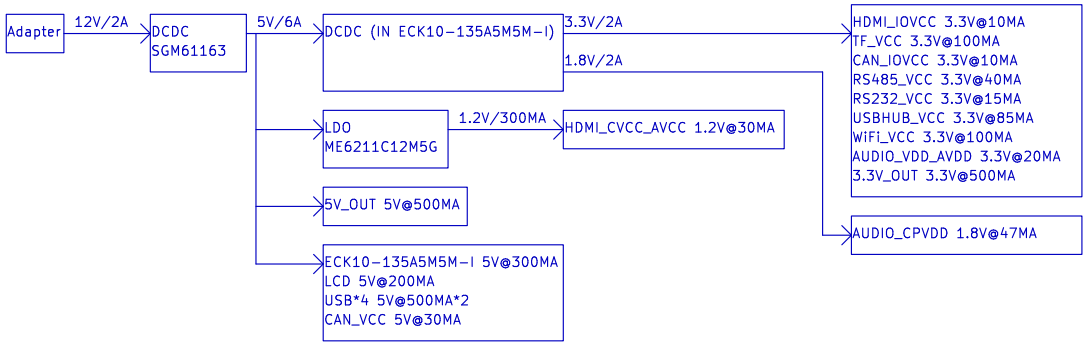




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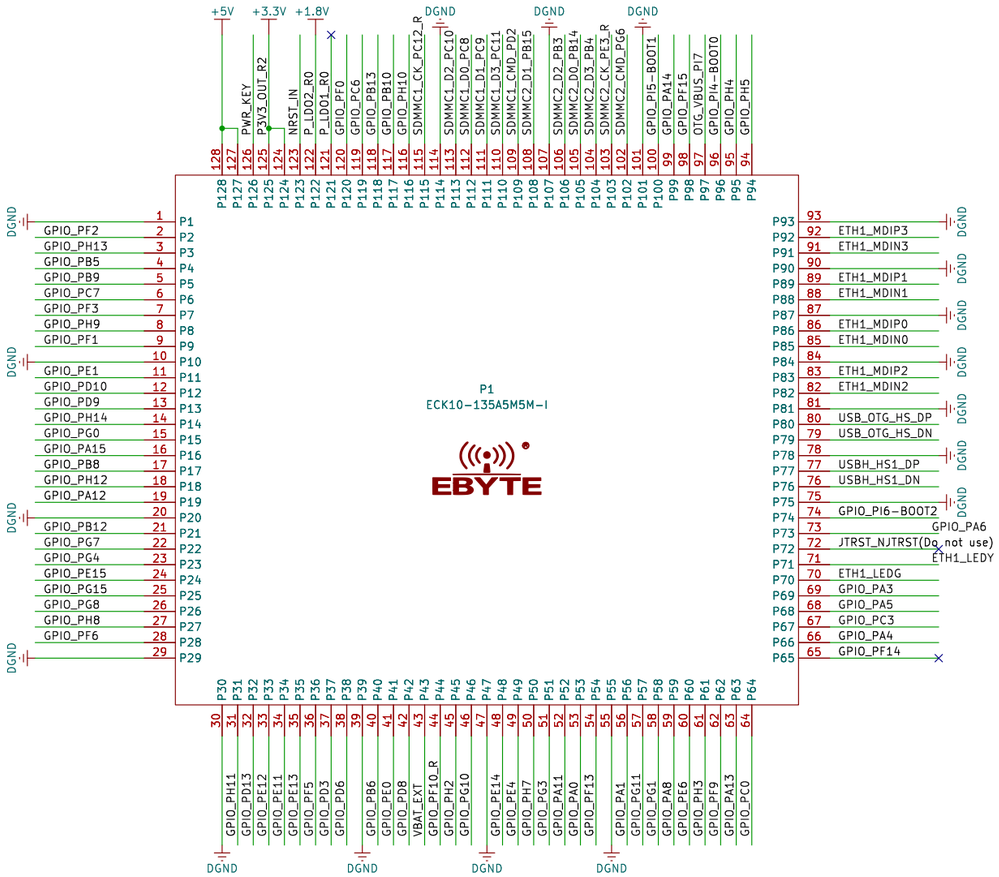
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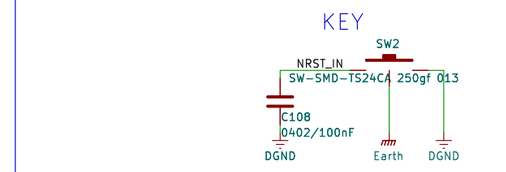
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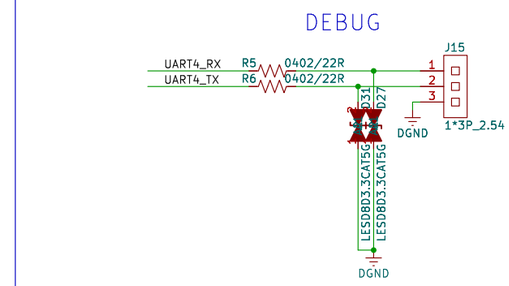
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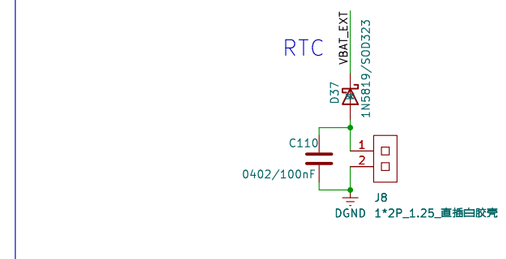
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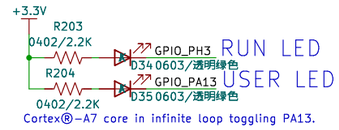
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6	SDMMC1_D0	SDMMC1_D0	SDMMC1_D0_PC8
6	SDMMC1_D1	SDMMC1_D1	SDMMC1_D1_PC9
6	SDMMC1_D2	SDMMC1_D2	SDMMC1_D2_PC10
6	SDMMC1_D3	SDMMC1_D3	SDMMC1_D3_PC11
11	SDMMC2_CK	SDMMC2_CK	SDMMC2_CK_PE3_R
11	SDMMC2_CMD	SDMMC2_CMD	SDMMC2_CMD_PG6
11	SDMMC2_D0	SDMMC2_D0	SDMMC2_D0_PB14
11	SDMMC2_D1	SDMMC2_D1	SDMMC2_D1_PB15
11	SDMMC2_D2	SDMMC2_D2	SDMMC2_D2_PB3
11	SDMMC2_D3	SDMMC2_D3	SDMMC2_D3_PB4
11	BT_nRST	BT_nRST	GPIO_PH4
11	BT_WAKE_R	BT_WAKE_R	GPIO_PF15
11	BT_HOST_WAKE_R	BT_HOST_WAKE_R	GPIO_PA14
11	WL_REG_ON	WL_REG_ON	GPIO_PH5
11	WL_HOST_WAKE	WL_HOST_WAKE	GPIO_PA4
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	UART4_TX	UART4_TX	GPIO_PD6
13	UART5_RX	UART5_RX	GPIO_PF13
13	UART5_TX	UART5_TX	GPIO_PA0
11	UART8_TX	UART8_TX	GPIO_PE4
11	UART8_RX	UART8_RX	GPIO_PF9
11	UART8_RTS	UART8_RTS	GPIO_PE14
11	UART8_CTS	UART8_CTS	GPIO_PG10
8	USART3_TX	USART3_TX	GPIO_PG11
8	USART3_RX	USART3_RX	GPIO_PG4
8	USART3_RTS	USART3_RTS	GPIO_PG8
8	USART3_CTS	USART3_CTS	GPIO_PH10
7	FDCAN2_RX	FDCAN2_RX	GPIO_PG3
7	FDCAN2_TX	FDCAN2_TX	GPIO_PG1
5,13	I2C1_SCL	I2C1_SCL	GPIO_PB8
5,13	I2C1_SDA	I2C1_SDA	GPIO_PD3
12,13	I2C3_SCL	I2C3_SCL	GPIO_PH12
12,13	I2C3_SDA	I2C3_SDA	GPIO_PH7
12	I2S1_CK	I2S1_CK	GPIO_PC3
12	I2S1_MCK	I2S1_MCK	GPIO_PC0
12	I2S1_SDI	I2S1_SDI	GPIO_PA6
12	I2S1_SDO	I2S1_SDO	GPIO_PA3
12	I2S1_WS	I2S1_WS	GPIO_PA5
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5	LTDC_R1	LTDC_R1	GPIO_PG7
5	LTDC_R2	LTDC_R2	GPIO_PH8
5	LTDC_R3	LTDC_R3	GPIO_PB12
5	LTDC_R4	LTDC_R4	GPIO_PE1
5	LTDC_R5	LTDC_R5	GPIO_PF5
5	LTDC_R6	LTDC_R6	GPIO_PE13
5	LTDC_R7	LTDC_R7	GPIO_PF6
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5	LTDC_B3	LTDC_B3	GPIO_PF2
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5	LTDC_B6	LTDC_B6	GPIO_PB6
5	LTDC_B7	LTDC_B7	GPIO_PE15
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5	LTDC_HSYNC	LTDC_HSYNC	GPIO_PC6
5	LTDC_VSYNC	LTDC_VSYNC	GPIO_PE12
5	HDMI_NRST	HDMI_NRST	GPIO_PG15
5	HDMI_INT	HDMI_INT	GPIO_PA12
5	LCD_PWM	LCD_PWM	GPIO_PH9
	GPIO_PH11		GPIO_PH11
5	LCD_RST	LCD_RST	GPIO_PF10_R
5	TP_INT	TP_INT	GPIO_PH2
10	OTG_VBUS_PI7	OTG_VBUS_PI7	
10	USB_OTG_HS_DP	USB_OTG_HS_DP	
10	USB_OTG_HS_DN	USB_OTG_HS_DN	
9	USBH_HS1_DP	USBH_HS1_DP	
9	USBH_HS1_DN	USBH_HS1_DN	
4	ETH1_MDIP3	ETH1_MDIP3	
4	ETH1_MDIN3	ETH1_MDIN3	
4	ETH1_MDIP1	ETH1_MDIP1	
4	ETH1_MDIN1	ETH1_MDIN1	
4	ETH1_MDIP0	ETH1_MDIP0	
4	ETH1_MDIN0	ETH1_MDIN0	
4	ETH1_MDIP2	ETH1_MDIP2	
4	ETH1_MDIN2	ETH1_MDIN2	
4	ETH1_LEDY	ETH1_LEDY	
4	ETH1_LEDG	ETH1_LEDG	
6	GPIO_PI6-BOOT2	GPIO_PI6-BOOT2	
6	GPIO_PI5-BOOT1	GPIO_PI5-BOOT1	
6	GPIO_PI4-BOOT0	GPIO_PI4-BOOT0	
13	GPIO_PA11	GPIO_PA11	
13	GPIO_PA1	GPIO_PA1	
13	GPIO_PA8	GPIO_PA8	
13	GPIO_PE6	GPIO_PE6	
13	NRST_IN	NRST_IN	
13	PWR_KEY	PWR_KEY	

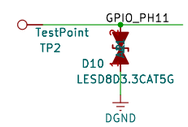


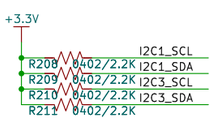












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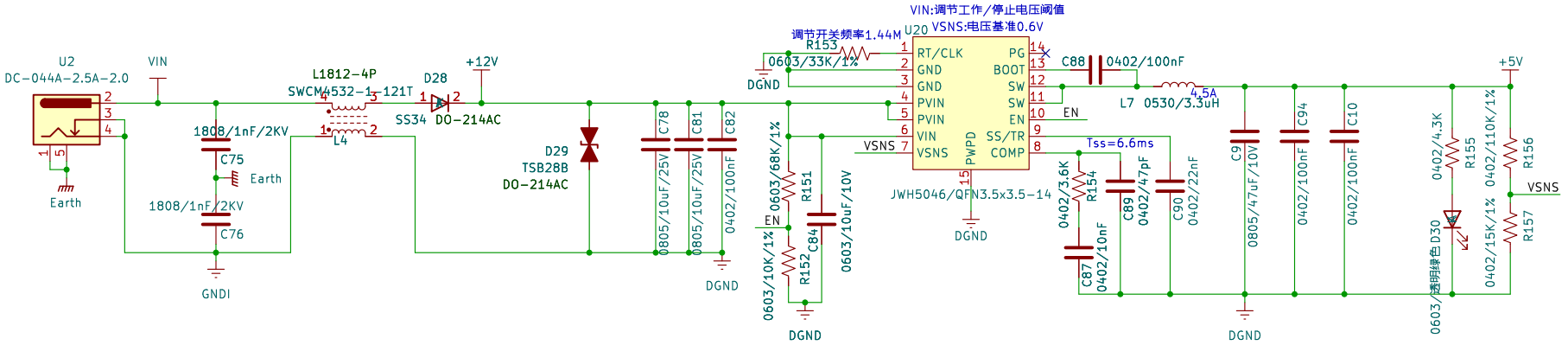
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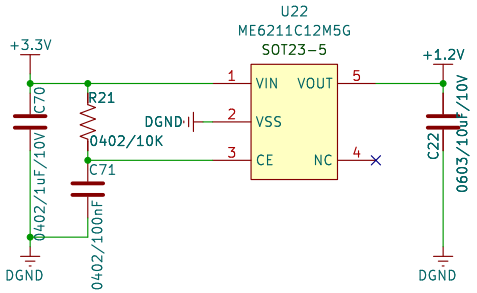
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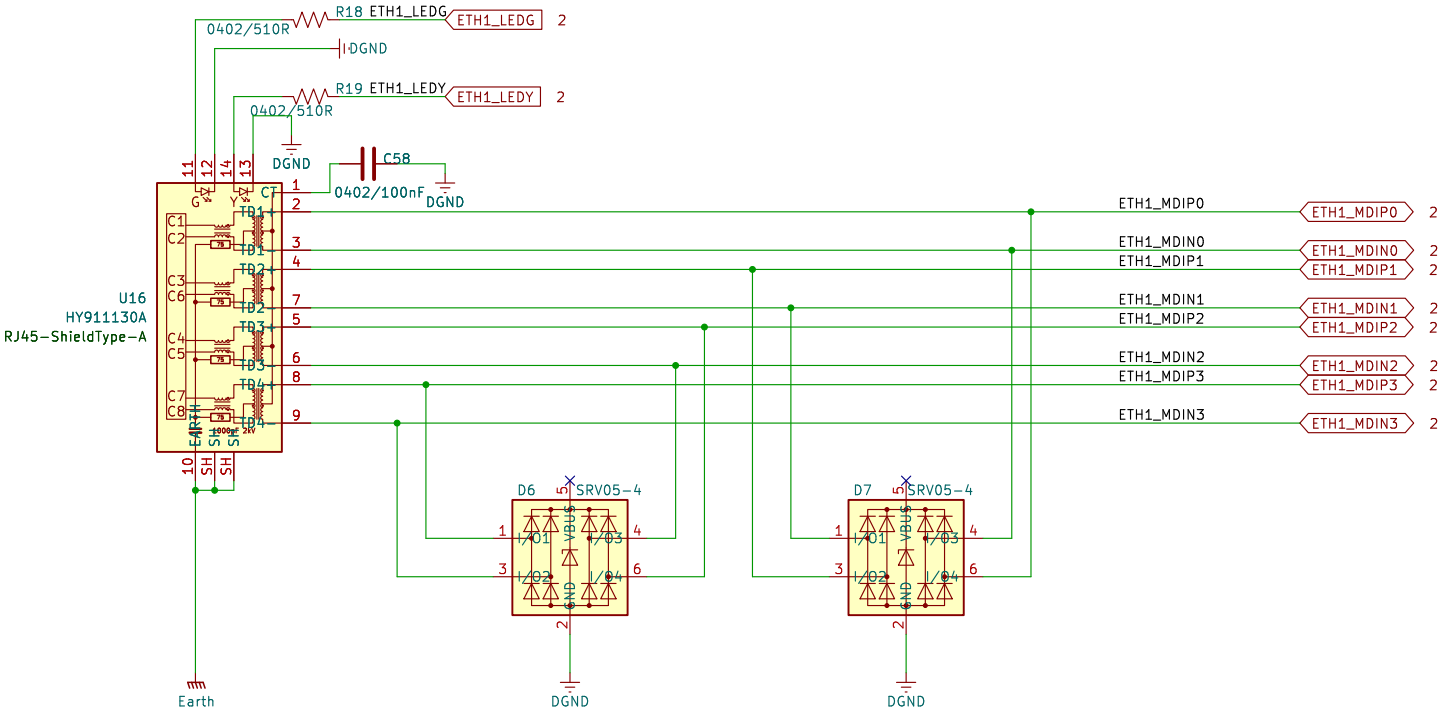
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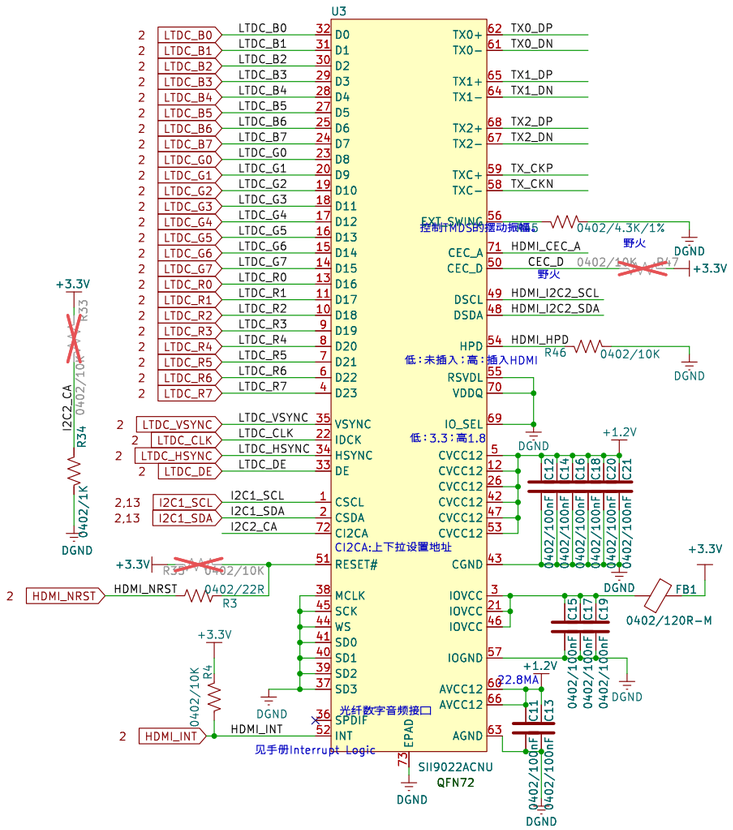
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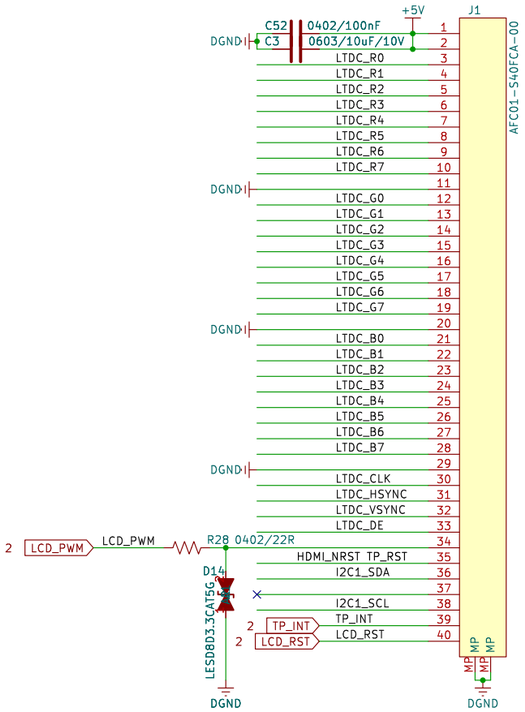
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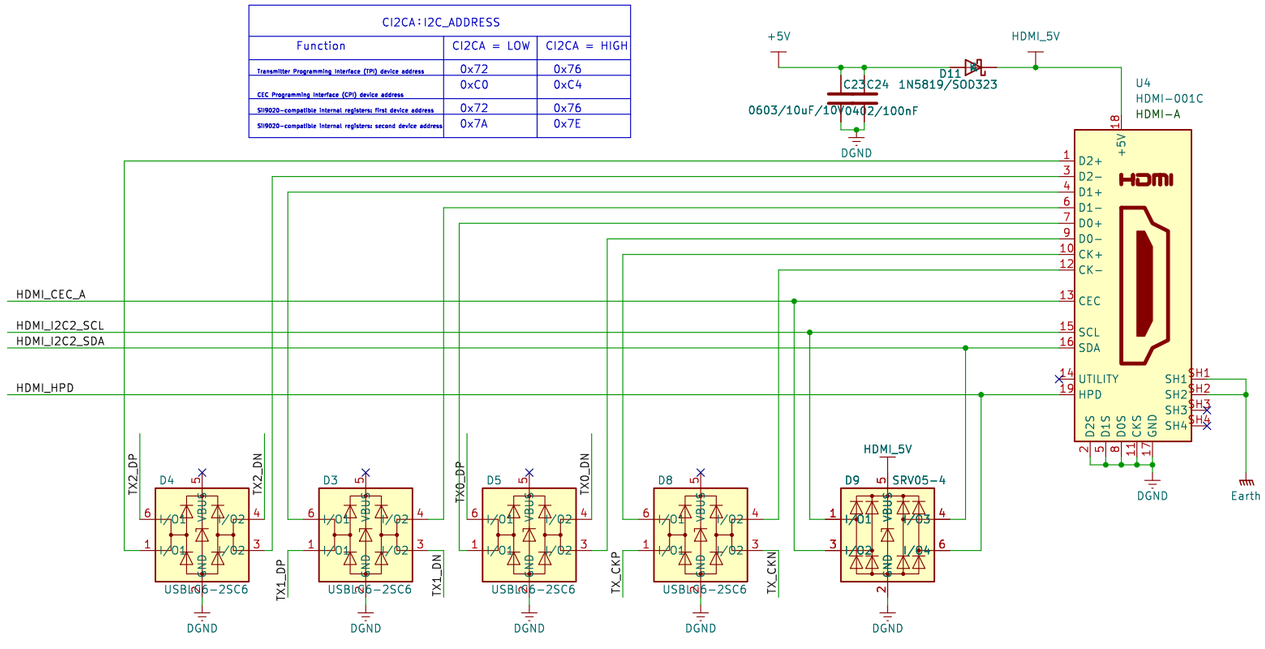
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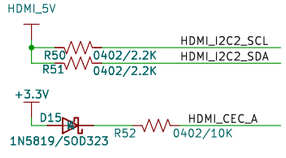
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CI2CA:I2C_ADDRESS		
Function	CI2CA = LOW	CI2CA = HIGH
Transmitter Programming Interface (TPI) device address	0x72	0x76
CEC Programming Interface (CPI) device address	0xC0	0xC4
SII9020-compatible internal registers: first device address	0x72	0x76
SII9020-compatible internal registers: second device address	0x7A	0x7E





HDMI_5V
PWR_FLAG

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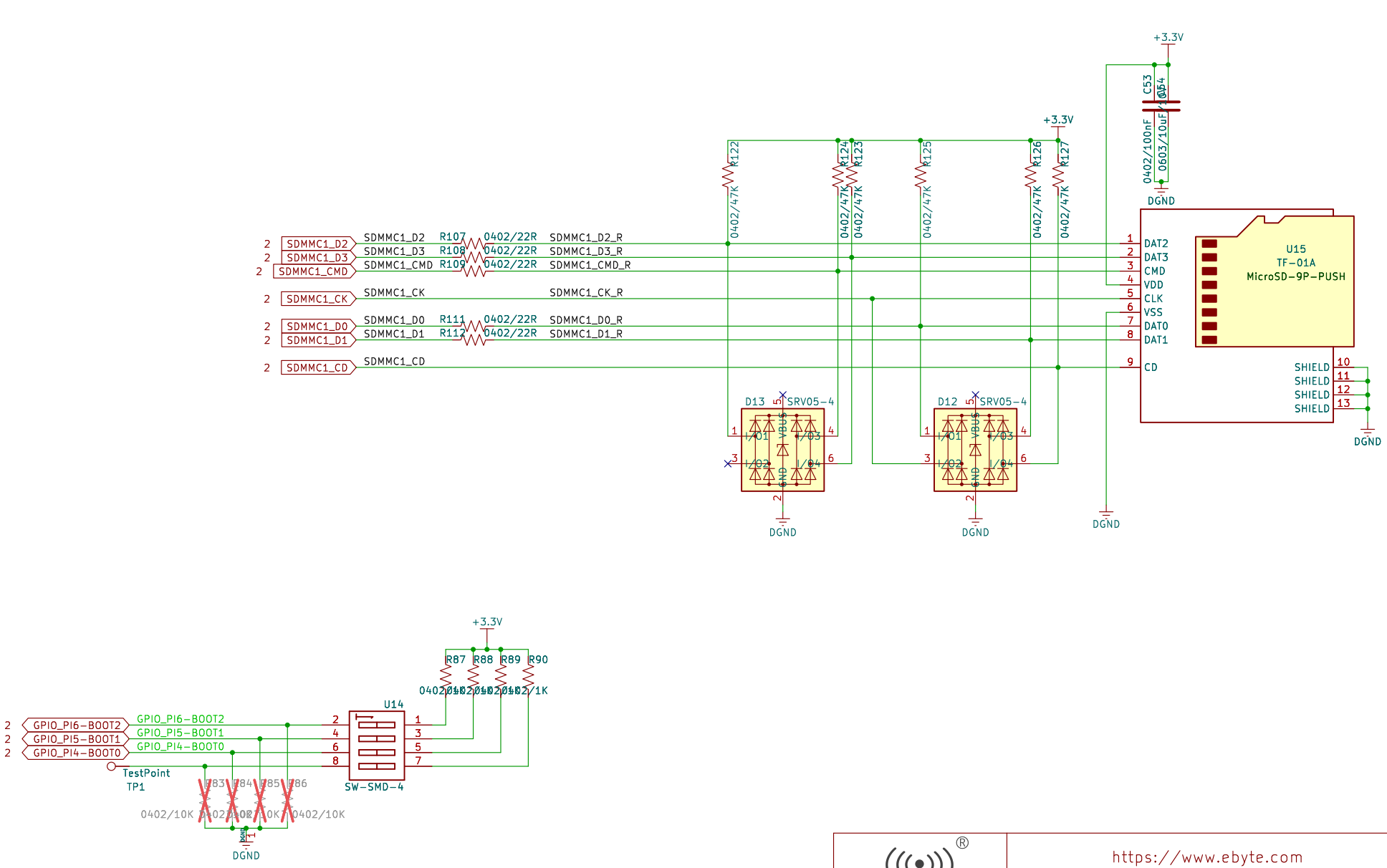
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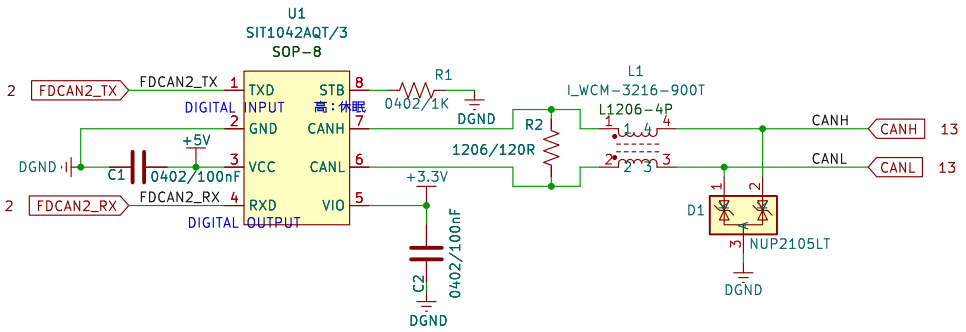
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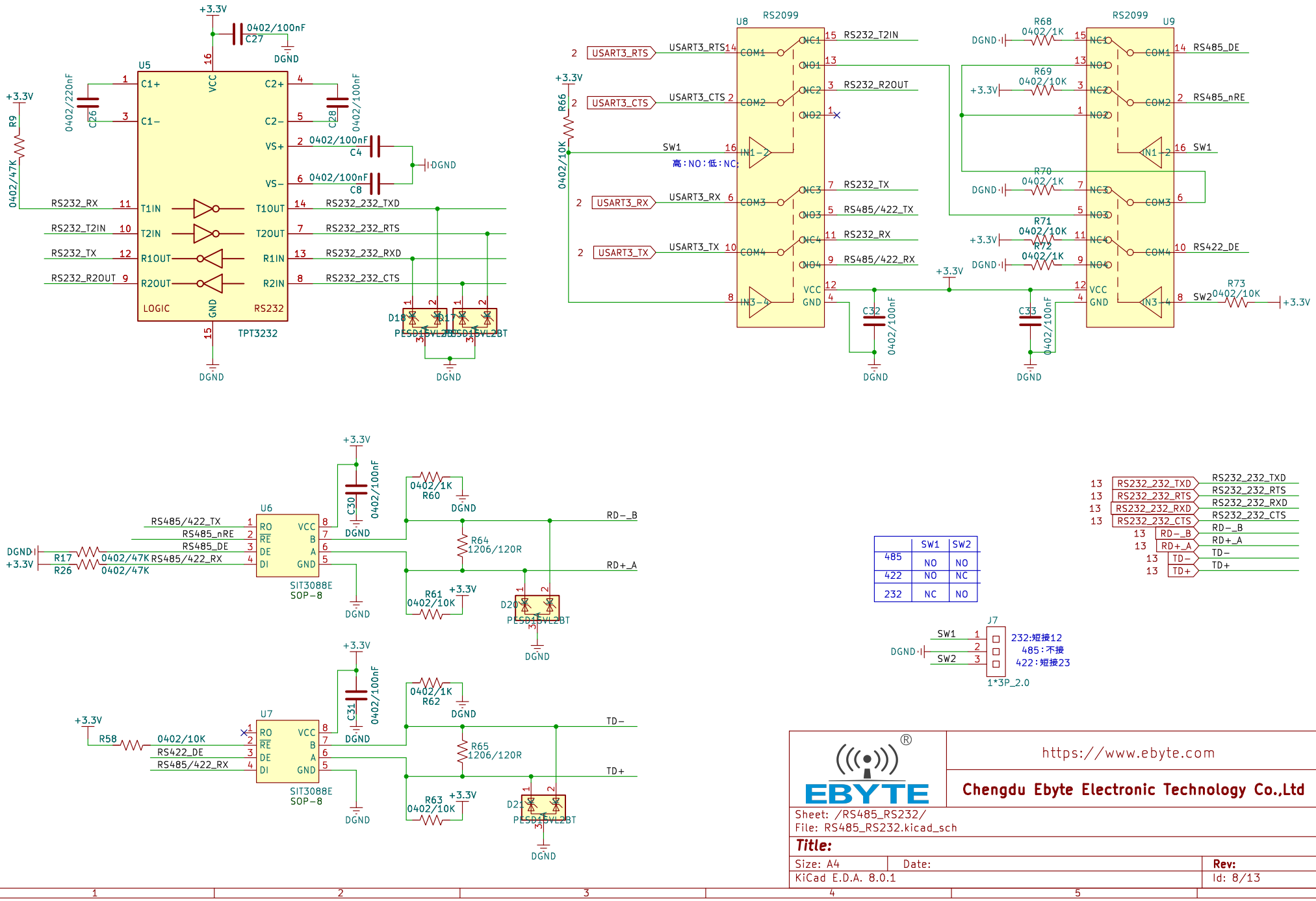


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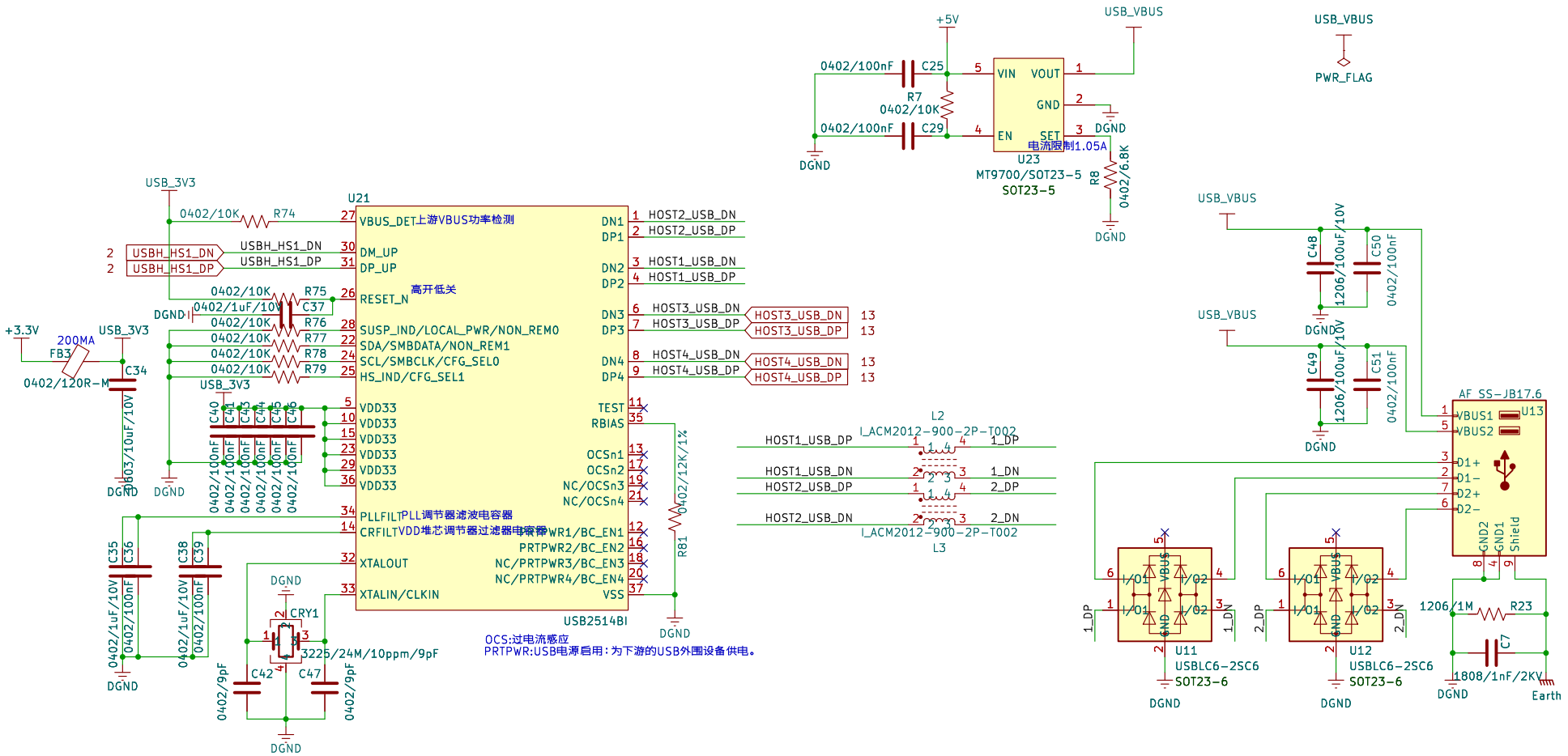
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	SW1	SW2
485	NO	NO
422	NO	NC
232	NC	NO

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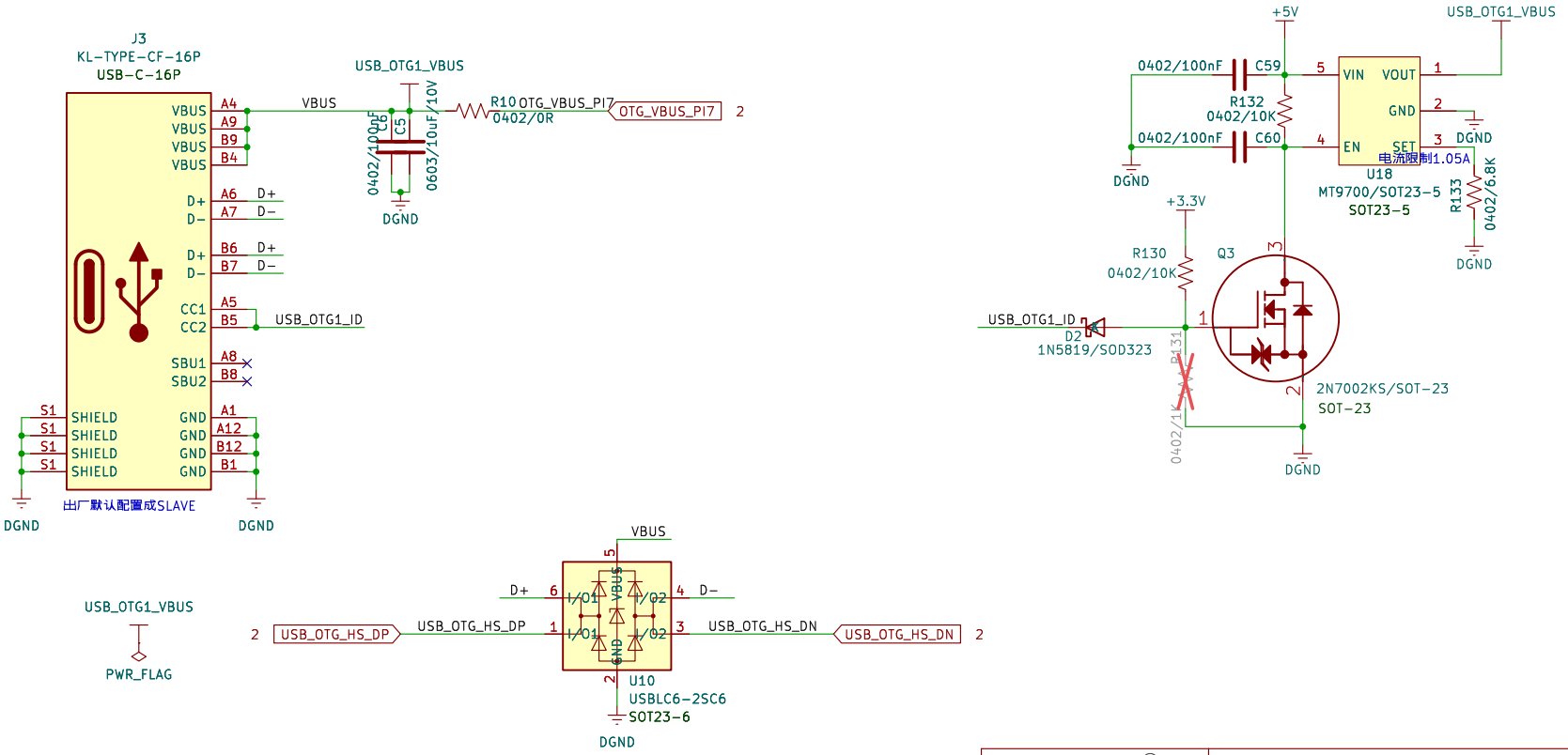
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当CC1和CC2都被下拉时，表明连接的是上行端口UFP；当CC1和CC2都未被下拉时，表明连接的是下行端口DFP
当USB Type-C接口作为下行端口（DFP）与上行端口（UFP）连接时，CC引脚会检测到UFP的下拉，即CC引脚被拉低，这表示UFP设备已连接并请求VBUS电源

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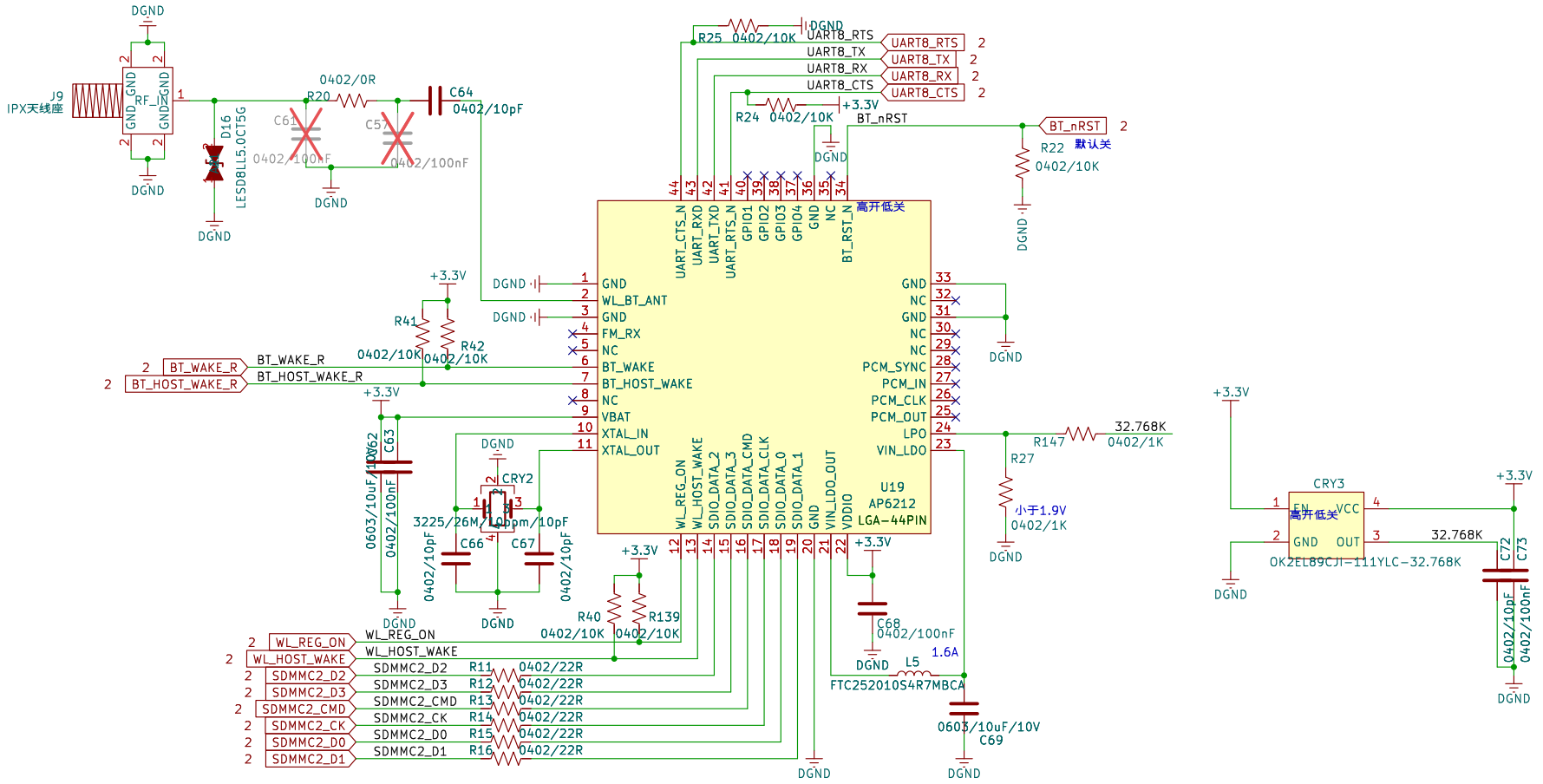
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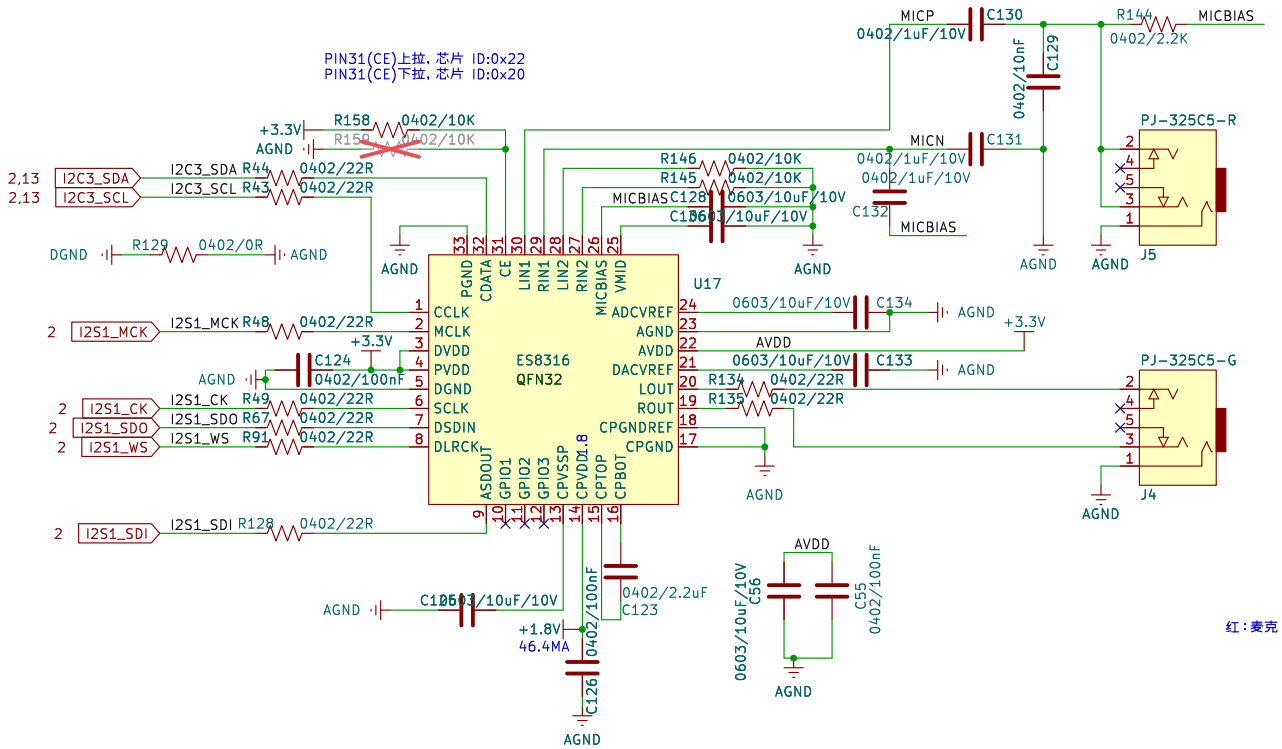


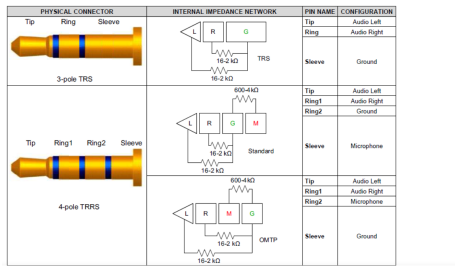
WL_REG_ON　WIFI（低电平）复位脚，内部调节器电源，启动时序相关
WL_HOST_WAKE　无线局域网唤醒主机
BT_nRST　BT（低电平）复位脚，上电时序有关
BT_WAKE_R　主机唤醒蓝牙设备
BT_HOST_WAKE_R　蓝牙设备，以唤醒主机

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PWR_FLAG

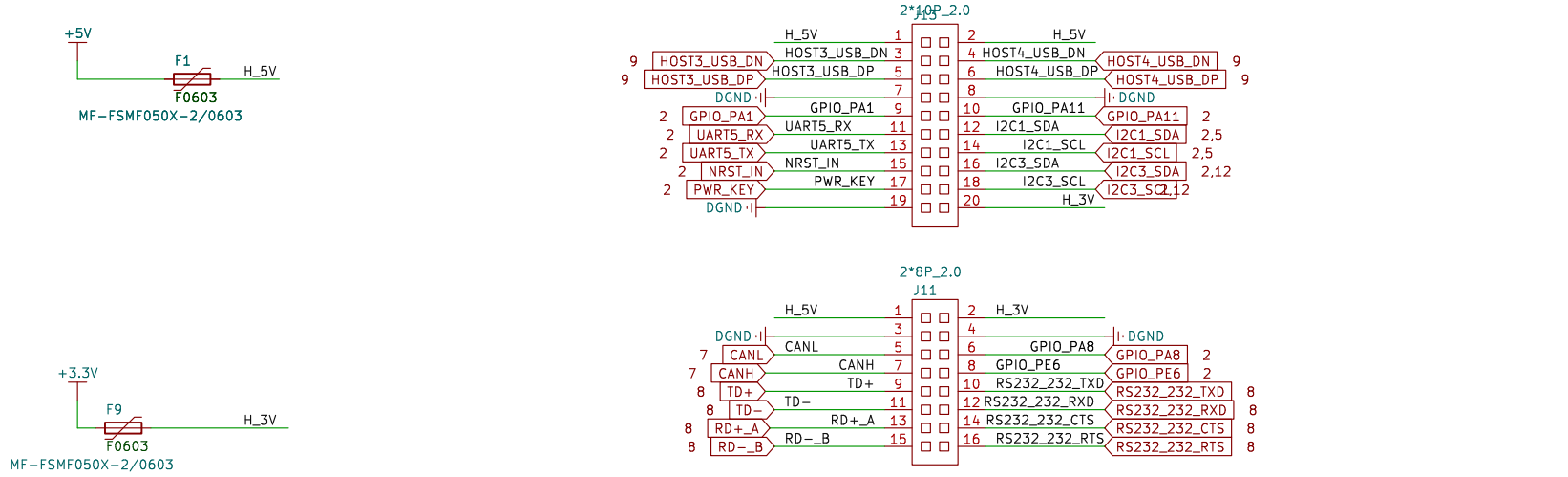
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