Table of Content

Block Diagram
File: Block Diagram.kicad_sch

Power Tree
File: Power Tree.kicad_sch

SOM CONNECTOR 1
File: SOM CONNECTOR 1.kicad_sch

SOM CONNECTOR 2
File: SOM CONNECTOR 2.kicad_sch

POWER
File: POWER.kicad_sch

USB&OTG
File: USB&OTG.kicad_sch

DEBUG_UART
File: DEBUG_UART.kicad_sch

ETH0
File: ETH0.kicad_sch

ETH1
File: ETH1.kicad_sch

TF Card
File: TF Card.kicad_sch

MIPI_DSI
File: MIPI_DSI0.kicad_sch

MIPI_CSI0
File: MIPI_CSI0.kicad_sch

MINI PCIE
File: MINI PCIE.kicad_sch

RTC&USER
File: RTC&USER.kicad_sch

LOCAL BUS
File: LOCAL BUS.kicad_sch

CONNECTOR1
File: CONNECTOR1.kicad_sch

CONNECTOR2
File: CONNECTOR2.kicad_sch

Mechanical
File: Mechanical.kicad_sch

30031

P2025-EC003

Revision History

V1.0-2025.6-25-WJ
1、初始版本。

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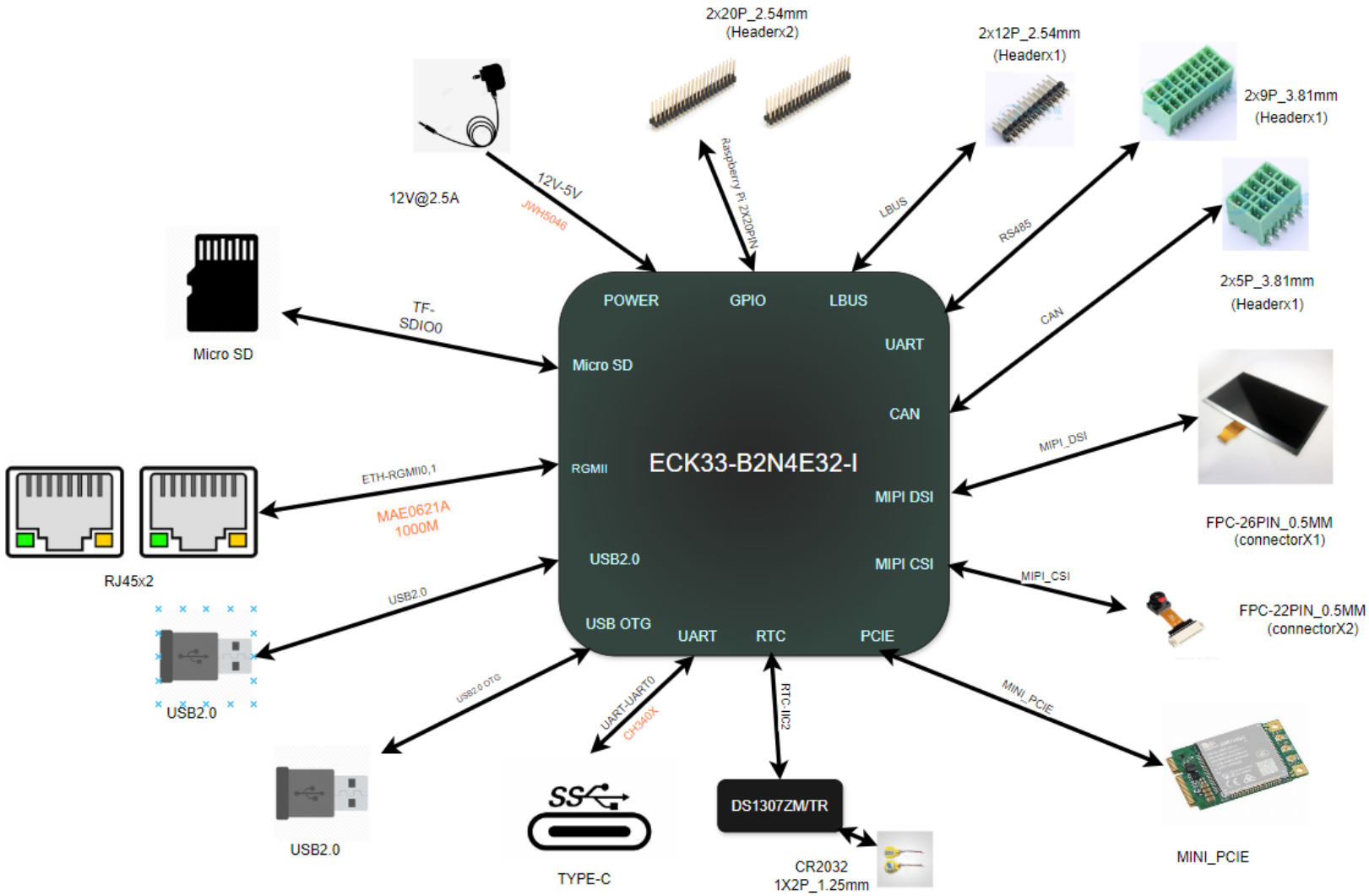
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Sheet: /
File: 30031V1.0.kicad_sch

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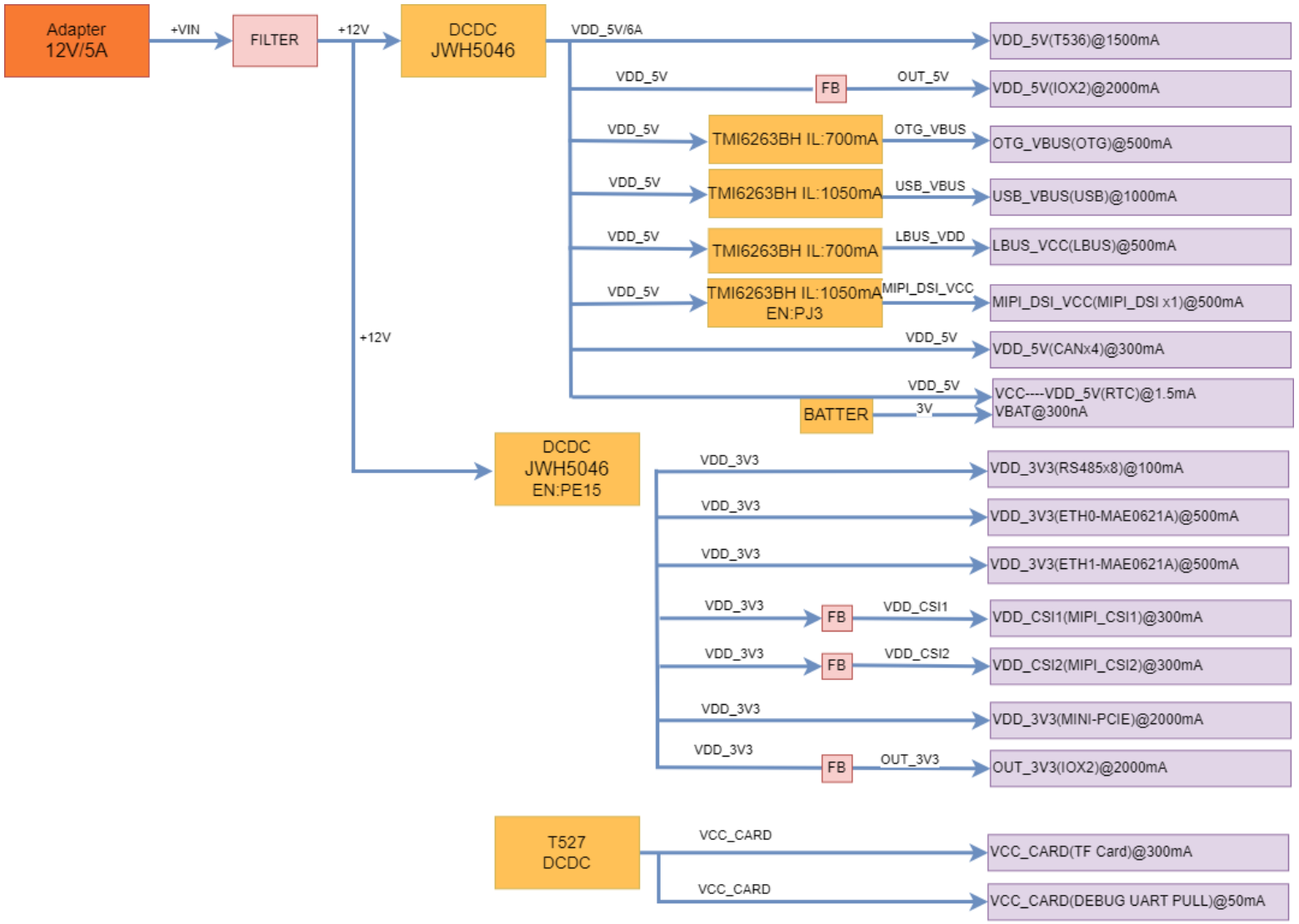
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File: Block Diagram.kicad_sch

Title: Block Diagram

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KiCad E.D.A. 8.0.7		Id: 1/19

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File: Power Tree.kicad_sch

Title: Power Tree

Size: A4	Date: 2025.06.25	**Rev: V1.0**
KiCad E.D.A. 8.0.7		Id: 2/19

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U1A
ECK33_T536_LGA381
Ebyte-PCB:ECK33_T536_LGA381

CK24M-OUT A14 — CK24M-OUT 17
CK32K-OUT A15 — CK32K-OUT 17

GPADC1-0TP-X1 A32 — GPADC1-0/TP-X1 17
GPADC1-1TP-X2 A33 — GPADC1-1/TP-X2 17
GPADC1-2TP-Y1 A34 — GPADC1-2/TP-Y1 17
GPADC1-3TP-Y2 A35 — GPADC1-3/TP-Y2 17
GPADC1-7 A70 — GPADC1-7 17
GPADC1-8 A71 — GPADC1-8 17
GPADC1-9 A72 — GPADC1-9 17
GPADC2-0 A83 — GPADC2-0 17
GPADC2-1 A84 — GPADC2-1 17
GPADC2-2 A85 — GPADC2-2 17
GPADC2-3 A86 — GPADC2-3 17
GPADC2-4 A54 — GPADC2-4 17
GPADC2-5 A50 — GPADC2-5 17
GPADC2-6 A51 — GPADC2-6 17
GPADC2-7 A55 — GPADC2-7 17
GPADC2-8 A52 — GPADC2-8 17
GPADC2-9 A53 — GPADC2-9 17
GPADC3-0 A11 — GPADC3-0 17
LRADC0 A69 — LRADC0 17

PI12/TWI3-SDA/PWM2-4/DMIC-CLK/I2S0-DIN3/I2S0-DOUT3/PCIE-CLKREQN/PI-EINT12 A23 — PI12 16
PA2/GPADC0-2/PA-EINT2 A21 — PA2 15
PA3/GPADC0-3/PA-EINT3 A42 — PA3 15
PA4/GPADC0-4/PA-EINT4 A43 — PA4 15
PA5/GPADC0-5/PA-EINT5 A41 — PA5 15
PA6/GPADC0-6/PA-EINT6 A20 — PA6 15
PA7/GPADC0-7/PA-EINT7 A44 — PA7 15
PA8/GPADC0-8/PA-EINT8 A45 — PA8 15
PA9/GPADC0-9/PA-EINT9 A22 — PA9 15

PC3/SPI0-CS0/NAND-CE1/SPI0-CS0/SPIF-CS0/PC-EINT3 A7 — PC3/SPI0-CS0 16
PC2/SPI0-MOSI/NAND-CLE/SPI0-MOSI/SPIF-MOSI/PC-EINT2 A8 — PC2/SPI0-MOSI 16
PC4/SPI0-MISO/NAND-CE0/SPI0-MISO/SPIF-MISO/PC-EINT4 A9 — PC4/SPI0-MISO 16
PC12/SPI0-CLK/NAND-DQS/SPI0-CLK/SPIF-CLK/PC-EINT12 A29 — PC12/SPI0-CLK 16
PC7/SPI0-CS1/NAND-RB1/SPI0-CS1/SPIF-DQS/PC-EINT7 A30 — PC7/SPI0-CS1 16

VDD_CORE_5V A1
VDD_CORE_5V A2
VDD_CORE_5V A3
VDD_CORE_5V A4
VDD_CORE_5V A24
VDD_CORE_5V A25
VDD_CORE_5V A26
VDD_CORE_5V A27
VDD_CORE_5V A46
VDD_CORE_5V A47
A1 — FB2 100R_1A_08 — VDD_5V
A4 — FB1 100R_1A_08

VDD_CORE_3V3 A65
VDD_CORE_3V3 A66
A65 — PWR_FLAG — VCC_CARD

GND A5
GND A6
GND A10
GND A12
GND A13
GND A16
GND A17
GND A18
GND A19
GND A28
GND A31
GND A36
GND A37
GND A38
GND A39
GND A40
GND A48
GND A49
GND A56
GND A57
GND A59
GND A60
GND A61
GND A62
GND A63
GND A68
GND A73
GND A74
GND A75
GND A76
GND A77
GND A78
GND A79
GND A80
GND A81
GND A88
GND A89
GND A90
GND A91
GND A92
GND A93
DGND

U1B
ECK33_T536_LGA381
Ebyte-PCB:ECK33_T536_LGA381

PB9/UART0-TX/UART0-TX/I2S2-DIN2/I2S2-DOUT2/TWI0-SCK/UART8-DSR/PB-EINT9 B8 — DE-UART0-TX 7
PB10/UART0-RX/UART0-RX/I2S2-DIN3/PWM0-1/I2S2-DOUT3/TWI0-SDA/UART8-DTR/PB-EINT10 B9 — DE-UART0-RX 7
PG6/UART1-TX/UART1-TX/TWI6-SCK/PG-EINT6 B79 — UART1-TX 15
PG7/UART1-RX/UART1-RX/TWI6-SDA/PG-EINT7 B80 — UART1-RX 15
PG8/UART1-RTS/UART1-RTS/TWI5-SCK/PG-EINT8 B70 — RUN_LED 14
PG9/UART1-CTS/UART1-CTS/TWI5-SDA/PG-EINT9 B69 — USER_LED 14
PH4/UART3-TX/UART3-TX/SPI3-CS0/PWM1-4/DMIC-CLK/LBUS-LD28/PH-EINT4 B1 — PH4/PWM1-4 16
PH5/UART3-RX/UART3-RX/SPI3-CLK/LEDC/PCIE-PERSTN/DMIC-DATA0/LBUS-LD29/PH-EINT5 B2 — PCIE-PERSTN 13
PH6/UART3-RTS/UART3-RTS/SPI3-MOSI/TWI6-SCK/PCIE-WAKEN/DMIC-DATA1/LBUS-LD30/PH-EINT6 B4 — PH6/TWI6-SCK 16
PH7/UART3-CTS/UART3-CTS/SPI3-MISO/TWI6-SDA/PCIE-CLKREQN/DMIC-DATA2/LBUS-LD31/PH-EINT7 B3 — PH7/TWI6-SDA 16
PI13/UART4-TX/UART4-TX/PWM2-5/DMIC-DATA0/IR-RX0/I2S3-BCLK/PI-EINT13 B56 — UART4-TX 15
PI14/UART4-RX/UART4-RX/PWM2-6/DMIC-DATA1/IR-RX1/I2S3-LRCK/PI-EINT14 B57 — UART4-RX 15
PI15/UART4-RTS/UART4-RTS/TWI2-SCK/DMIC-DATA2/CAN0-TX0/IR-RX2/I2S3-DIN0/PI-EINT15 B39 — PI15/TWI2-SCK 16
PI16/UART4-CTS/UART4-CTS/TWI2-SDA/DMIC-DATA3/CAN0-RX0/IR-RX3/I2S3-MCLK/PI-EINT16 B38 — PI16/TWI2-SDA 16
PB13/UART7-TX/TWI4-SCK/UART7-TX/SPI1-MOSI/PWM0-4/IR-RX2/PB-EINT13 B23 — PB13/UART7-TX 16
PB14/UART7-RX/TWI4-SDA/UART7-RX/SPI1-MISO/PWM0-5/IR-RX3/PB-EINT14 B24 — PB14/UART7-RX 16
PB4/UART8-TX/TWI1-SCK/I2S2-MCLK/PWM0-8/IR-RX0/UART8-TX/PB-EINT4 B25 — PB4/UART8-TX 16
PB5/UART8-RX/TWI1-SDA/I2S2-BCLK/PWM0-9/IR-RX1/UART8-RX/PB-EINT5 B26 — PB5/UART8-RX 16
PL4/S-UART1-TX/S-JTAG-MS/S-UART1-TX/S-IR-RX/PL-EINT4 B42 — S-UART1-TX 15
PL5/S-UART1-RX/S-JTAG-CK/S-UART1-RX/PL-EINT5 B43 — S-UART1-RX 15
PL6/S-UART1-RTS/S-JTAG-DO/S-UART1-RTS/PL-EINT6 B45 — PL6 16
PL7/S-UART1-CTS/S-JTAG-DI/S-UART1-CTS/PL-EINT7 B44 — PL7 16
PL2/S-UART0-TX/S-UART0-TX/S-IR-RX/S-PWM0-2/PL-EINT2 B6 — S-UART0-TX 15
PL3/S-UART0-RX/S-UART0-RX/S-PWM0-3/PL-EINT3 B7 — S-UART0-RX 15

PB11/TWI5-SCK/TWI5-SCK/UART7-RTS/SPI1-CS0/PWM0-2/UART8-RI/PB-EINT11 B13 — PB11/UART7-RTS 16
PB12/TWI5-SDA/TWI5-SDA/UART7-CTS/SPI1-CLK/PWM0-3/PB-EINT12 B12 — PB12/UART7-CTS 16
PL8/S-TWI1-SCK/S-TWI1-SCK/S-RJTAG-MS/PL-EINT8 B64 — PL8/S-TWI1-SCK 16
P9/S-TWI1-SDA/S-TWI1-SDA/S-RJTAG-CK/S-PWM0-1/PL-EINT9 B65 — PL9/S-TWI1-SDA 16

PI9/SPI2-MISO/UART6-RTS/SPI2-MISO/PWM2-3/I2S0-LRCK/CAN2-RX0/IR-TX/PI-EINT9 B14 — PI9/I2S0-LRCK 16
PI8/SPI2-MOSI/UART6-CTS/SPI2-MOSI/PWM2-2/I2S0-BCLK/CAN2-TX0/PI-EINT8 B15 — PI8/I2S0-BCLK 16
PI11/SPI2-HOLD/TWI3-SCK/SPI2-HOLD/PWM2-8/I2S0-DIN0/I2S0-DOUT1/PCIE-WAKEN/PI-EINT11 B16 — PI11/I2S0-DOUT1 16
PI10/SPI2-WP/OWA-OUT/SPI2-WP/PWM2-7/I2S0-DOUT0/I2S0-DIN1/PCIE-PERSTN/PI-EINT10 B35 — PI10/I2S0-DIN1 16
PI7/SPI2-CLK/UART6-RX/SPI2-CLK/PWM2-1/I2S0-DIN2/I2S0-DOUT2/CLK-FANOUT1/PI-EINT7 B36 — PI7/I2S0-DIN2 16
PI6/SPI2-CS0/UART6-TX/SPI2-CS0/PWM2-0/I2S0-MCLK/LEDC/CLK-FANOUT0/PI-EINT6 B37 — PI6/I2S0-MCLK 16

PB3/SPI4-MISO/UART2-CTS/SPI4-MISO/JTAG-DI/RJTAG-DI/PB-EINT3 B75 — PB3 16
PB0/SPI4-CS0/UART2-TX/SPI4-CS0/JTAG-MS/PWM0-6/RJTAG-MS/PB-EINT0 B76 — UART2-TX 15
PB2/SPI4-MOSI/UART2-RTS/SPI4-MOSI/JTAG-DO/RJTAG-DO/PB-EINT2 B78 — PB2 16
PB1/SPI4-CLK/UART2-RX/SPI4-CLK/JTAG-CK/PWM0-7/RJTAG-CK/PB-EINT1 B77 — UART2-RX 15

PL12/S-SPI0-MOSI/S-PWM0-1/S-UART0-RTS/S-SPI0-MOSI/PL-EINT12 B31 — PL12/S-SPI0-MOSI 16
PL13/S-SPI0-MISO/S-PWM0-3/S-UART0-CTS/S-SPI0-MISO/PL-EINT13 B32 — PL13/S-SPI0-MISO 16
PL11/S-SPI0-CLK/S-IR-RX/S-UART0-RX/S-SPI0-CLK/PL-EINT11 B11 — PL11/S-SPI0-CLK 16
PL10/S-SPI0-CS0/S-PWM0-0/S-UART0-TX/S-SPI0-CS0/PL-EINT10 B10 — PL10/S-SPI0-CS0 16

PM0/S-TWI0-SCK/S-UART1-TX/S-PWM0-0/PM-EINT0 B66 — PM0/S-TWI0-SCK 16
PM1/S-TWI0-SDA/S-UART1-RX/S-PWM0-1/PM-EINT1 B67 — PM1/S-TWI0-SDA 16
PM2/S-TWI1-SCK/S-UART1-RTS/S-UART0-TX/PM-EINT2 B51 — PM2 16
PM3/S-TWI1-SDA/S-UART1-CTS/S-UART0-RX/PM-EINT3 B53 — PM3 16
PM4//S-PWM0-2/S-UART0-RTS/PM-EINT4 B50 — PM4/S-PWM0-2 16
PM5/S-IR-RX/S-PWM0-3/S-UART0-CTS/PM-EINT5 B68 — PM5/S-PWM0-3 16

PF3/SDC0-CMD/SDC0-CMD/JTAG-DO/I2S3-LRCK/SPI4-CS0/RJTAG-DO/PF-EINT3 B60 — SDC0-CMD 10
PF6/SDC0-DET/I2S3-MCLK/PF-EINT6 B61 — SDC0-DET 10
PF2/SDC0-CLK/SDC0-CLK/UART0-TX/I2S3-DIN2/SPI4-CLK/I2S3-DOUT2/PF-EINT2 B62 — SDC0-CLK 10
PF1/SDC0-D0/SDC0-D0/JTAG-DI/I2S3-DOUT0/I2S3-DIN1/RJTAG-DI/PF-EINT1 B74 — SDC0-D0 10
PF0/SDC0-D1/SDC0-D1/JTAG-MS/I2S3-DIN0/I2S3-DOUT1/RJTAG-MS/PF-EINT0 B73 — SDC0-D1 10
PF5/SDC0-D2/SDC0-D2/JTAG-CK/I2S3-BCLK/SPI4-MISO/RJTAG-CK/PF-EINT5 B72 — SDC0-D2 10
PF4/SDC0-D3/SDC0-D3/UART0-RX/I2S3-DIN3/SPI4-MOSI/I2S3-DOUT3/PF-EINT4 B71 — SDC0-D3 10

PCIE21-REFCLKP B21 — PCIE21-REFCLKP 13
PCIE21-REFCLKN B22 — PCIE21-REFCLKN 13
PCIE21-TXP B40 — PCIE21-TXP 13
PCIE21-TXN B41 — PCIE21-TXN 13
PCIE21-RXP B18 — PCIE21-RXP 13
PCIE21-RXN B19 — PCIE21-RXN 13

GND B5
GND B17
GND B20
GND B27
GND B28
GND B29
GND B30
GND B33
GND B34
GND B46
GND B47
GND B48
GND B49
NC B52
GND B54
GND B55
GND B58
GND B59
GND B63
GND A58
GND A64
GND A67
GND A82
GND A87
DGND

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PG0/SDC1-CLK/SDC1-CLK/UART13-TX/PG-EINT0	C32	PG0/SDC1-CLK	16
PG1/SDC1-CMD/SDC1-CMD/UART13-RX/PG-EINT1	C31	PG1/SDC1-CMD	16
PG2/SDC1-D0/SDC1-D0/PCIE-PERSTN/UART13-RTS/PG-EINT2	C10	PG2/SDC1-D0	16
PG3/SDC1-D1/SDC1-D1/PCIE-WAKEN/UART13-CTS/PG-EINT3	C9	PG3/SDC1-D1	16
PG4/SDC1-D2/SDC1-D2/PCIE-CLKREQN/PG-EINT4	C8	PG4/SDC1-D2	16
PG5/SDC1-D3/SDC1-D3/PG-EINT5	C7	PG5/SDC1-D3	16
PG13/I2S1-DOUT0/UART14-CTS/I2S1-DOUT0/I2S1-DIN1/CAN3-RX0/PG-EINT13	C29	PG13/I2S1-DOUT0	16
PG14/I2S1-DIN0/I2S1-DIN0/I2S1-DOUT1/PG-EINT14	C30	PG14/I2S1-DIN0	16
PG10/I2S1-MCLK/UART14-TX/I2S1-MCLK/TWI2-SCK/PG-EINT10	C49	PG10/I2S1-MCLK	16
PG12/I2S1-LRCK/UART14-RTS/I2S1-LRCK/CAN3-TX0/PG-EINT12	C50	PG12/I2S1-LRCK	16
PG11/I2S1-BCLK/UART14-RX/I2S1-BCLK/TWI2-SDA/PG-EINT11	C51	PG11/I2S1-BCLK	16
PK0/LBUS-LD0/MCSIA-D0N/UART11-TX/SPI2-CS0/LBUS-LD0/PK-EINT0	C42	LBUS-LD0	15
PK1/LBUS-LD1/MCSIA-D0P/UART11-RX/SPI2-CLK/LBUS-LD1/PK-EINT1	C62	LBUS-LD1	15
PK2/LBUS-LD2/MCSIA-D1N/TWI2-SCK/UART11-RTS/SPI2-MOSI/LBUS-LD2/PK-EINT2	C41	LBUS-LD2	15
PK3/LBUS-LD3/MCSIA-D1P/TWI2-SDA/UART11-CTS/SPI2-MISO/LBUS-LD3/PK-EINT3	C79	LBUS-LD3	15
PK4/LBUS-LD4/MCSIA-CKN/UART12-TX/NCSI-D0/SPI2-WP/LBUS-LD4/PK-EINT4	C61	LBUS-LD4	15
PK5/LBUS-LD5/MCSIA-CKP/UART12-RX/NCSI-D1/SPI2-HOLD/LBUS-LD5/PK-EINT5	C40	LBUS-LD5	15
PK6/LBUS-LD6/MCSIB-D0N/UART12-RTS/NCSI-D2/LBUS-LD6/PK-EINT6	C78	LBUS-LD6	15
PK7/LBUS-LD7/MCSIB-D0P/UART12-CTS/NCSI-D3/LBUS-LD7/PK-EINT7	C60	LBUS-LD7	15
PK8/LBUS-LD8/MCSIB-D1N/CAN1-TX0/NCSI-D4/LBUS-LD8/PK-EINT8	C92	PK8	17
PK9/LBUS-LD9/MCSIB-D1P/MCSI3-MCLK/CAN1-RX0/NCSI-D5/LBUS-LD9/PK-EINT9	C39	PK9	16
PK10/LBUS-LD10/MCSIB-CKN/TWI1-SCK/CAN0-TX0/NCSI-D6/LBUS-LD10/PK-EINT10	C77	TWI1-SCK	12
PK11/LBUS-LD11/MCSIB-CKP/TWI1-SDA/CAN0-RX0/NCSI-D7/LBUS-LD11/PK-EINT11	C59	TWI1-SDA	12
PK12/LBUS-LD12/MCSIC-D0N/UART7-TX/TWI1-SCK/NCSI-D8/LBUS-LD12/PK-EINT12	C91	MCSIC-D0N	12
PK13/LBUS-LD13/MCSIC-D0P/UART7-RX/TWI1-SDA/NCSI-D9/LBUS-LD13/PK-EINT13	C76	MCSIC-D0P	12
PK14/LBUS-LD14/MCSIC-D1N/UART7-RTS/UART5-RTS/NCSI-D10/SPI3-CS0/LBUS-LD14/PK-EINT14	C90	MCSIC-D1N	12
PK15/LBUS-LD15/MCSIC-D1P/UART7-CTS/UART5-CTS/NCSI-D11/SPI3-CLK/LBUS-LD15/PK-EINT15	C75	MCSIC-D1P	12
PE0/LBUS-CS0/MCSI0-MCLK/SPI3-CS0/LBUS-CS0/PE-EINT0	C19	LBUS-CS0	15
PE1/LBUS-CS1/TWI2-SCK/UART4-TX/SPI3-CLK/LBUS-CS1/PE-EINT1	C18	OTG-ID	6
PE2/LBUS-CS2/TWI2-SDA/UART4-RX/SPI3-MOSI/LBUS-CS2/PE-EINT2	C17	OTG_EN	6
PE3/LBUS-CS3/TWI3-SCK/UART4-RTS/CSI0-XHS/SPI3-MISO/LBUS-CS3/PE-EINT3	C16	OTG-VBUS-CPU	6
PE4/LBUS-DP0/TWI3-SDA/UART4-CTS/CSI1-XHS/SPI3-WP/LBUS-DP0/PE-EINT4	C15	LBUS-DP0	15
PE5/LBUS-DP1/MCSI1-MCLK//LEDC/SPI3-HOLD/LBUS-DP1/PE-EINT5	C14	LBUS-RESET	15
PE6/LBUS-DP2/UART12-TX/TWI2-SCK/TCON-FSYNC0/LBUS-DP2/PE-EINT6	C13	UART12-TX	15
PE7/LBUS-DP3/UART12-RX/TWI2-SDA/CLK-FANOUT0/LBUS-DP3/PE-EINT7	C12	UART12-RX	15
PE8/LBUS-WR/UART12-RTS/NCSI-D0/CLK-FANOUT1/LBUS-WR/PE-EINT8	C37	LBUS-WR	15
PE9/LBUS-READY/UART12-CTS/NCSI-D1/SPI2-CS0/LBUS-READY/PE-EINT9	C38	LBUS-READY	15
PE10/LBUS-ALE/MCSI3-MCLK/PWM0-3/NCSI-D2//SPI2-CLK/LBUS-ALE/PE-EINT10	C58	LBUS-ALE	15
PE11/LBUS-BURST0/TWI1-SCK/UART5-RTS/NCSI-D3/UART6-TX/SPI2-MOSI/LBUS-BURST0/PE-EINT11	C57	LBUS-BURST0	15
PE12/LBUS-BURST1/TWI1-SDA/UART5-CTS/NCSI-D4/UART6-RX/SPI2-MISO/LBUS-BURST1/PE-EINT12	C56	LBUS-BURST1	15
PE13/LBUS-BURST2/TWI4-SCK/UART5-TX/NCSI-D5/UART6-RTS/SPI2-WP/LBUS-BURST2/PE-EINT13	C35	LBUS-BURST2	15
PE14/LBUS-LCLK/TWI4-SDA/UART5-RX/NCSI-D6/UART6-CTS/SPI2-HOLD/LBUS-LCLK/PE-EINT14	C93	LBUS-LCLK	15
PE15/LBUS-INTR/MCSI2-MCLK/PWM0-2/NCSI-D7//CAN2-TX0/LBUS-INTR/PE-EINT15	C73	PE15	17
PE16/LBUS-DRQ/TWI6-SCK/PWM0-4/CSI0-XVS-FSYNC/TWI0-SCK/CAN2-RX0/LBUS-DRQ/PE-EINT16	C74	LBUS-DRQ	15
PE17/LBUS-LBE/TWI6-SDA/PWM0-5/CSI1-XVS-FSYNC/TWI0-SDA/LBUS-LBE/PE-EINT17	C36	LBUS-LBE	15
AP-NMI	C28	AP-NMI	17
RESET	C4	RESET	14
FEL	C27	FEL	14
PWRON	C5	PWRON	14
PK16/UART5-TX/MCSIC-CKN/TWI4-SCK/UART5-TX/NCSI-D12/SPI3-MOSI/LBUS-LD16/PK-EINT16	C69	MCSIC-CKN	12
PK17/UART5-RX/MCSIC-CKP/TWI4-SDA/UART5-RX/NCSI-D13/SPI3-MISO/LBUS-LD17/PK-EINT17	C70	MCSIC-CKP	12
PK18/UART6-TX/MCSID-D0N/MCSI0-MCLK/UART6-TX/NCSI-D14/SPI3-WP/LBUS-LD18/PK-EINT18	C81	MCSID-D0N	12
PK19/UART6-RX/MCSID-D0P/TWI2-SCK/UART6-RX/NCSI-D15/SPI3-HOLD/LBUS-LD19/PK-EINT19	C82	MCSID-D0P	12
PK20/UART6-RTS/MCSID-D1N/TWI2-SDA/UART6-RTS/NCSI-PCLK/LBUS-LD20/PK-EINT20	C84	MCSID-D1N	12
PK21/UART6-CTS/MCSID-D1P/MCSI1-MCLK/UART6-CTS/NCSI-MCLK/LBUS-LD21/PK-EINT21	C83	MCSID-D1P	12
PI2/UART9-TX/I2S0-DIN0/UART9-TX/CAN3-TX0/PI-EINT2	C65	UART9-TX	15
PI3/UART9-RX/I2S0-MCLK/UART9-RX/CAN3-RX0/PI-EINT3	C66	UART9-RX	15
PI4/UART9-RTS/TWI5-SCK/UART9-CTS/PI-EINT4	C68	PI4/TWI5-SCK	16
PI5/UART9-CTS/TWI5-SDA/UART9-RTS/PI-EINT5	C67	PI5/TWI5-SDA	16
PH12/UART11-TX/IR-RX1/I2S3-DOUT0/I2S3-DIN1/LCD-D16/CAN2-TX0/UART11-TX/PH-EINT12	C54	UART11-TX	15
PH13/UART11-RX/IR-RX2/I2S3-DOUT1/I2S3-DIN0/LCD-D17/CAN2-RX0/UART11-RX/PH-EINT13	C55	UART11-RX	15
PD12/SPI1-MOSI/LCD-D18/LVDS1-D1P/SPI1-MOSI/DBI-SDO/PWM1-2/UART8-RTS/PD-EINT12	C21	PD12/UART8-RTS	16
PD14/SPI1-HOLD/LCD-D20/LVDS1-D2P/SPI1-HOLD/PWM1-4DBI-DCXDBI-WRX/UART3-TX/UART8-DCD/PD-EINT14	C22	PD14-PWR-EN	5
PD13/SPI1-MISO/LCD-D19/LVDS1-D1N/SPI1-MISO/DBI-SDIDBI-TEDBI-DCX/PWM1-3/UART8-CTS/PD-EINT13	C23	PD13/UART8-CTS	16
PD10/SPI1-CS0/LCD-D14/LVDS1-D0P/SPI1-CS0/PWM1-0DBI-CSX/TWI6-SCK/UART8-TX/PD-EINT10	C64	PD10	17
PD15/SPI1-WP/LCD-D21/LVDS1-D2N/SPI1-WP/PWM1-5DBI-TE/UART3-RX/UART8-DSR/PD-EINT15	C45	PD15	17
PD11/SPI1-CLK/LCD-D15/LVDS1-D0N/SPI1-CLK/PWM1-1DBI-SCLK/TWI6-SDA/UART8-RX/PD-EINT11	C44	PD11	17
USB0-DM	C24	USB0-DN	6
USB0-DP	C25	USB0-DP	6
USB1-DM	C46	USB1-DN	6
USB1-DP	C47	USB1-DP	6
PH0/TWI0-SCK/TWI0-SCK/PWM2-4/UART1-TX/CAN1-TX0/LBUS-LD24/PH-EINT0	C86	TWI0-SCK	12
PH1/TWI0-SDA/TWI0-SDA/PWM2-5/UART1-RX/CAN1-RX0/LBUS-LD25/PH-EINT1	C85	TWI0-SDA	12
PH8/TWI3-SCK/PWM2-6/SPI3-WP/OWA-IN/LCD-D0/DMIC-DATA3/TWI3-SCK/PH-EINT8	C72	TWI3-SCK	14
PH9/TWI3-SDA/PWM2-7/SPI3-HOLD/I2S3-MCLK/LCD-D1/TWI3-SDA/PH-EINT9	C71	TWI3-SDA	14
PD18/TWI6-SCK/LCD-CLK/LVDS1-D3P/TWI6-SCK/PWM1-8/CAN0-TX0/PD-EINT18	C52	PD18/PWM1-8	16
PD19/TWI6-SDA/LCD-DE/LVDS1-D3N/TWI6-SDA/PWM1-9/CAN0-RX0/PD-EINT19	C53	PD19	17
PK22/MCSID-CKN/TWI3-SCK/PWM0-6/NCSI-HSYNC/CAN3-TX0/LBUS-LD22/PK-EINT22	C87	MCSID-CKN	12
PK23/MCSID-CKP/TWI3-SDA/PWM0-7/NCSI-VSYNC/CAN3-RX0/LBUS-LD23/PK-EINT23	C88	MCSID-CKP	12
GND	C34		
GND	C3		
GND	C11		
GND	C33		
GND	C43		
GND	C48		
GND	C89		

DGND

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LINEOUTLP	C1	LINEOUTLP	14,17
LINEOUTLN	C2	LINEOUTLN	14,17
NC	C6		
PD6/LVDS0-CKP/LCD-D10/LVDS0-CKP/DSI-D2P/PWM0-6//UART10-RTS/PD-EINT6	D43	DSI-D2P	11
PD7/LVDS0-CKN/LCD-D11/LVDS0-CKN/DSI-D2N/PWM0-7/UART10-CTS/PD-EINT7	D44	DSI-D2N	11
PD0/LVDS0-D0P/LCD-D2/LVDS0-D0P/DSI-D0P/PWM0-0/UART9-TX/PD-EINT0	D1	DSI-D0P	11
PD1/LVDS0-D0N/LCD-D3/LVDS0-D0N/DSI-D0N/PWM0-1/UART9-RX/PD-EINT1	D2	DSI-D0N	11
PD2/LVDS0-D1P/LCD-D4/LVDS0-D1P/DSI-D1P/PWM0-2/UART9-RTS/PD-EINT2	D23	DSI-D1P	11
PD3/LVDS0-D1N/LCD-D5/LVDS0-D1N/DSI-D1N/PWM0-3/UART9-CTS/PD-EINT3	D24	DSI-D1N	11
PD4/LVDS0-D2P/LCD-D6/LVDS0-D2P/DSI-CKP/PWM0-4/UART10-TX/PD-EINT4	D4	DSI-CKP	11
PD5/LVDS0-D2N/LCD-D7/LVDS0-D2N/DSI-CKN/PWM0-5/UART10-RX/PD-EINT5	D5	DSI-CKN	11
PD8/LVDS0-D3P/LCD-D12/LVDS0-D3P/DSI-D3P/PWM0-8/CAN1-TX0/PD-EINT8	D26	DSI-D3P	11
PD9/LVDS0-D3N/LCD-D13/LVDS0-D3N/DSI-D3N/PWM0-9/CAN1-RX0/PD-EINT9	D27	DSI-D3N	11
PD20/UART2-TX/LCD-HSYNC/PWM0-2/UART2-TX/TWI5-SCK/PD-EINT20	D58	DSI-TP-INT	11
PD21/UART2-RX/LCD-VSYNC/PWM0-3/UART2-RX/TWI5-SDA/PD-EINT21	D59	DSI-TP-RST	11
PD22/UART2-RTS/PWM0-1/UART2-RTS/TWI4-SCK/TWI0-SCK/PD-EINT22	D60	TWI4-SCK	11
PD23/UART2-CTS/PWM0-0/UART2-CTS/TWI4-SDA/TWI0-SDA/PD-EINT23	D71	TWI4-SDA	11
/PH14/IR-TX/I2S3-DOUT2/I2S3-DIN2/PWM1-5/UART11-RTS/PH-EINT14	D15	DSI-BL-CTR	11
/PH15/IR-RX0/I2S3-DOUT3/I2S3-DIN3/LEDC/UART11-CTS/PH-EINT15	D16	DSI_LCD_RESET	11
/PB6//I2S2-LRCK//PWM1-0/IR-TX/UART8-RTS/PB-EINT6	D17	CSI1_PWR_EN	12
PI0/CAN0-TX/I2S0-BCLK/CAN0-TX0/PI-EINT0	D19	CAN0-TX	15
PI1/CAN0-RX/I2S0-LRCK/CAN0-RX0/PI-EINT1	D18	CAN0-RX	15
PB7/CAN1-TX/OWA-IN/I2S2-DOUT0/I2S2-DIN1/PWM1-1/CAN1-TX0/UART8-CTS/PB-EINT7	D38	CAN1-TX	15
PB8/CAN1-RX/OWA-OUT/I2S2-DIN0/I2S2-DOUT1/PWM0-0/CAN1-RX0/UART8-DCD/PB-EINT8	D37	CAN1-RX	15
PD16/CAN2-TX/LCD-D22/LVDS1-CKP/CAN2-TX0/PWM1-6/UART3-RTS/UART8-DTR/PD-EINT16	D41	CAN2-TX	15
PD27/CAN2-RX/LCD-D23/LVDS1-CKN/CAN2-RX0/PWM1-7/UART3-CTS/UART8-RI/PD-EINT17	D40	CAN2-RX	15
PH20/CAN3-TX/PWM2-8/I2S3-BCLK/LCD-D8/CAN3-TX0/PH-EINT10	D22	CAN3-TX	15
PH11/CAN3-RX/IR-RX3/PWM2-9/I2S3-LRCK/LCD-D9/CAN3-RX0/PH-EINT11	D21	CAN3-RX	15
PJ24/RGMII1-MDC/PWM2-0/TWI4-SCK/UART13-RTS/RGMII1-MDC/PJ-EINT24	D63	RGMII1-MDC	9
PJ25/RGMII1-MDIO/PWM2-1/TWI4-SDA/UART13-CTS/RGMII1-MDIO/PJ-EINT25	D62	RGMII1-MDIO	9
PJ22/RGMII1-TXCK/UART14-RTS/IR-TX/RGMII1-TXCK/RMII1-TXCK/PJ-EINT22	D8	RGMII1-TXCK	9
PJ23/RGMII1-TXCTL/UART14-CTS/RGMII1-TXCTL/RMII1-TXEN/PJ-EINT23	D9	RGMII1-TXCTL	9
PJ21/RGMII1-TXD0/TWI0-SDA/UART14-RX/RGMII1-TXD0/RMII1-TXD0/PJ-EINT21	D13	RGMII1-TXD0	9
PJ20/RGMII1-TXD1/TWI0-SCK/UART14-TX/RGMII1-TXD1/RMII1-TXD1/PJ-EINT20	D12	RGMII1-TXD1	9
PJ31/RGMII1-TXD2/RGMII1-TXD2/RMII1-NULL/PJ-EINT31	D11	RGMII1-TXD2	9
PJ30/RGMII1-TXD3/RGMII1-TXD3/RMII1-NULL/PJ-EINT30	D10	RGMII1-TXD3	9
PJ29/RGMII1-RXCK/TWI1-SDA/RGMII1-RXCK/RMII1-NULL/PJ-EINT29	D46	RGMII1-RXCK	9
PJ18/RGMII1-RXCTL/PWM2-8/IR-RX2/RGMII1-RXCTL/RMII1-CRS-DV/PJ-EINT18	D47	RGMII1-RXCTL	9
PJ17/RGMII1-RXD0/PWM2-7/IR-RX1/RGMII1-RXD0/RMII1-RXD0/PJ-EINT17	D32	RGMII1-RXD0	9
PJ16/RGMII1-RXD1/PWM2-6/IR-RX0/RGMII1-RXD1/RMII1-RXD1/PJ-EINT16	D31	RGMII1-RXD1	9
PJ28/RGMII1-RXD2/TWI1-SCK/RGMII1-RXD2/RMII1-NULL/PJ-EINT28	D30	RGMII1-RXD2	9
PJ27/RGMII1-RXD3/PWM2-3/TWI5-SDA/UART13-RX/RGMII1-RXD3/RMII1-NULL/PJ-EINT27	D29	RGMII1-RXD3	9
PJ26/EPHY1-CLK-25M/PWM2-2/TWI5-SCK/UART13-TX/RGMII1-EPHY-2550M/PJ-EINT26	D49	RGMII1-RESET	9
PJ19/RGMII1-CLKIN-125M/PWM2-9/IR-RX3/RGMII1-CLKIN/RMII1-RXER/PJ-EINT19	D48	CSI0_PWR_EN	12
PJ8/RGMII0-MDC/PWM0-8/I2S1-BCLK/RGMII0-MDC/PJ-EINT8	D54	RGMII0-MDC	8
PJ9/RGMII0-MDIO/PWM0-9/I2S1-LRCK/RGMII0-MDIO/PJ-EINT9	D53	RGMII0-MDIO	8
PJ6/RGMII0-TXCK/PWM0-6/I2S2-DIN3/I2S2-DOUT3/RGMII0-TXCK/RMII0-TXCK/PJ-EINT6	D51	RGMII0-TXCK	8
PJ7/RGMII0-TXCTL/PWM0-7/I2S1-MCLK//RGMII0-TXCTL/RMII0-TXEN/PJ-EINT7	D52	RGMII0-TXCTL	8
PJ5/RGMII0-TXD0/PWM0-5/I2S2-DIN2/I2S2-DOUT2/RGMII0-TXD0/RMII0-TXD0/PJ-EINT5	D68	RGMII0-TXD0	8
PJ4/RGMII0-TXD1/PWM0-4/I2S2-DIN0/I2S2-DOUT1/RGMII0-TXD1/RMII0-TXD1/PJ-EINT4	D67	RGMII0-TXD1	8
PJ15/RGMII0-TXD2/PWM2-5/UART10-CTS/TWI6-SDA/RGMII0-TXD2/RMII0-NULL/PJ-EINT15	D66	RGMII0-TXD2	8
PJ14/RGMII0-TXD3/PWM2-4/UART10-RTS/TWI6-SCK/RGMII0-TXD3/RMII0-NULL/PJ-EINT14	D65	RGMII0-TXD3	8
PJ13/RGMII0-RXCK/PWM2-3/UART10-RX/RGMII0-RXCK/RMII0-NULL/PJ-EINT13	D80	RGMII0-RXCK	8
PJ2/RGMII0-RXCTL/PWM0-2/I2S2-LRCK/RGMII0-RXCTL/RMII0-CRS-DV/PJ-EINT2	D69	RGMII0-RXCTL	8
PJ1/RGMII0-RXD0/PWM0-1/I2S2-BCLK/RGMII0-RXD0/RMII0-RXD0/PJ-EINT1	D79	RGMII0-RXD0	8
PJ0/RGMII0-RXD1/PWM0-0/I2S2-MCLK/RGMII0-RXD1/RMII0-RXD1/PJ-EINT0	D78	RGMII0-RXD1	8
PJ12/RGMII0-RXD2/PWM2-2/UART10-TX/RGMII0-RXD2/RMII0-NULL/PJ-EINT12	D77	RGMII0-RXD2	8
PJ11/RGMII0-RXD3/PWM2-1/I2S1-DIN0/I2S1-DOUT1/RGMII0-RXD3/RMII0-NULL/PJ-EINT11	D76	RGMII0-RXD3	8
PJ10/EPHY0-CLK-25M/PWM2-0/I2S1-DOUT0/I2S1-DIN1/RGMII0-EPHY-2550M//PJ-EINT10	D35	RGMII0-RESET	8
PJ3/RGMII0-CLKIN-125M/PWM0-3/I2S2-DOUT0/I2S2-DIN1/RGMII0-CLKIN/RMII0-RXER//PJ-EINT3	D34	DSI-PWR-EN	11
PA0/GPADC0-0/PA-EINT0	D73	PA0	17
PA1/GPADC0-1/PA-EINT1	D74	PA1	17
GND	D3		
GND	D6		
GND	D7		
GND	D14		
GND	D20		
GND	D25		
GND	D28		
GND	D33		
GND	D36		
GND	D39		
GND	D42		
GND	D45		
GND	D50		
GND	D55		
NC	D56		
NC	D57		
GND	D61		
GND	D64		
GND	D70		
GND	D72		
GND	D75		
GND	C20		
GND	C26		
GND	C63		
GND	C80		

DGND

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GND	E1	
GND	E2	
GND	E3	
GND	E4	
GND	E5	
GND	E6	
GND	E7	
GND	E8	
GND	E9	
GND	E10	
GND	E11	
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GND	E31	
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DGND

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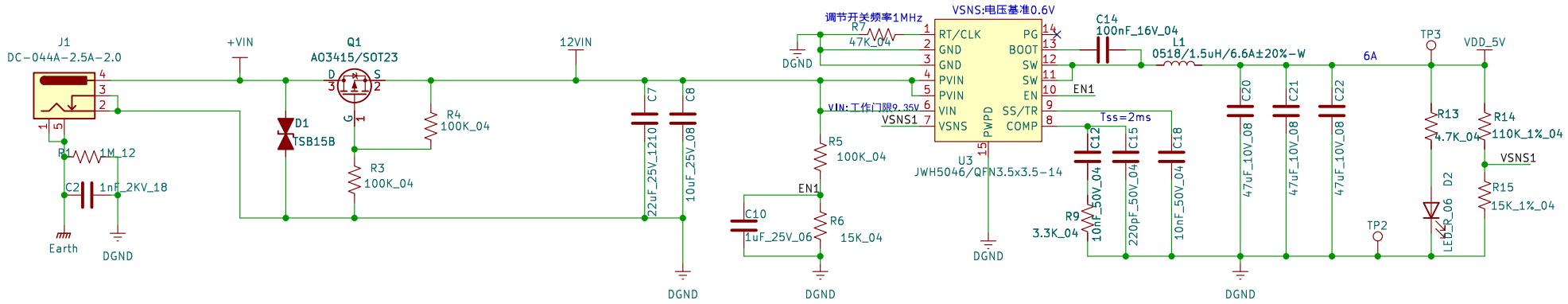
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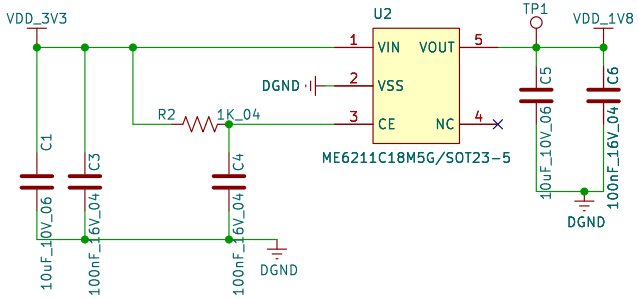
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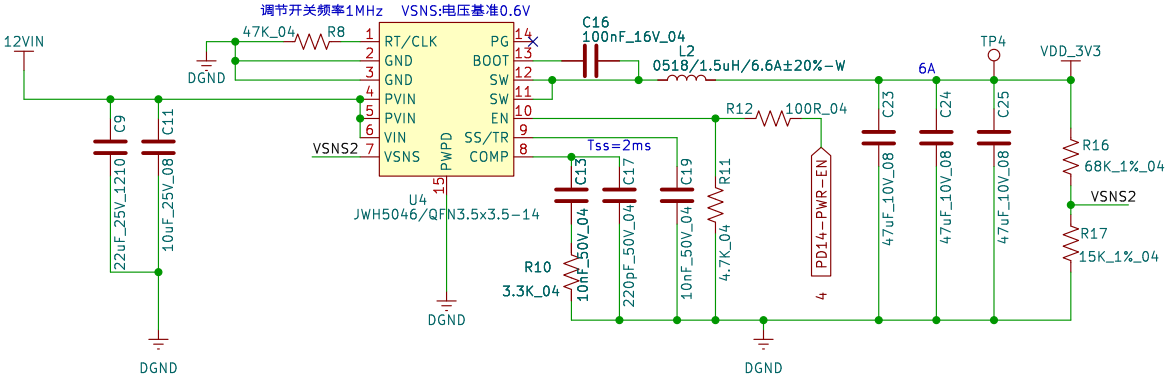
12V TO 5V



3.3V TO 1.8V



12V TO 3.3V





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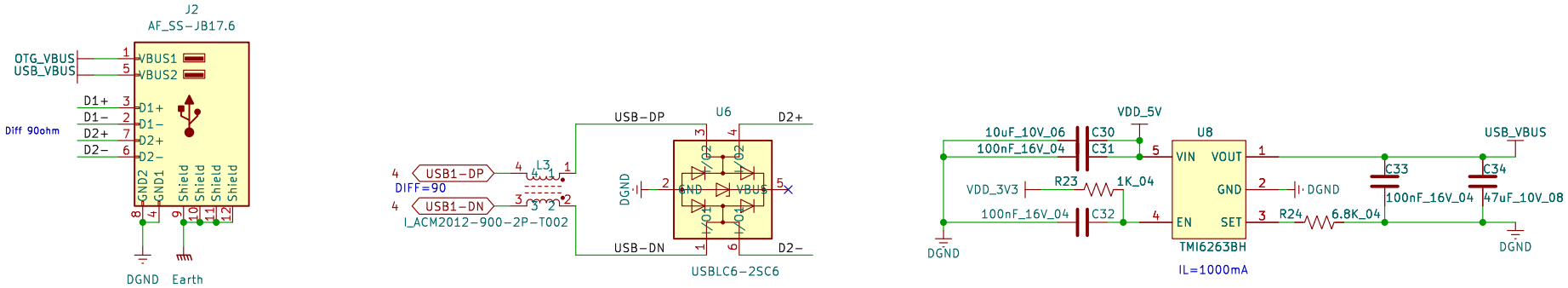
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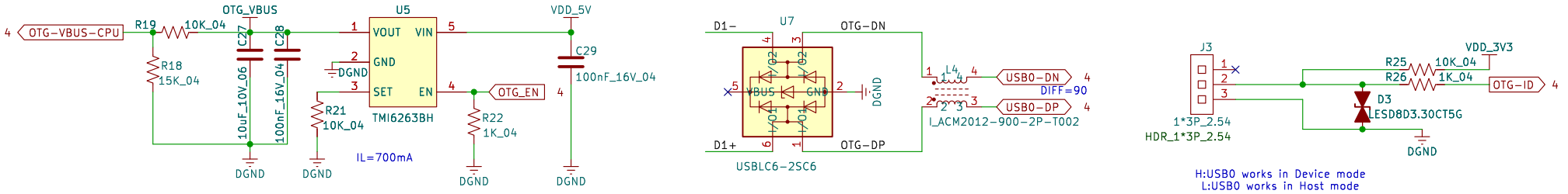
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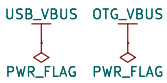
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USB



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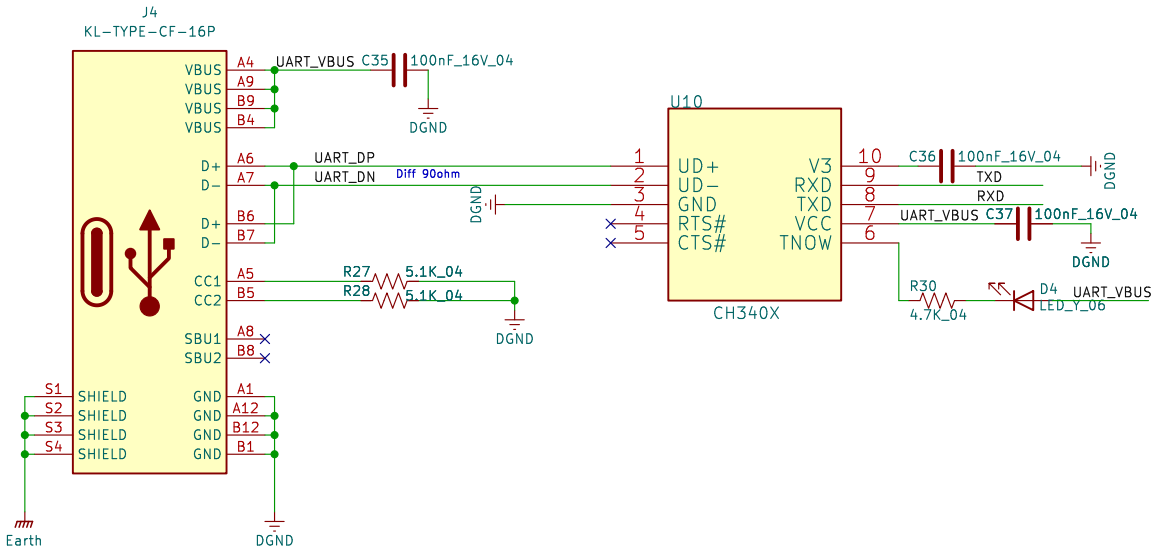
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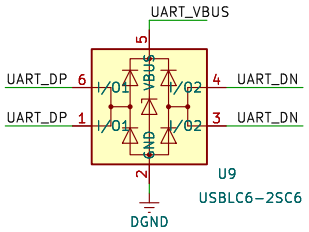
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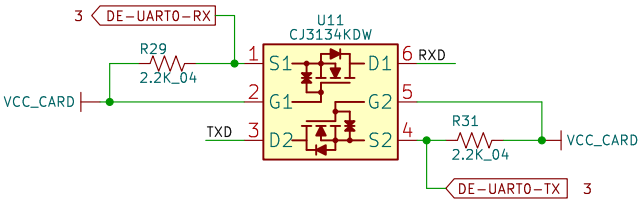
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DEBUG







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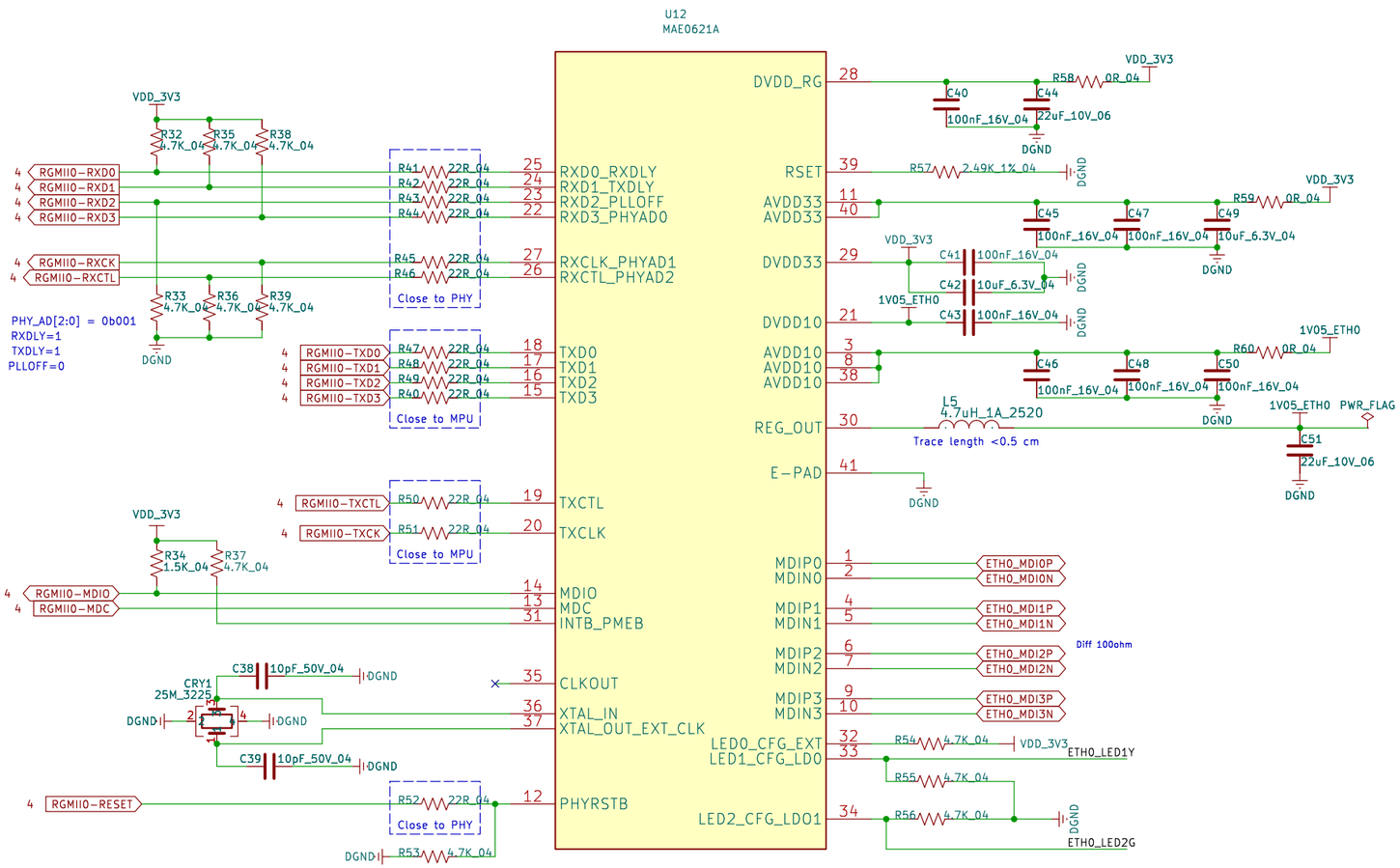
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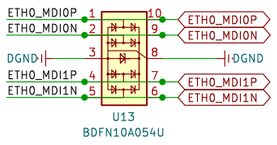
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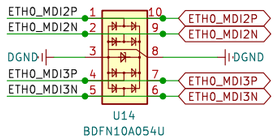
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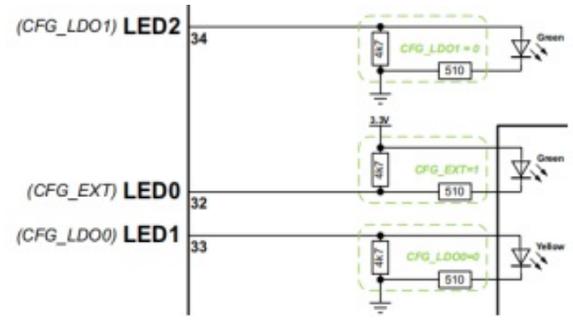
ETH0



If the external 25MHz clock used, the XTAL_IN needs to be pulled down to GND by a resistor, The recommend resistor value is 0ohm.
The external 25MHz clock must be 3.3V domain, detail description see datasheet.

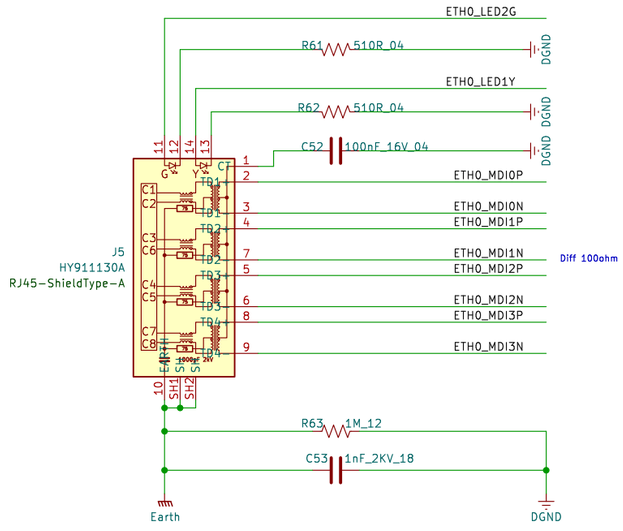






LED1: Low=Link Up at 100M Blink=T/R;
LED2: High=Link Up at 1000M Blink=T/R;

EXT: external power source.
LDO: 3.3V.



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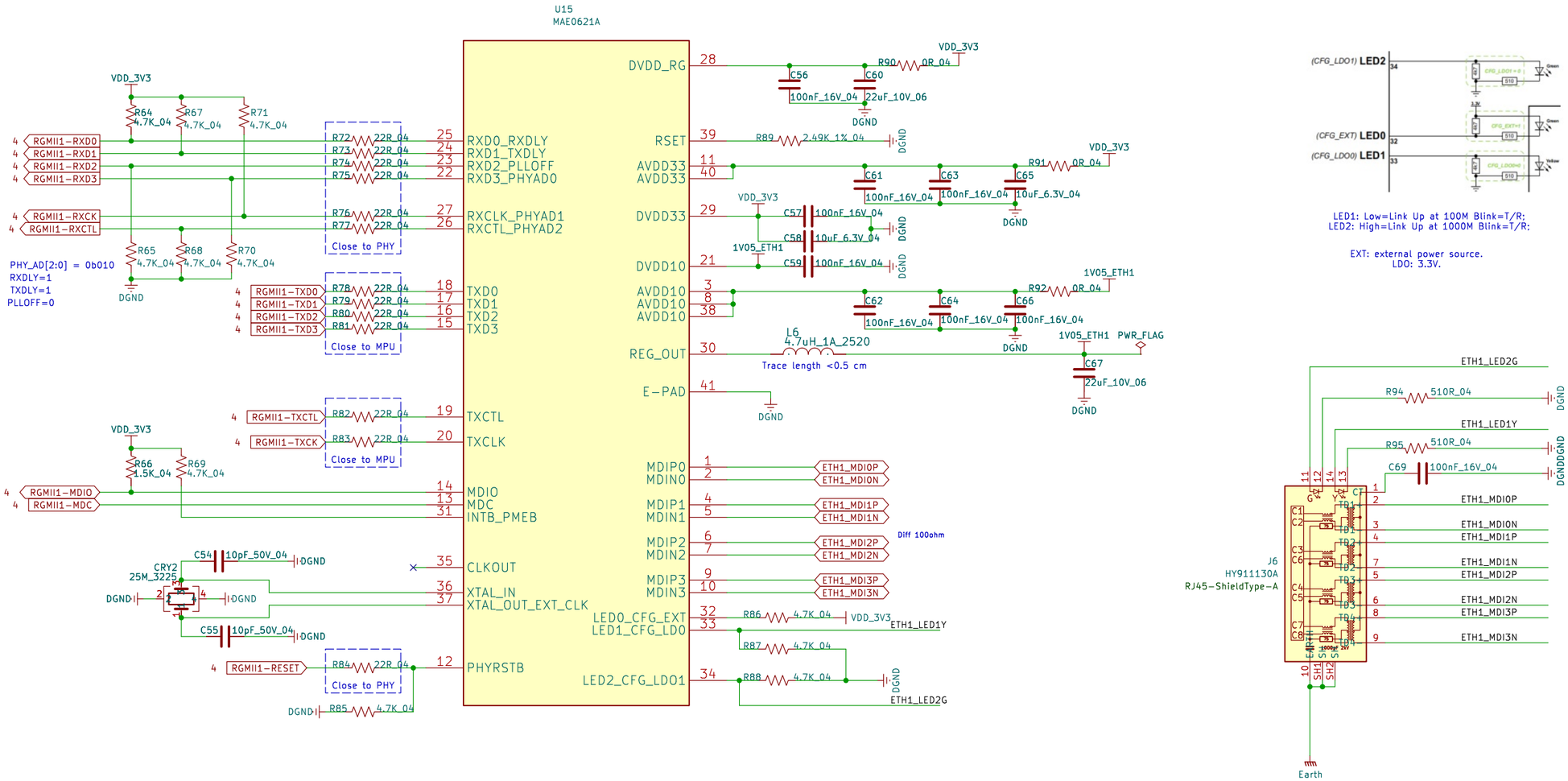
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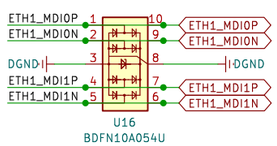
ETH1

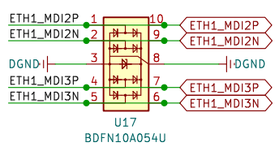


LED1: Low=Link Up at 100M Blink=T/R;
LED2: High=Link Up at 1000M Blink=T/R;

EXT: external power source.
LDO: 3.3V.

If the external 25MHz clock used, the XTAL_IN needs to be pulled down to GND by a resistor, The recommend resistor value is 0ohm.
The external 25MHz clock must be 3.3V domain, detail description see datasheet.

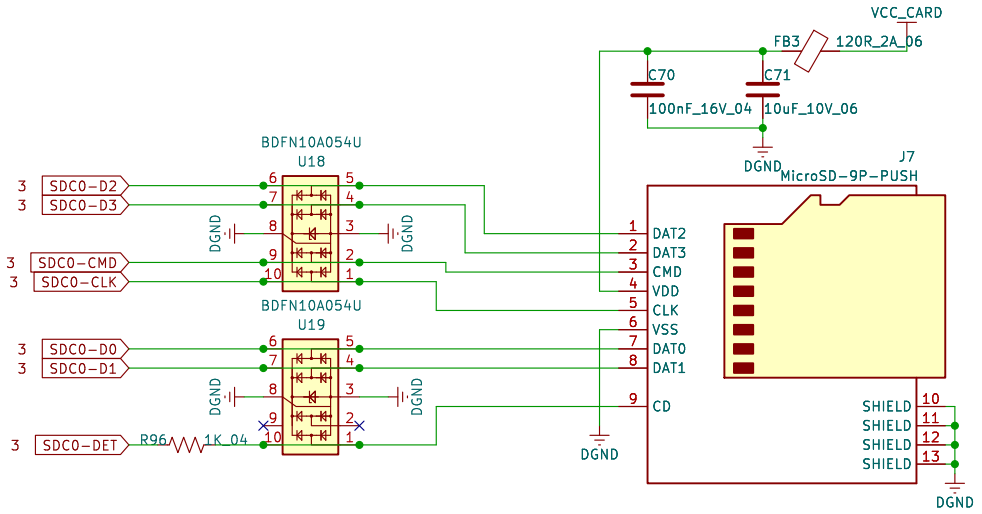




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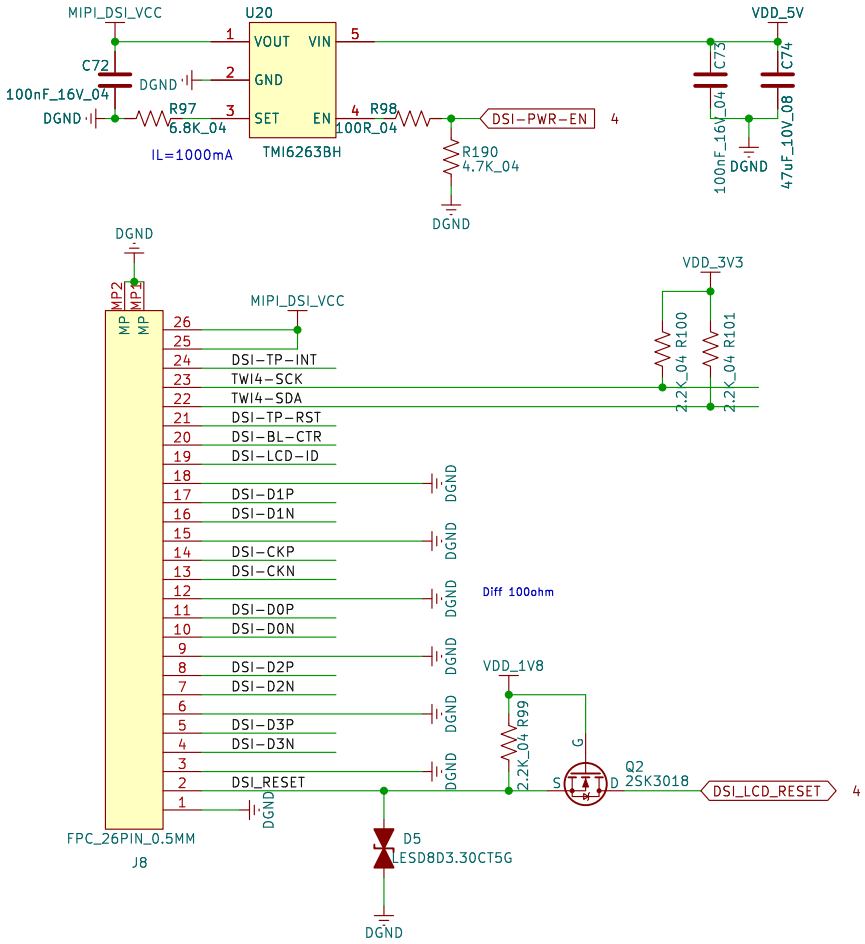
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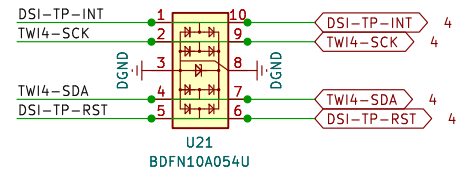


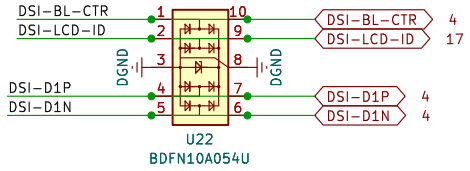
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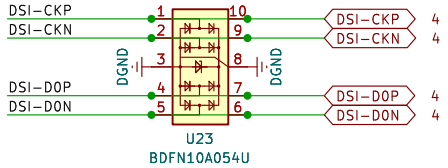
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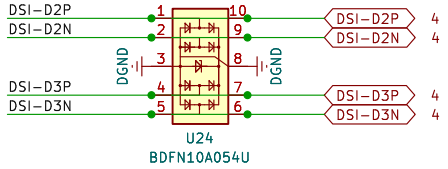
DSI













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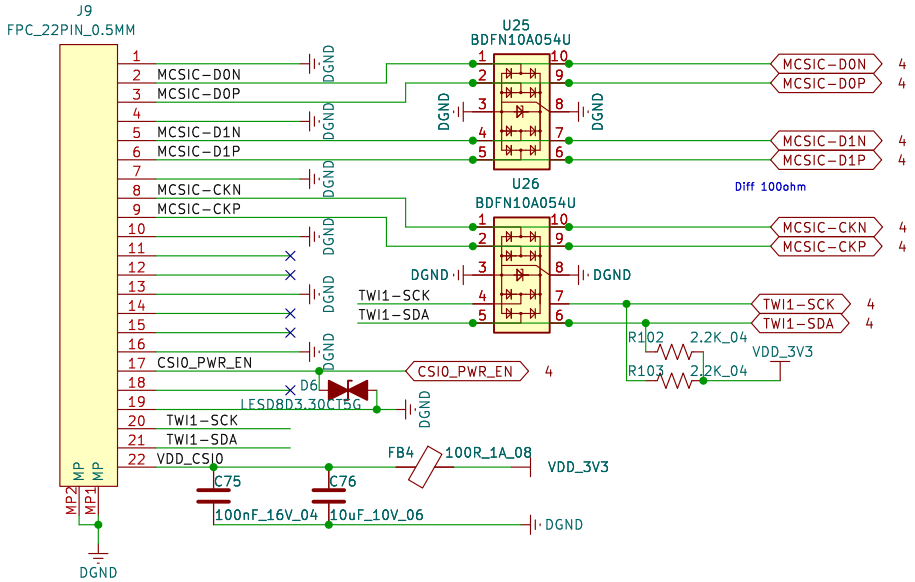
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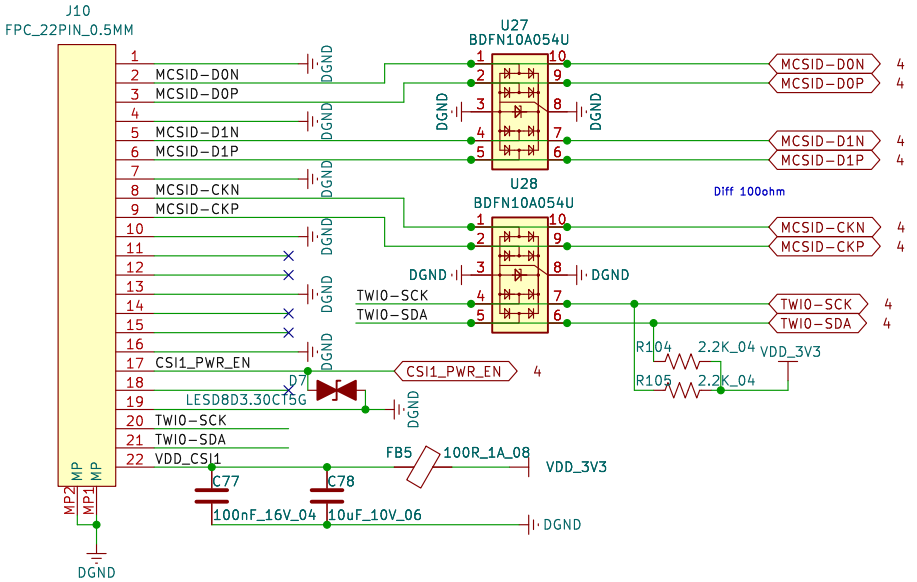
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CSI0



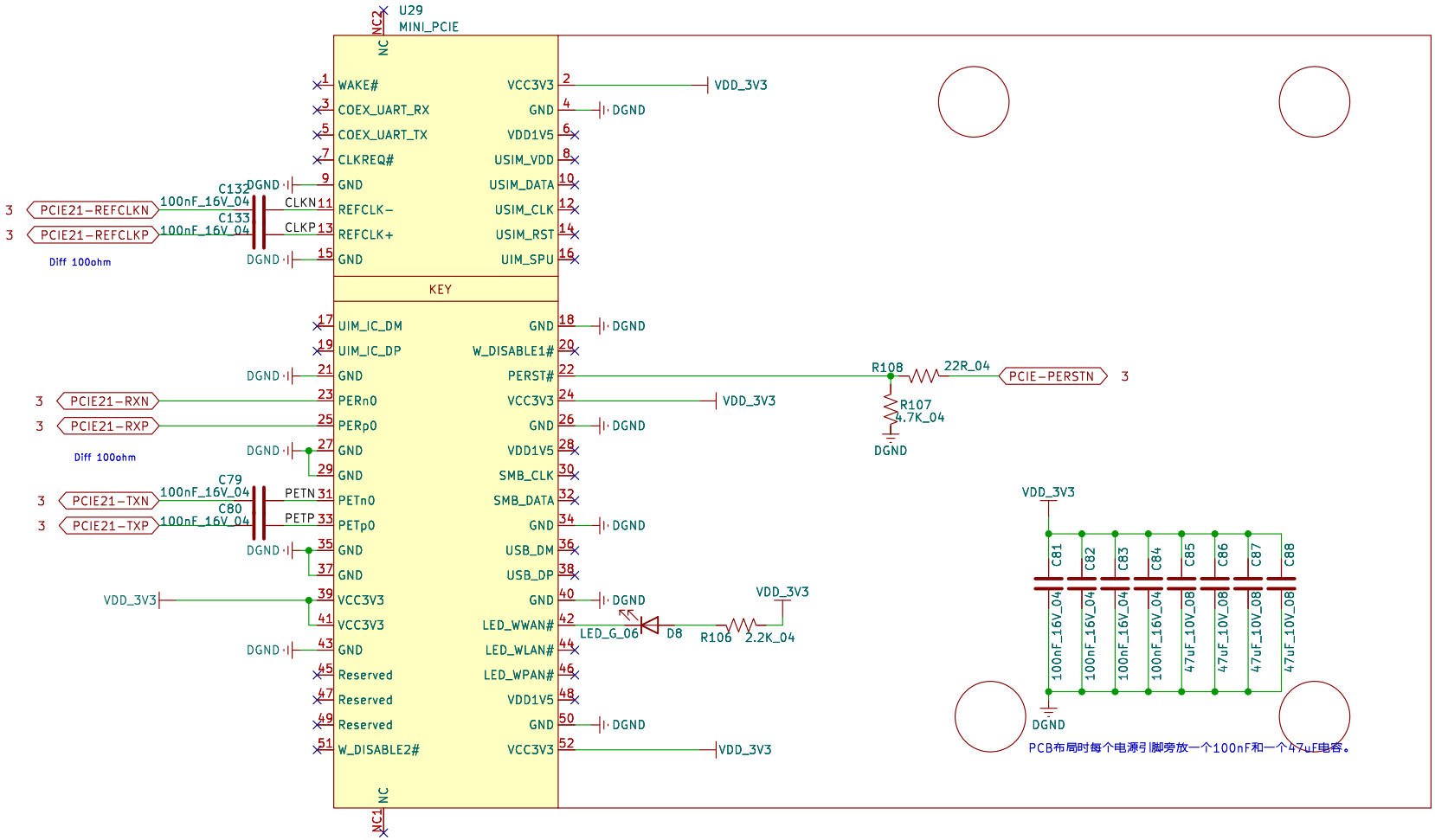
CSI1



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MINI PCIe

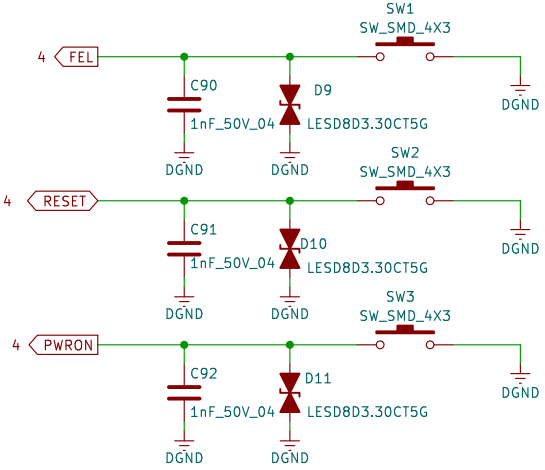


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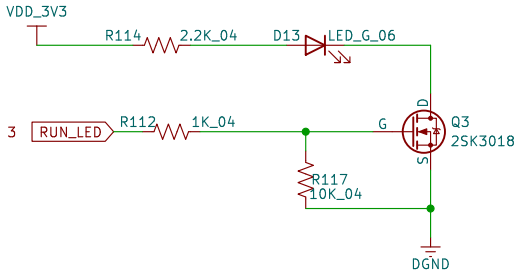
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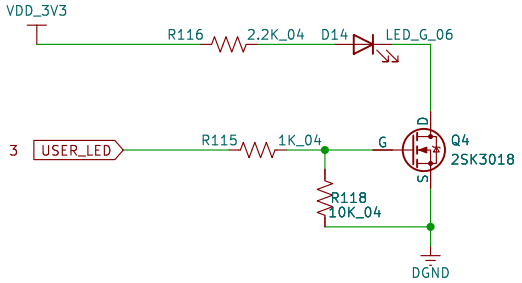
KEY



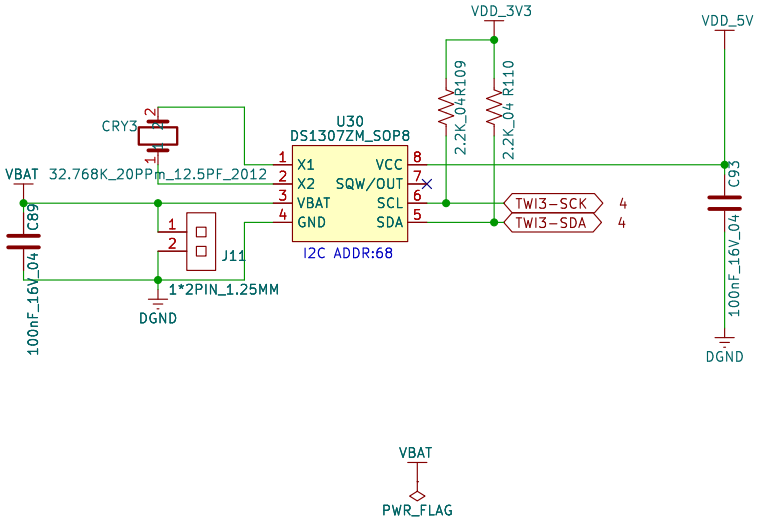
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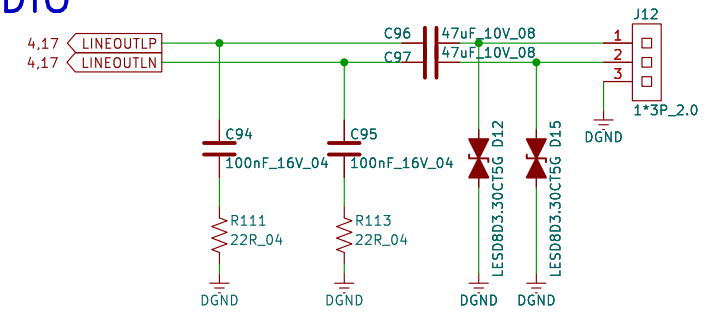
USER_LED



RTC



AUDIO



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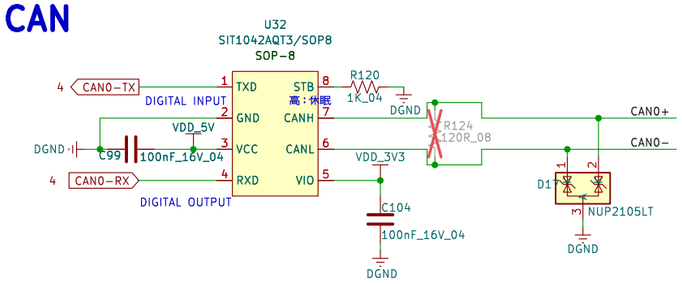
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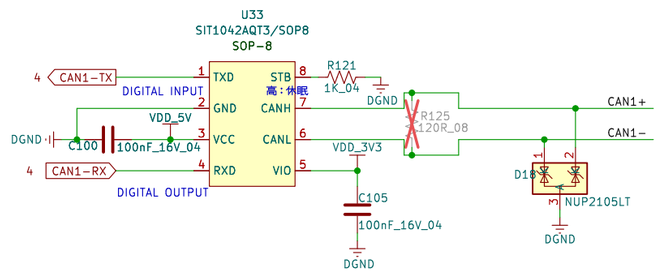
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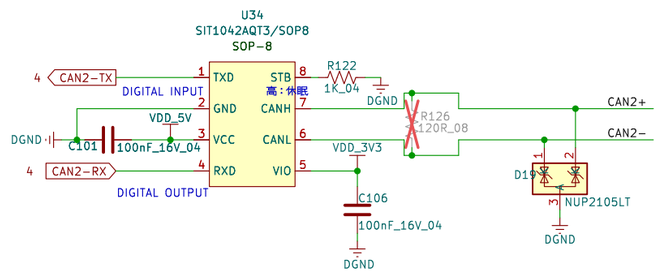
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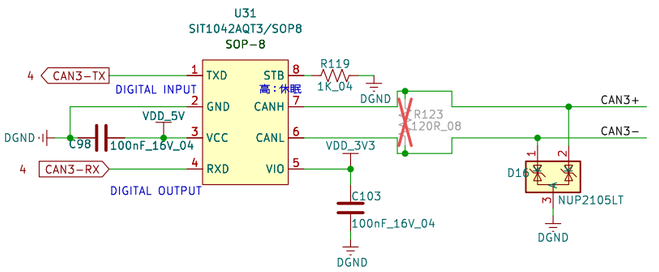
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CAN

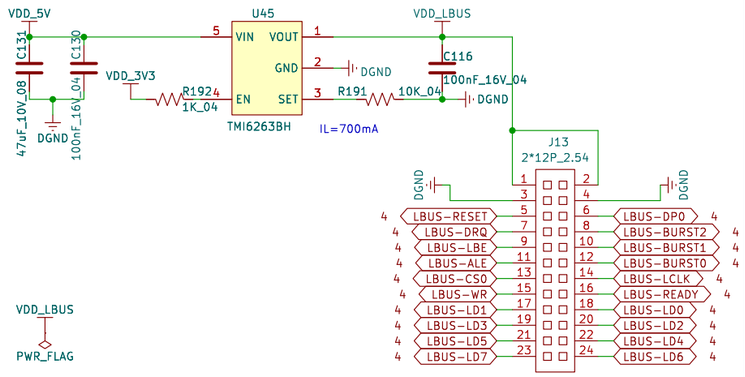




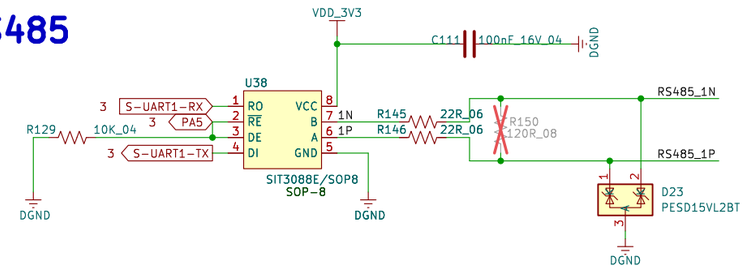


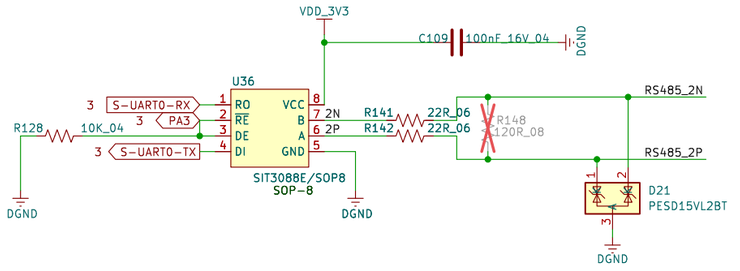


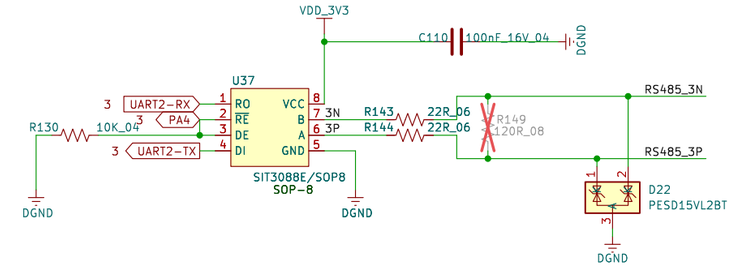
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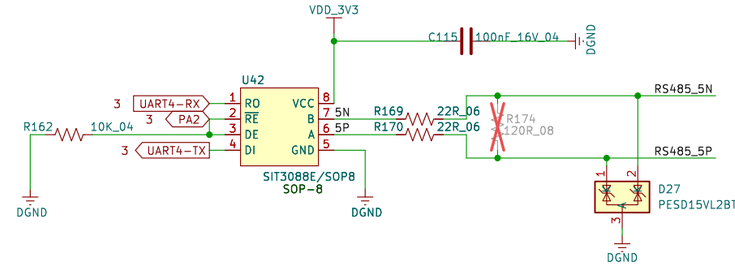


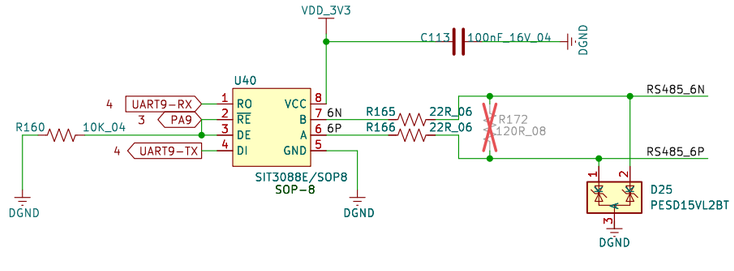
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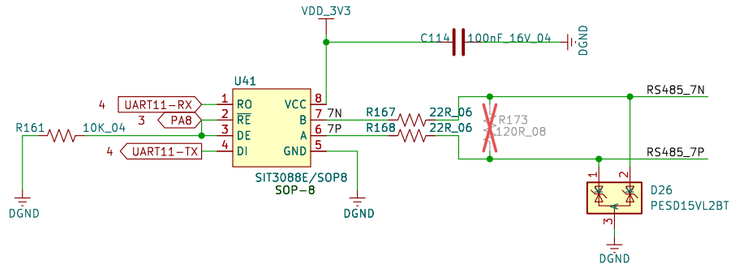


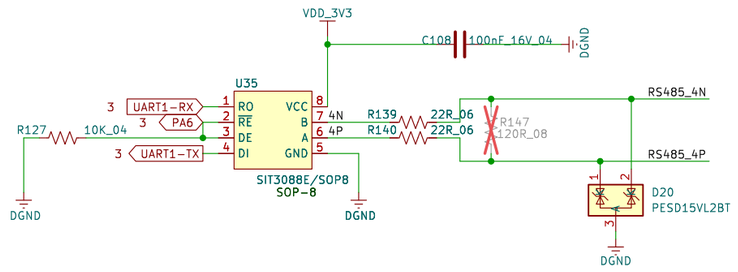


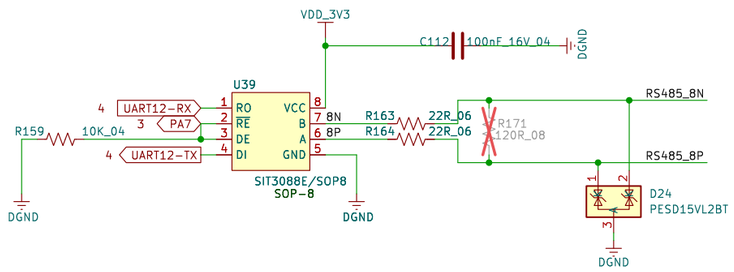


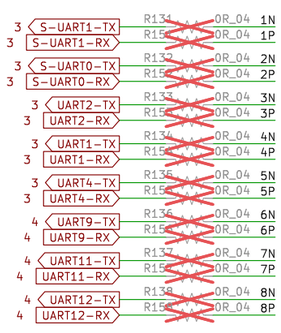


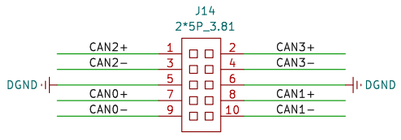


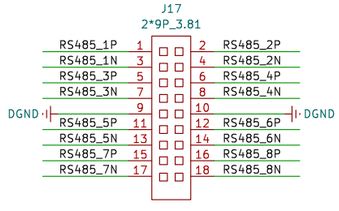












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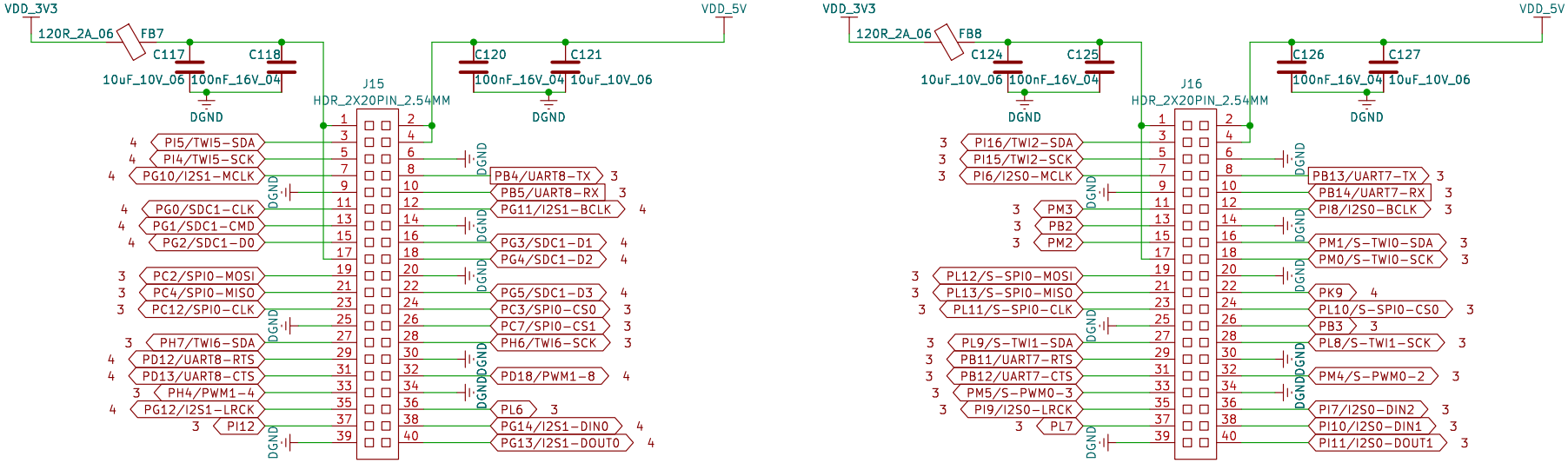
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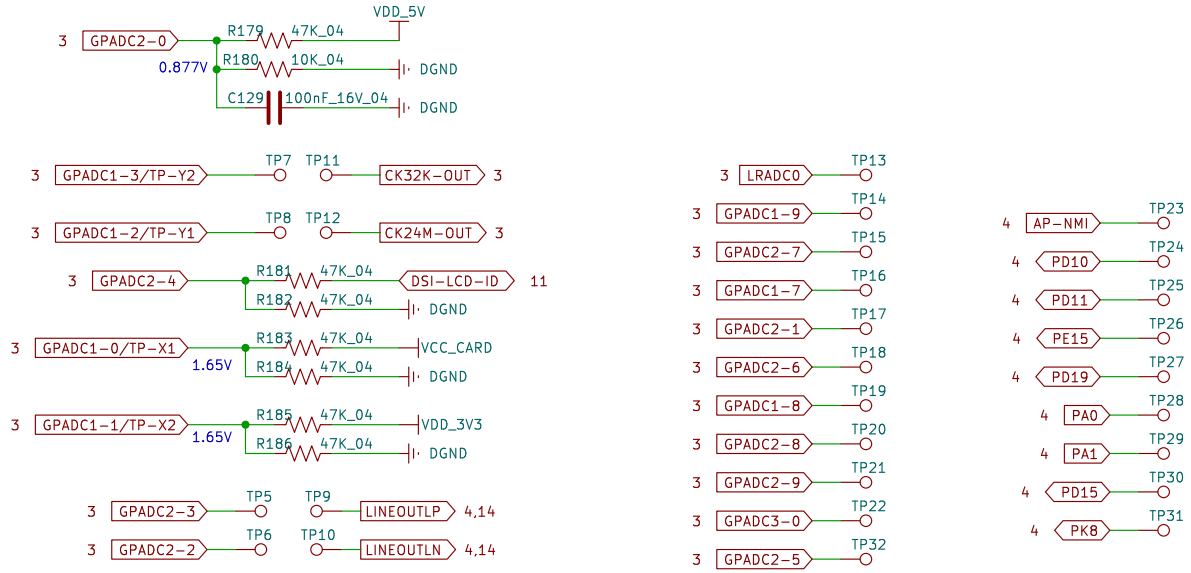
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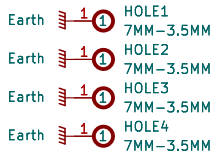
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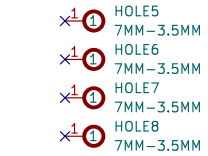
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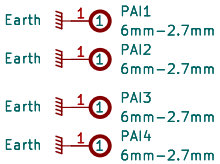
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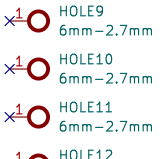
Board Mechanical Hole



Cooling Mechanical Hole



Raspberry Pi Hole



MINI PCIE Hole



Fiducial Marks

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